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Colour CCTV camera, 8mm lens, 12V d.c. 200mA 582x628 Resolution 380 lines Automatic aperture lens Mirror function PAL Back Light Compensation MLR, 100x40x40mm. Ref EE2 £75.90

Built-in Audio .15lux CCD camera 12V d.c. 200mA 480 lines s/n ratio >48db 1V P-P output 110mm x 60mm x 50mm. Ref EE1 $\mathfrak{L}108.90$





Metal CCTV camera housings for internal or external use. Made from aluminium and plastic they are suitable for mounting body cameras in. Available in two sizes 1 – 100 x 70 x 170mm and 2 – 100 x 70 x 280mm. Ref EE6 £24.20 EE7 £28.60 multi-position brackets. Ref EE8 £8.80



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Fully cased IR light source suitable for CCTV applications. The unit measures 10 x 10 x



Self-cocking pistol plcr002 crossbow with metal body. Self-cocking for precise string alignment Aluminium alloy construction High tec fibre glass limbs Automatic safety catch Supplied with three bolts Track style for greater accuracy. Adjustable rear sight 50lb drawweight 150ft sec velocity Break action 17 string 30m range £23.84 Ref PLCB002





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A high quality external colour CCTV camera with built-in infra-red LEDs measuring 60 x 60 x 60mm Easy connect leads colour Waterproof PAL 1/4in. CCD 542 x 588 pixels 420 lines 0.05 lux 3.6mm F2 78 deg lens 12V d.c. 400mA Built-in light level sensor. £108.90. Ref EE13

Colour pinhole CCTV camera module audio. Compact. 20x20x20mm, built-in audio and ea connect leads PAL CMOS sensor 6-9V d.c. Effective Pixels 628x582 Illumination 2 lux Definition >240 Signal/noise ratio >40db Power Signal/noise ratio >40db Power consumption 200mW £38.50. Ref EE21



A small colour CCTV camera measuring just 35 x 28 x 30mm. Supplied complete with bracket, microphone and easy connect leads. Built-in audio. Colour 380 line resolution PAL 0.2 lux +18db constitution. Effective studies 20 sensitivity. Effective pixels 628 x 582 Power source 6-12V d.c. Power consumption 200mW £39.60. Ref EE16





Small transmitter designed to audio and video signals on 2-4GHz. Unit measures 45 x 35 x 10mm. Ideal for rheasures 45 x 35 x 10nm. Ideal for assembly into covert CCTV systems Easy connect leads Audio and video input 12V d.c. Complete with aerial Selectable channel switch £33. Ref EE19



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Beltronics BEL55O Euro radar and Belitorities BELSSO Euro radar and GATSO detector Claimed Detection Range: GATSO up to 400m. Radar & Laser guns up to 3 miles. Detects GATSO speed cameras at least 200 metres away, plenty of time to adjust your speed £350.90. Ref BEL550

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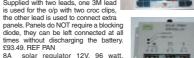
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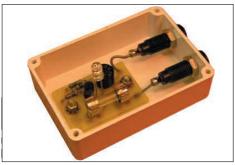
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NEXT MONTH

THUNDERSTORM MONITOR

Lightning poses a significant risk to us all, it causes more deaths than do most other natural hazards, including hurricanes and tornados. This "early warning system" monitors atmospheric charge and indicates when an unusually high potential difference between the atmosphere and earth occurs. Under test this inexpensive unit regularly picked up the possibility of a thunderstorm well in advance of any thunder or lightning. The monitor will greatly reduce the risk of injury or death by lightning strike.



LOGIC PROBE

When troubleshooting digital circuits, a logic probe provides a quick and easy way of determining the logic level at a particular node. Unlike voltmeters and oscilloscopes which are able to display a range of analogue voltage levels, the logic probe is essentially a digital instrument that provides a simple yet unambiguous "yes" or "no" indication of the node's logic level.

Unlike many commercially available logic probes, the one described next month is a precision instrument, in that it uses analogue techniques to sense the voltage levels, thereby providing an accurate indication of the true logic state. It is a pocket-sized instrument which is easy to construct and simple to use.

The probe is powered from the circuit under test. Any supply in the range 3V to 15V can be accommodated, thus allowing the probe to be used on a wide range of TTL and CMOS logic families. The logic level at any node in the circuit is detected simply by touching the probe tip to the appropriate point.

FLOATING POINT FOR PICS

An introduction to the use of Microchip's floating point maths routines for the PIC16F series of microprocessors. The article takes a quick look at the theory of fixed and floating point number representations, and then descriptions are given of several customised files and utility programs which can be downloaded from the EPE Downloads site, and which together comprise everything necessary to use the floating point code. The routines include floating point add, subtract, multiply and divide together with some auxiliary functions.

MW AMPLITUDE MODULATOR

A popular pastime amongst amateurs is renovating old valve or transistor radios. These radios are usually a.m. only, tuning the medium and long wave bands, with maybe a shortwave band. With the advent of high-quality stereo f.m. transmissions on v.h.f. and more recently DAB radio a great deal of music and other material is unavailable to these radios. The unit described here takes its input from the audio output of the f.m. or DAB receiver and regenerates it into an a.m. modulated format in the medium wave band. No modifications to the f.m. or DAB radio or the radio receiving the output from the unit are necessary. The unit will also take the output from an amplifier connected to CD, tape or record players, and again make this available to a.m. m.w. radios via a screened

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Will program virtually ALL 8 to 40 pin PICs plus certain ATMEL AVR, SCENIX SX and EEPROM 24C devices. Also supports In System Programming (ISP) for PIC

and ATMEL AVRs. Free software. Blank chip auto detect for super fast bulk programming. Requires a 40-pin wide ZIF socket (not included)

Assembled Order Code: AS3144 - £54.95

ATMEL 89xxx Programmer

Uses serial port and any standard terminal comms program. 4 LEDs display the status. ZIF sockets not included. Supply: 16VDC



Kit Order Code: 3123KT - £29.95 Assembled Order Code: AS3123 - £34.95

NEW! USB & Serial Port PIC Programmer



USB/Serial connection. Header cable for ICSP. Free Windows software. See website for PICs supported. ZIF Socket and USB Plug A-B

lead extra. 18VDC. Kit Order Code: 3149KT – £29.95 Assembled Order Code: AS3149 - £44.95

Introduction to PIC Programming

Go from a complete PIC beginner to burning your first PIC and writing your own code in no time! Includes a 49-page stepby-step Tutorial Manual,



Programming Hardware (with LED bench testing section), Win 3.11–XP Programming Software (will Program, Read, Verify & Erase), and a rewritable PIC16F84A that you can use with different code (4 detailed examples provided for you to learn from). Connects to PC parallel port. Kit Order Code: 3081KT – £14.95

Assembled Order Code: AS3081 - £24.95

ABC Maxi AVR Development Board

The ABC Maxi board has an open architecture design based on Atmel's AVR AT90S8535 RISC microcontroller and is



ideal for developing new designs. Features:

8Kb of In-System Programmable Flash (1000 write/erase cycles) ● 512 bytes internal SRAM ● 512 bytes EEPROM

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- bi-directional with internal pull-up resistors)

 Output buffers can sink 20mA current
- (direct I.e.d. drive) 4 x 12A open drain MOSFET outputs RS485 network connector • 2-16 LCD Connector
- 3.5mm Speaker Phone Jack

Supply: 9-12VDC.
The ABC Maxi STARTER PACK includes one assembled Maxi Board, parallel and serial cables, and Windows software CD-ROM featuring an Assembler, BASIC compiler and in-system programmer.

Order Code ABCMAXISP - £79.95 The ABC Maxi boards only can also be purchased separately at £59.95 each.

Controllers & Loggers

Here are just a few of the controller and data acquisition and control units we have See website for full details. Suitable PSU for all units: Order Code PSU445 - £8.95

Rolling Code 4-Channel UHF Remote

State-of-the-Art. High security. 4 channels. Momentary or latching relay output. Range up to 40m. Up to 15 TXs can be learned by one Rx (kit includes one Tx but more available separately). 4 indicator LEDs.

Rx: PCB 77x85mm, 12VDC/6mA (standby). Two & Ten Channel versions also available. Kit Order Code: 3180KIT - £41.95 Assembled Order Code: AS3180 - £49.95

Computer Temperature Data Logger



Serial port 4-channel temperature logger. °C or °F. Continuously logs up to 4 separate sensors located 200m+ from board. Wide range of free software applications for storing/using data. PCB just 38x38mm. Powered

by PC. Includes one DS1820 sensor and four header cables. Kit Order Code: 3145KT – £19.95

Assembled Order Code: AS3145 - £26.95 Additional DS1820 Sensors - £3.95 each

Most items are available in kit form (KT suffix) or pre-assembled and ready for use (AS prefix).

NEW! DTMF Telephone Relay Switcher

Call your phone number using a DTMF phone from anywhere in the world and remotely turn on/off any of the 4 relays as desired. User settable



Security Password, Anti-Tamper, Rings to Answer, Auto Hang-up and Lockout. Includes plastic case. 130 x 110 x 30mm. Power: 12VDC.

Kit Order Code: 3140KT - £39.95 Assembled Order Code: AS3140 - £49.95

Serial Port Isolated I/O Module



Computer controlled 8-channel relay board. 5A mains rated relay outputs and 4 opto-isolated digital inputs (for monitoring switch

states, etc). Useful in a variety of control and sensing applications. Programmed via serial port (use our new Windows interface, terminal emulator or batch files). Serial cable can be up to 35m long. Includes plastic case 130 x 100 x 30mm. Power: 12VDC/500mA

Kit Order Code: 3108KT - £54.95 Assembled Order Code: AS3108 - £64.95

Infra-red RC 12-Channel Relay Board



Control 12 on-board relays with included infra-red remote control unit. Toggle or momentary. 15m+ range. 112 x 122mm. Supply: 12VDC/0.5A.

Kit Order Code: 3142KT - £41.95 Assembled Order Code: AS3142 - £51.95

PC Data Acquisition & Control Unit

Monitor and log a mixture of analogue and digital inputs and control external devices via the analogue and digital outputs. Monitor pressure, tempera-



pressure, terriperature, light intensity, weight, switch state, movement, relays, etc. with the apropriate sensors (not supplied). Data can be processed, stored and the results used to control devices such as motors, sirens, relays, servo motors (up to 11) and two stepper motors.

Features

- 11 Analogue Inputs 0·5V, 10 bit (5mV/step)
- 16 Digital Inputs 20V max. Protection 1K in series, 5.1V Zener
- 1 Analogue Output 0-2⋅5V or 0-10V. 8 bit (20mV/step)
- 8 Digital Outputs Open collector, 500mA, 33V max
- Custom box (140 x 110 x 35mm) with printed front & rear panels
- Windows software utilities (3.1 to XP) and programming examples Supply: 12V DC (Order Code PSU203)

Kit Order Code: 3093KT - £69.95 Assembled Order Code: AS3093 - £99.95

Hot New Kits This Summer!

Here are a few of the most recent kits added to our range. See website or join our email Newsletter for all the latest news.

NEW! EPE Ultrasonic Wind Speed Meter



Solid-state design wind speed meter (anemometer) that uses ultrasonic techniques and has no moving parts and does not need

calibrating. It is intended for sports-type activities, such as track events, sailing, hang-gliding, kites and model aircraft flying, to name but a few. It can even be used to monitor conditions in your garden. The probe is pointed in the direction from which the wind is blowing and the speed is displayed on an LCD display.

Specifications

- Units of display: metres per second, feet per second, kilometres per hour and miles per hour
- Resolution: Nearest tenth of a metre
- Range: Zero to 50mph approx.

Based on the project published in Everyday Practical Electronics, Jan 2003. We have made a few minor design changes (see web site for full details). Power: 9VDC (PP3 battery or Order Code PSU345). Main PCB: 50 x 83mm.

Kit Order Code: 3168KT – **£34.95**

NEW! Audio DTMF Decoder and Display



Detects DTMF tones via an on-board electret microphone or direct from the phone lines through the onboard audio transformer. The

numbers are displayed on a 16-character, single line display as they are received. Up to 32 numbers can be displayed by scrolling the display left and right. There is also a serial output for sending the detected tones to a PC via the serial port. The unit will not detect numbers dialled using pulse dialling. Circuit is microcontroller based. Supply: 9-12V DC (Order Code PSU345). Main PCB: 55 x 95mm.

Kit Order Code: 3153KT – £17.95 Assembled Order Code: AS3153 – £29.95

NEW! EPE PIC Controlled LED Flasher



This versatile
PIC-based LED
or filament bulb
flasher can be
used to flash
from 1 to 160

LEDs. The user arranges the LEDs in any pattern they wish. The kit comes with 8 superbright red LEDs and 8 green LEDs. Based on the Versatile PIC Flasher by Steve Challinor, *EPE* Magazine Dec '02. See website for full details. Board Supply: 9-12V DC. LED supply: 9-45V DC (depending on number of LED used). PCB: 43 x 54mm. Kit Order Code: 3169KT – £10.95

Most items are available in kit form (KT suffix) or assembled and ready for use (AS prefix)

FM Bugs & Transmitters

Our extensive range goes from discreet surveillance bugs to powerful FM broadcast transmitters. Here are a few examples. All can be received on a standard FM radio and have adjustable transmitting frequency.

MMTX' Micro-Miniature 9V FM Room Bug



Our best selling bug! Good performance. Just 25 x 15mm. Sold to detective agencies worldwide. Small enough to hide just about anywhere. Operates at the 'less busy' top

end of the commercial FM waveband and also up into the more private Air band.
Range: 500m. Supply: PP3 battery.
Kit Order Code: 3051KT – £8.95
Assembled Order Code: AS3051 – £14.95

HPTX' High Power FM Room Bug

Our most powerful room bug. Very Impressive



performance. Clear and stable output signal thanks to the extra circuitry employed. Range: 1000m @ 9V. Supply: 6-12V DC (9V PP3 battery clip suppied). 70 x 15mm. Kit Order Code: 3032KT – £9.95 Assembled Order Code: AS3032 – £17.95

MTTX' Miniature Telephone Transmitter



Attach anywhere along phone line.
Tune a radio into the signal and hear

exactly what both parties are saying. Transmits only when phone is used. Clear, stable signal. Powered from phone line so completely maintenance free once installed. Requires no aerial wire – uses phone line as antenna. Suitable for any phone system worldwide. Range: 300m. 20 x 45mm. Kit Order Code: 3016KT – £7.95
Assembled Order Code: AS3016 – £13.95

3 Watt FM Transmitter



Small, powerful FM transmitter. Audio preamp stage and three RF stages deliver 3 watts of RF power. Can be used with the electret

microphone supplied or any line level audio source (e.g. CD or tape OUT, mixer, sound card, etc). Aerial can be an open dipole or Ground Plane. Ideal project for the novice wishing to get started in the fascinating world of FM broadcasting. 45 x 145mm. Kit Order Code: 1028KT – £22.95 Assembled Order Code: AS1028 – £34.95

25 Watt FM Transmitter

Four transistor based stages with a Philips BLY89 (or equivalent) in the final stage. Delivers a mighty 25 Watts of RF power. Accepts any line level audio source (input sensitivity is adjustable). Antenna can be an open dipole, ground plane, 5/8, J, or YAGI configuration. Supply 12-14V DC, 5A. Supplied fully assembled and aligned – just connect the aerial, power and audio input. 70 x 220mm.

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This book introduces the PIC16F84 and PIC16C711, and is the easy way to get started for anyone who is new to PIC programming. We begin with four simple experiments, the first of which is explained over ten and half a pages assuming no starting knowledge except the ability to operate a PC. Then having gained some practical experience we study the basic principles of PIC programming, learn about the 8 bit timer, how to drive the liquid crystal display, create a real time clock, experiment with the watchdog timer, sleep mode, beeps and music, including a rendition of Beethoven's Für Elise. Finally there are two projects to work through, using the PIC16F84 to create a sinewave generator and investigating the power taken by domestic appliances. In the space of 24 experiments, two projects and 56 exercises the book works through from absolute beginner to experienced engineer level.

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Book + made up kit 1a + software...... £73.50 Book + unmade kit 1u + software...... £66.50 (PP UK £4, Europe £10, Rest of world £14)

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Experimenting with C & C++ Programmes teaches us to programme by using C to drive the simple hardware circuits built using the materials supplied in the kit. The circuits build up to a storage oscilloscope using relatively simple C techniques to construct a programme that is by no means simple. When approached in this way C is only marginally more difficult than BASIC and infinitely more powerful. C programmers are always in demand. Ideal for absolute beginners and experienced programmers.

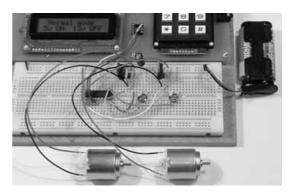
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Experimenting with the PIC16F877

The second PIC book starts with the simplest of experiments to give us a basic understanding of the PIC16F877 family. Then we look at the 16 bit timer, efficient storage and display of text messages, simple frequency counter, use a keypad for numbers, letters and security codes, and examine the 10 bit A/D converter.

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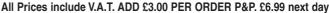
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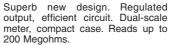
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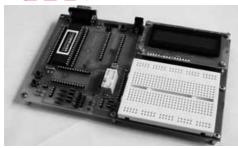
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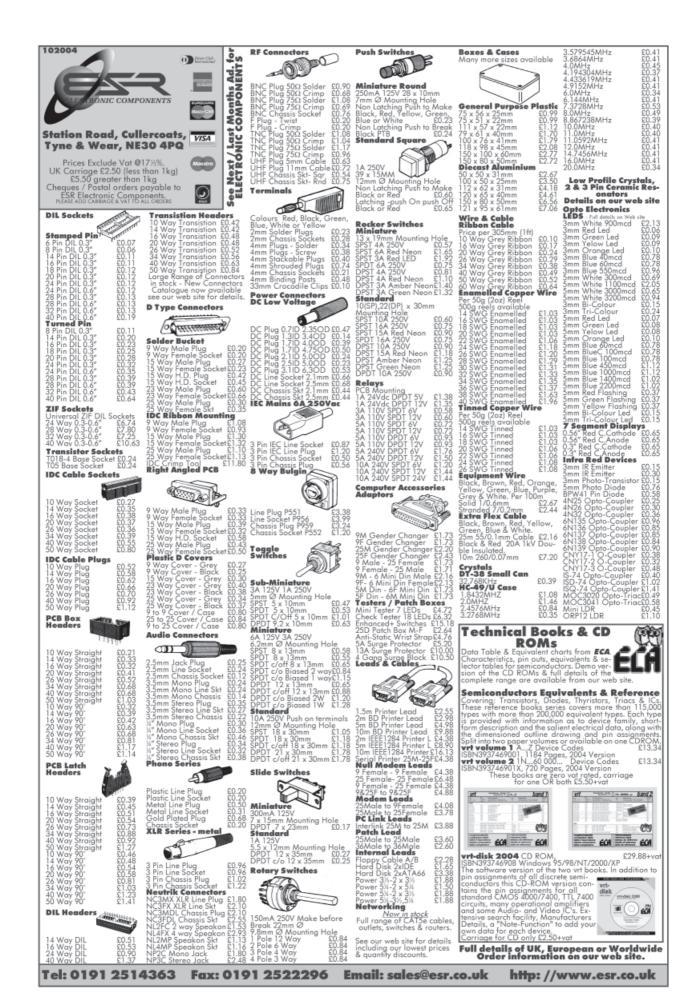
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Robots

Robot - automaton with human appearance; automatic mechanical device; machine-like person. Robot - a mechanical man; a more than humanly efficient automaton; an automatic traffic signal. [Czech robota, statute labour; from Karel Capek's play R.U.R. (1920).]

Robot – a mechanical device that can be programmed to perform a variety of tasks of manipulation and locomotion under automatic control.

Three different interpretations of the word robot. The first from The New Little Oxford Dictionary, the second from Chambers Twentieth Century Dictionary and the third from McGraw-Hill Dictionary of Scientific and Technical Terms. The first two give what could perhaps be described as a popular, non-technical interpretation of the word. They are, however, not what I and, I guess, most other "technical" people think of as a robot. My own view is that a robot is essentially a programmable device as described in the third definition. This means that the robots in "Robot Wars" type encounters are not really robots at all but remote controlled fighting machines

Of course, the meanings or interpretations of words change over time and no doubt many youngsters will think of Robot Wars devices when talking about robots, whilst older, non-technical, people may think of "a mechanical man". It is obvious that the word is used in different ways and covers a multitude of devices from humanoid automatons (androids), with or without intelligence, to buggies like our Smart Karts, fighting radio controlled machines, robot arms and various walking platforms etc. even to "an automatic traffic signal" - not sure where that one came from.

Our Robots - Special Supplement in this issue demonstrates the wide variety of devices now available for what we know as robotics applications.

Smart Kart

Although our own Smart Kart presently lacks any manipulative ability it can be programmed, has "locomotion under automatic control" and will be extended over the coming months to give it better vision and limited manipulation abilities. The designer, Owen Bishop, is planning a range of developments which should see Smart Kart playing interactive games and learning about its environment. The PIC brain used in the design comes from Microchip and constructors might be interested in the Free Online Competition in this issue where Microchip have provided over \$2100 of PIC development tools as prizes.

Mike don

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Moon and Tide Clock Calendar

John Becker

A pictorial guide to tracking moon and tide states throughout the year.

HIS article describes a novel design in which the phases of the moon and the ebb and flow of the tide are shown on a graphics liquid crystal display (GLCD), along with clock and calendar data. It also illustrates how a PS/2 PC keyboard can be interfaced to a PIC-controlled circuit which only infrequently needs to have its settings adjusted, thus saving on the cost of the pushbutton switches which would otherwise be required.

A detailed discussion of interfacing a PIC to a PS/2 keyboard, and also to a PS/2 mouse, was published in *EPE* August '04 (*PIC to PS/2 Mouse and Keyboard Interfacing*).

In the application described now, the keyboard is basically only required to set the real-time factors for the Moon, tide, clock and calendar, after which it can be returned to the PC to which it belongs. It may be re-attached to the PIC circuit at a later date if required, to adjust clock timing accuracy, for example. The various factors

680

to be set are selected by pressing given keyboard keys, such as "M" for minutes or "Y" for years etc, and then correcting the selected values up or down by the use of the "+" or "-" keys.

Current clock time is shown in 24-hour mode, calendar information is displayed as weekday, day of the month, month and year. It automatically corrects itself for different month lengths, and for leap years. It is not Millennium compatible as such things do not actually bother us now (and the Millennium Bug never significantly intruded on our lives anyway)!

Well Phased

Moon status is displayed graphically, mimicking what you actually see in real life. Most Moon displays normally show only four or eight phases per month – this design provides a more detailed simulation, having 256 separate progressions from full Moon to full Moon. With this degree of resolution, you are unlikely to ever see the change from one phase to the next, unless you actually wait to see it, and that could mean a really long wait (ground three hours)!

long wait (around three hours)!
Tide display is in a form of bargraph. When the tide is rising, a black triangle slowly enlarges from nil near the bottom of the screen, expanding until its peak reaches the top of the screen. As the tide then starts to fall, the peak of the triangle is slowly flattened, the flat-



ness progressively descending the screen, until the tide has fully receded, prior to rising again. Even from a distance you can see the current state of the tide and whether it is rising or falling.

Just for fun, there is also an optional little gimmick when high tide occurs – revealed later! Simultaneously, the current time is also shown below the tide triangle, to the right, where it stays until changed at the next high tide point.

Moon Calculations

We are (or should be!) aware that the Moon orbits the Earth in an easterly direction, in a period which we know as one month. As a rule of thumb, we probably regard this period as taking 28 days. In fact it is nearer to 29.53 days, although it varies over the year, depending upon complex gravitational factors, principally caused by the Sun and our varying distance from it.

In the author's *Canute Tide Predictor* of June 2000, such factors were taken into account for its assessment of the tides, which are, of course, caused mainly by the Moon, as further discussed shortly. This Moon-Tide clock's calculations are done more simply, and are based on a continuous unchanging Moon cycle, taken as 29-53 days, or 42524-05 minutes, or 2551443-0 seconds per lunar month.

As said, the graphics display is capable of showing 256 Moon states. A single Moon state (segment) thus lasts for 2551443/256 = 9966.5738 seconds (2 hours, 46 minutes, 6.57 seconds). In the PIC program, a counter is set with the whole number of seconds (9967) for one segment. The counter is decremented (a value of 1 deducted) every second. Each time the counter rolls over to zero, it is reset to 9967, and another counter is incremented (a value of 1 added). Simultaneously, the screen's Moon display is updated to show the new phase status.

Each of the 256 Moon segments has a different curvature, to simulate the observed changing shape of the Moon. The mathematics behind this simulation are complex, and based upon the sines of an angle of rotation, of which there are 360 degrees in a full cycle. Whilst it is possible (just about!) to program a PIC to calculate sines, it was decided that a simpler technique should be employed, using a lookup table.

Everyday Practical Electronics, October 2004

QuickBASIC was used to do the actual calculations, outputting to a disk file the individual integer (whole number) results for a simple curve of given diameter in relation to 180 degrees. These results were prefixed by the PIC command **RETLW**, so that the PIC software simply uses a looped counter having 180 steps (angle positions) and then accesses a table which returns the value associated with that angle.

In fact, two tables are used, because what is needed are the coordinates (*x* and *y*) at which the screen should have a single pixel set (black) or reset (normal clear background).

Lunamatics!

Hopefully, some of you will be interested in the maths behind this, especially if you want to draw a circle or ellipse on a GLCD screen. In Fig.1 is shown a circle and a right-angled triangle drawn from the circle's centre to the perimeter. The angle used for the calculation is that shown marked as A. The line marked H is the hypotenuse of the triangle and, irrespective of the angle, its length is always the same as the radius of the circle.

The vertical line descending from the point at which line *H* meets the circumference is *opposite* the angle and marked *Y*. The horizontal line from the bottom of *Y* to the centre is marked *X*, and is the angle's *adjacent* side.

Standard geometry tells us that the sine of an angle has a value equal to the length of the opposite side (Y) divided by the length of the hypotenuse (H), i.e.:

sine A = Y / H

We know the length of *H*, the radius of the circle, but we need to know *Y*. The above equation can be rearranged so that:

 $Y = sine A \times H$

This value represents coordinate *Y* at which we wish to set or reset a pixel. But we still need to know the horizontal position for this pixel, coordinate *X*. This can be calculated using tangents, but can equally well be calculated using the formula:

hypotenuse² = opposite² + adjacent². In Fig.1 the adjacent side is X, the value we need to know, so the above equation can be rewritten as:

 $X = \sqrt{(H^2 - Y^2)}$

We now have the *X* coordinate (the GLCD's horizontal line of pixels) and the *Y* coordinate (the GLCD's vertical line of pixels). In the program, the *X* and *Y* coordinates for angles 0° to 179° are held in two separate tables. The software's angle counter simply accesses the tables in turn and returns with the two screen coordinates of the pixel to be accessed.

For angles between 180° and 359° the software uses the same values as those between 0° and 179° , the resulting X coordinate being taken as a negative value.

It is worth commenting that the entire suite of software routines for using a GLCD was discussed in the author's feature article *Using PICs with Graphic L.C.D.s* (Feb '01). Those routines have been imported to the Moon-Tide program as a complete library file.

Flattening the Curve

When we view the Moon's face, the boundary (terminator) between light and dark is seen as a curve whose radius changes as the Moon orbits the earth. Simulating this is actually somewhat easier than might be supposed.

In the software, a multiplying factor changes for each of the 256 phase positions. For the full external curve of the Moon, the resulting answer is that for the normal radius of the Moon (48 pixels wide). At each subsequent phase, the resulting value decreases horizontally (the *X* coordinate gets smaller, while leaving the *Y* coordinate intact).

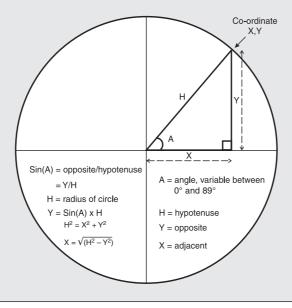


Fig.1. Geometric principle used in the PIC's software for calculating Moon curves.

The multiplying value is also relative to which "quarter" of the Moon is being processed: no Moon seen, first quarter (right side illuminated), full Moon, last quarter (right side dark), no Moon, etc.

Note that even though the Moon's screen diameter is only 48 pixels, it is still necessary to use a loop that plots the curve positions for 180 degrees. This is because some sections of the curve (those nearer the top and bottom) need more than one pixel set/reset on a given line.

Incidentally, in an early version of the software, the Moon phases were originally set at eight variants, as represented by Jonathan Hare's interestingly simple *PIC Moon Clock* of April '04. Putting that type of image coding into a PIC turned out to be complex, involving images first created through Windows' Paint and digitised into lookup tables. Such techniques were discussed in the author's *PIC World Clock* of Aug '02.

Having successfully done so, though, there was a nagging feeling that a smoother transition between phases could be achieved with a bit of thought – which turned out to be *a lot* of thought when temptation was succumbed to!

The software files include the QuickBASIC file (Circltst.bas) which was used to obtain the coordinates for the GLCD circle plot.

Tideometry!

The relationship between the Moon and the tides is, at one level, very straightforward – the Moon orbits the Earth while the Earth rotates beneath the Moon. It should be capable of software simulation using simple equations, the Earth's oceans always being pulled towards the Moon's gravitational field, and swept around the globe as a wave front. (In fact, it's two tidal wave fronts that actually exist, one on either side of globe.)

Several factors prevent this simplistic modelling portraying the true situation – principally the influence of the Sun's gravity, and the land masses above and below

the seas and oceans. This was explained more fully in the *Canute* article, and the software of that design reflected this greater complexity.

As with the Moon phase values, for this simple Moon-Tide clock, a fixed *average* period between tides has been used, without reference to the Sun or Moon. Using the same basic figures as with *Canute*, the average period between one high tide and the next has been taken as 745-2361 minutes, i.e. 12 hours 25 minutes 14 seconds, or 44714-166 seconds.

On the GLCD screen, 47 horizontal lines have been allocated to display the tide condition, rising and falling. The full cycle from one high tide point to the next thus takes $47 \times 2 = 94$ positions. Each position (segment) thus represents $44714 \cdot 166 / 94 = 475 \cdot 683$ seconds (just under eight minutes). To simplify the calculations, this has been taken as the integer value of 476, representing a theoretical accuracy of 0.0666%

(bare in mind that in nature the tides vary by far greater amounts than this).

However, the program also takes into account the timing between the high tide points as well as individual segments, resetting the tide segment counter in synchronisation with the high tide period counter, so effectively doubling the theoretical accuracy to about 0.032%.

At fully low tide, the tide display area of the screen is blank. At the first tide segment increment, a single pixel is activated at the middle of the allocated tide area on the lowest line, the apex of the forthcoming triangle. At the next step, the first pixel moves up one line, and the second line displays three pixels, followed by five pixels at the next step. The triangle continues to expand upwards by one pixel, and outwards by two pixels. The maximum base width of the triangle is 64 pixels. Once this has been reached, the upper triangle continues to rise to its maximum of 47 pixels, but the base width remains at 64, so a rectangle begins to form in the lower

At the moment that the tide triangle peaks at the top of the screen (high tide), the current time in hours and minutes is displayed at the bottom right of the triangle, and then remains unchanged until the next high tide point, approximately 12 and a half hours later.



Example display for a rising tide, with a fairly new-ish Moon.

Once the triangle has peaked at the top, its apex becomes progressively flattened as the tide falls, a condition which continues right down to the lowest display line, which then disappears until the tide starts to rise again, as described.

It is stressed that the tide displays on the screen should never be used for any situation which might endanger life. If you really need to know the predicted tide times for a particular location, consult the Coast Guard (many seaside resorts also have tide charts on view at seafront information centres).

The tide displays on this clock are purely for general interest, indicating, for instance, whether a morning or afternoon trip to the coast might suit you better.

Keyboard Control

The Moon-Tide clock has been designed solely for external adjustment via a PS/2 keyboard. No switches have been provided, nor has the software been written to make use of them. It is emphasised that the keyboard *must* be a PS/2 type. This is the type used with most modern PCs. They can also be bought inexpensively from a PC retailer.

The principle of interfacing a PIC to PS/2 keyboard and PS/2 mouse was described in the author's article referred to earlier. A practical example of interfacing a PS/2 mouse to a PIC was described in his AlphaMouse Game of September '04. The Moon-Tide clock is the follow-up design to show a practical circuit controlled by a PS/2 keyboard.

In essence, a keyboard can be interfaced to any two PIC input/output pins. The pins are buffered by two resistors of about $1k\Omega$ and biassed normally high via two other resistors, of about $10k\Omega$. The keyboard has two bidirectional lines, DATA and CLK (clock), which are connected to the allocated PIC pins via the buffering resistors. Power to the keyboard is supplied by the PIC unit's 5V power supply.

Data is exchanged serially between the keyboard and PIC, and the keyboard's CLK line synchronises its transfer. The PIC itself does not generate any clock signals, it simply reads the status of the CLK line and responds accordingly, inputting or outputting serial data from/to the DATA line. In many applications, including this Moon-Tide clock, the PIC is simply required to input keyboard data.

Keyboard data is coded in a complex manner and requires a lookup table to allocate those codes to their respective keyboard notations and ASCII equivalent values. To the untrained observer. the relationship might seem bizarre! It is, though, the method that has been widely adopted, and is known as the Scan Table 2 code.

In the Moon-Tide clock the various correction functions have been allocated specific keyboard keys. On receipt of the codes for those keys, the lookup table routes the

program to the required routine. The function modes are listed and discussed later.

While running, the program reads the status of the two keyboard connection lines. These are normally both held high by the biassing resistors (allowing the keyboard to be disconnected when finished with). If either line goes low, the software assumes the keyboard wishes to send data and immediately enters the keyboard data receive routine. On receipt of the keyboard scan code data, the lookup table is called and the appropriate correction routine is entered.

Keyboard data is continuously looked for and processed accordingly. Any time the ENTER key is pressed, the program stores the new correction values to the PIC's non-volatile EEPROM (electrically erasable read-only memory). Even while processing keyboard data, the PIC's clock and calendar (etc) routines continue to be updated as usual.

Clock Counting

The PIC's TMR0 timer is set to rollover 50 times a second. Effectively (see later), at each fiftieth rollover a seconds counter is incremented, and if it has reached 60 (one minute), it is reset to zero. A ripple effect now ensues, in which the values for the minutes, hours, days, weekday, month and year are incremented as appropriate to the value of the preceding counter.

The months counter when incremented uses a table to set the number of days for that month for the sake of the days counter's maximum limit. While doing so,

the years value is checked to see if a leap year is current and, if the month is February, sets the days limit to either 28 or 29 as necessary.

When the seconds counter is incremented, the tide and Moon counters are decremented, and when either has reached zero, it is reset to its starting value and the corresponding tide or Moon phase display is updated.

While the main program is waiting for the TMR0 timer to

rollover, it constantly reads the status of the keyboard lines and immediately responds to any change, so that there is an immediate response to any keyboard press.

The action of plugging in or disconnecting the keyboard does not interfere with the program timing routines.

Further keyboard matters will be discussed after the Moon-Tide circuit diagram and its printed circuit board assembly have been described.

Circuit Description

The complete circuit diagram for the Moon-Tide Clock Calendar is shown in Fig.2. It is worth noting that it is almost identical to that for the author's *PIC World Clock* referred to earlier. The only differences are that four extra resistors (R2 to R5) have been added to suit the keyboard input, and that the four previous switches have been dropped. Indeed, the author's prototype Moon-Tide clock was actually proved on the *World Clock's* printed circuit board, hard-wiring the extra resistors to it, and ignoring the switches.

A PIC16F877 microcontroller (IC1) is the active heart of the circuit. It is run at 3·2768MHz, as set by crystal X1 in conjunction with capacitors C1 and C2. Keyboard input is via socket SK2, with the DATA line connected to PIC pin RA0 via buffering resistor R4, and the CLK line connected via R5 to pin RA1. Resistors R2 and R3, provide the required pull-up bias.

The GLCD display module (X2) is controlled by PIC ports C and D. The pin use order is identical not only to the *World Clock*, but also to the original demo circuit employed when the use of GLCDs was described in the author's *Using Graphics L.C.D.s* of Feb '01.

The GLCD requires a negative voltage as well as a positive supply. This is generated by the voltage inverter IC3, which outputs a negative voltage of approximately –5V when powered at +5V. Capacitor C7 smooths IC3's output, and preset VR1 then sets the current flow between IC3 and the GLCD's contrast setting pin, CX, so allowing the screen display intensity to be adjusted.

Provision has been made for the circuit to be jointly powered by an internal 9V PP3 battery (B1) and an external 9V to 12V d.c. mains adaptor connected via socket SK1. Normally, the circuit would receive its main power from the adaptor, with the battery supplying power in the event of a



Example display of a falling tide, with a nearly full Moon.

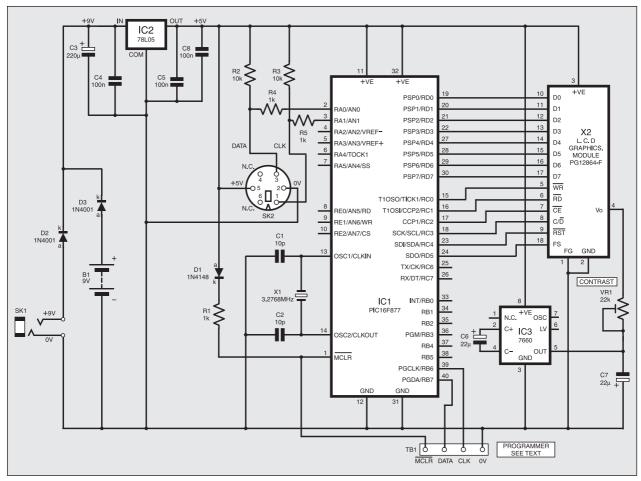


Fig.2. Complete circuit diagram for the Moon and Tide Clock Calendar.

mains power failure. Diodes D2 and D3 ensure that neither power source feeds into the other. Capacitors C3 and C4 smooth the input power supply.

Regulator IC2 reduces the input supply to +5V, to suit IC1, IC3 and the GLCD. Capacitors C5 and C8 help to stabilise the 5V supply lines. Power consumption is around 22mA. The keyboard when connected is also powered at 5V from the regulated supply, typically taking about another 80mA.

Following the publication of the *World Clock*, a few readers questioned why a lower power regulator had not been used in preference to the 78L05 regulator in that design. They also wondered why the GLCD and inverter IC3 could not be switched off when the display was not required for viewing. In principle, both actions would help to conserve battery power if the clock was in transit.

The answer is that the GLCD cannot be powered-down without it subsequently needing to be put through its initialisation routine, the length of which could disrupt the accuracy of the PIC's clock timing routines. It is acknowledged, of course, that another chip (PIC or dedicated real-time clock timer) could allow this option without timing disruption, but this option was/has not been taken.

If anyone were to consider redesigning the *World* or Moon-Tide clocks, there are several real-time clock chips that could be considered and the software amended to suit them, and provide the standby powerdown option.

Because of the GLCD high power consumption, the use of a lower power regulator was not felt to be significantly beneficial. It should also be noted that a 7805 1A regulator has been used in this design in order to provide sufficient current to power the keyboard when connected.

pre-programmed

7805 5V 1A voltage

(see text)

regulator

IC2

As usual, the author has provided a programming connector (TB1) for those who wish to modify the software to suit their own needs using his *Toolkit TK3* programmer. Components D1 and R1 protect the power line against adverse voltages during programming. They should be

p.c.b. mounting; mounting bolts to suit;

1mm terminal pins; solder, etc.

CON	IPONE	NTS	Approx. Guidanc		£58
Resistors R1, R4, R5 R2, R3	1k (3 off) 10k (2 off)	See SHOP	IC3	7660 d.cto converter	o-d.c. voltage
		TALK	Miscellane	eous	
Potentiomete VR1	r 22k min. round pre	page	SK1	mounting	daptor, p.c.b.
Capacitors			SK2	6-pin mini-D panel mo	
C1, C2	10p ceramic	,	X1 X2	3.2768MHz	
C3	220µradial e		7.2		l.c.d. module,
C4, C5, C8	100n cerami	ic disc, 5mm f)		T6963-ba	
C6, C7	22μ radial e	lect. 16V		circuit board, ava	
Semiconduct	ors			-pin d.i.l. socke	
D1	1N4148 sigr	nal diode		c.b. mounting; 9	
D2, D3	1N4001 rect (2 off)	ifier diode	battery ad	aptor, mains po mm x 110mm	owered; plastic
IC1	PIC16F877 microcont	roller,		keyboard (see le or pin-header	





retained even if re-programming is not contemplated.

Construction

The Moon-Tide clock is assembled on a single-sided printed circuit board, whose component layout and tracking details are shown in Fig.3. This board is available from the *EPE PCB Service*, code 467.

Assemble in the usual order of component size, starting with the few link wires, noting that some go under the IC1 and IC3 socket positions. Leave the battery holder until last. Correctly observe the orientation of the polarity sensitive components as indicated. Do not insert IC1, IC3 or connect the GLCD until the correctness of the 5V regulated supply has been proved. Use 1mm terminal pins for all off-board connections.

Thoroughly check the assembled board for component positioning errors and poor soldering.

Any suitably sized plastic case can be used; the prototype was housed in a simple one measuring 190mm × 110mm × 60mm. A rectangular cutout is needed in the lid to suit the mounting of the GLCD, and two holes are needed in the base to accept the sockets for the power supply and keyboard connector.

First Tests

When power is applied, the PIC software initialises various factors, including the GLCD. These factors include retrieving from the PIC's internal EEPROM the initial values relating to the clock, calendar, Moon and tide display. Having done so, the main screen is formatted. All factors at this time are those last set by the author within the source code (ASM file).

The bottom two text lines show the clock and calendar data, the Moon display shows the first segment of a new Moon, the tide is shown initially rising fast from zero to nearly full (just for display interest), and is then set to a half-way rise position. The clock is now in full running mode. You will probably need to adjust the GLCD's screen contrast setting preset, VR1, for the display to be visible.

Connect a PS/2 keyboard, whose l.e.d.s will flash the moment that connection is made and the keyboard's own circuitry receives power. Clock, Calendar and Moon/Tide displays can now be set for the current time and date.

The first keyboard key worth using is the question mark key ("?"). Repeatedly pressing and releasing this causes the full cycle of correction mode headings to be shown at the right of GLCD text line 7 (out of eight).

In addition to the mode heading itself, the symbol of the key that individually calls up that mode is displayed to its right. These are the mode headings displayed:

Mode	Key	Function
TIDE	T	Tide height adjust
MOON	#	Moon phase adjust
HOURS	Η	Hours adjust
MINS	M	Minutes adjust
SECS	S	Seconds adjust
DAYS	D	Days of month adjust
MONTH	L	Month (Lunar)
		name/number adjust
YEAR	Y	Year adjust
W-DAY	W	Weekday adjust

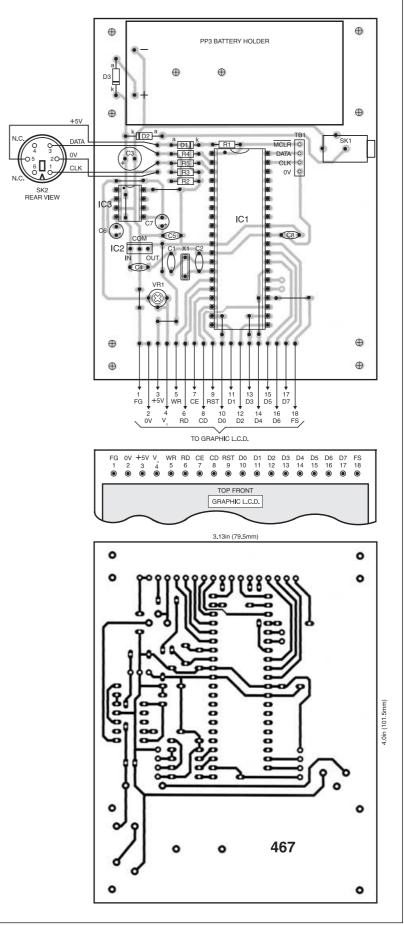


Fig.3. Component layout and master track pattern for the Moon and Tide Clock Calendar.





The modified World Clock p.c.b. used by the author when developing the Moon and Tide Clock Calendar. It differs slightly from the published p.c.b. shown in Fig.3.

T-ADJ % Clock timing accuracy adjust

C-ADJ C (C for sea!) tide timing accuracy adjust

M-ADJ O (O for orbit) Moon timing accuracy adjust

There are also five other functions whose key notations are not displayed:

+ Increment value of selected function

Decrement value of selected function

Z Reset seconds to zero

B Bird novelty display on/off (see later!)

ENTER Store new values to PIC's EEP-ROM registers

It will be seen from the above that the modes are mainly called by their initial letter keys. Because some modes have the same initial letter, though, other keys have been allocated to access them.

When any heading is shown, the values associated with that correction option can be adjusted up or down by using the plus (+) and minus (-) keys.

When changing values using the + and – keys, note that there is a built-in restriction which keeps the rate of change to a maximum of two per second. Whilst either key can be held down to repeat the change of value, this option should be treated cautiously as it has been found that on rare occasions the keyboard's own repetition rate has exceeded the ability of the PIC's half-second data reading to maintain synchronisation, and brief "lock-up" has occurred.

The reason is not known, but the PIC's restriction has to be maintained in order to keep the change rate at a reasonable speed

that can be readily seen and reacted upon by the user.

Setting Values

It is recommended that the values are set in the following order:

YEAR (Y): The year value can be cycled through 100 years, from 0 to 99. The century prefix of "20" is not used or displayed. The year should be set first since a check is made for each value to determine whether or not it is a leap year, which the affects the maximum number of days permitted when the month is February (28 or 29). When the year count reaches 99, the next increment via the "+" key causes the year number to roll over to zero; when it is at zero, the next decrement via the "-" key causes a rollover to 99.

MONTH (M): The month can be incremented or decremented and is indicated by the first three letters of its name, from JAN to DEC, with the selection of February being noted in relation to a leap year. Rollovers are from DEC to JAN, and from JAN to DEC.

DAYS (D): The day of the month can be changed up or down. The maximum upper limit is that appropriate to the month. At the upper limit, the upwards rollover is to a value of 1. Downwards rollover from a value of 1 is to the maximum number of days for that month. As said, February's maximum count is leap year dependent.

WEEKDAY (W): Weekdays are shown by the first three letters of their name, with appropriate rollovers on a continuous 7-step cycle.

HOURS (H): Hours are on a 24-hour basis, rollovers 23 to 0, and 0 to 23.

MINUTES (M): Minutes rollovers are from 59 to 0 and 0 to 59.

SECONDS (S): Seconds rollovers are also 59 to 0 and 0 to 59.

MOON (#): The Moon phase can be changed forwards or backwards. The progression is slower than with the clock/calendar changes as the software has to plot 180 points on the screen for each change of value. There are 256 steps to the complete phase cycle.

TIDE (T): Tide height can be changed up or down and there are two program routines for it. One is for when the tide state is being progressed forwards in time, the other for when it is being receded backwards in time. This is because of the different way in which the display is shown with regard to the upper section of the tide triangle.

As said previously, when the tide is rising, the triangle peak rises up the screen; when the tide is falling, the peak is progressively flattened. The rate of change in response to the + and – keys is also slower than with clock/calender changes, again because of the number of software steps required

At the bottom right of the screen is shown a number relative to the tide position. When the tide rises from nil to full, the number progressively decrements from 47 to 0, and a rollover to 93 (one less than 94 because zero is also a used value), at which point the tide is considered to be falling. The countdown is then from 93 to 48, the lowest point of tide-fall, and at 47 the tide starts to rise again, back to zero count.

It should be appreciated that the + and – keys do not directly indicate the raising or lowering of the tide display's upper lines. Rather, they should be regarded as tide *time travel forwards* (+), or tide *time travel backwards* (–)! It will become apparent when you use this mode.

ZERO SECONDS (Z): Seconds can be reset to zero simply by pressing key Z.

Timing Adjustments

As for any clock (unless triggered by the frequency of the mains power supply) adjustment to the actual rate at which the seconds are incremented has to be adjusted for accuracy. Even though this design is crystal controlled, the true rate at which the crystal oscillates can vary between individual units. For most applications these minor tolerance differences do not matter. But for a real-time clock they can become significant over time.

A similar technique to that used in some of the author's previous designs has been included with this Moon-Tide clock. As mentioned previously, the PIC's TMR0 timer has been set for a rollover every fiftieth of a second. An initial counter counts these rollovers in batches of 25, i.e. it counts half-seconds.

At each half second, a 24-bit value is added to another counter. The uncorrected value is hexadecimal number H'80 00 00' (three bytes). When H'80' (decimal 128) is added to H'80' the result is 256. With an 8-bit byte, this results in the counting byte being reset to zero, and a "Carry" flag is set. In this clock, at each addition of the three basic bytes, the Carry flag is read and if it is set, the clock's seconds counter is incremented. If this rolls over from 59 seconds to 0, a chain reaction takes place, with subsequent clock/calendar values being incremented as appropriate.

It will be seen that if the value added is increased from H'80 00 00' to H'80 00 01', the rate at which the seconds are incremented is now speeded up slightly. On the other hand, if the additive value is decreased by one unit to H'7F FF FF' the seconds rate of increment is slightly slowed

The value of H'80 00 00' is decimal 8388608. It can be changed upwards to 16777216, or decreased to just 1 (zero is not allowed, because there would then be no change following addition). The effect of this range of possible additive values is that the accuracy of the clock timing can be very finely changed, by as little as 100/8388608% (0.000119%).

When clock timing accuracy adjustment is called using the "%" key (mode display **T-ADJ** %), at the right of the bottom line is displayed the difference between H'80 00 00' and the value actually set. Without correction, a value of 0 will be shown. Using the + and – keys, this correction can be changed up or down in steps of one unit, with a + or – symbol alongside.

In the first few days or weeks of putting the Moon-Tide clock into service, do not change the correction value. Instead, having set the clock/calendar values to a known time, periodically check the GLCD time display against the current true time. In due course, calculations can be made on the difference between the two values, and the correction counter set accordingly.

Moon Rate Adjustment

The rate at which the Moon-Tide clock triggers the Moon counter is dependent upon the main clock timing routine's accuracy just discussed. However, the rate at

which the Moon counters change in response to the main timer can also be changed.

When "M-ADJ O" is selected using key "O", the right of the bottom screen line shows the number of seconds between each Moon phase segment (9967 unless previously changed). Should you ever find that you wish to alter the Moon's rate of change, you can increase or decrease the seconds base value to whatever value you deem necessary.

Tide Rate Adjustment

As with the Moon rate, the tide rate can also be adjusted if you ever wish to. When "C-ADJ C" is selected using key "C", the value of 44714 seconds (or the previously changed value) is displayed at the bottom right of the last line. The value may be changed to whatever you prefer.

Storing Values

When the ENTER key is pressed, all current settings are stored to the PIC's EEP-ROM registers, where they remain even if power is disconnected. In the unlikely event that power is lost (your battery back-up should keep the clock running correctly for quite some time if mains power is lost), when power is re-applied, the stored values are retrieved from the EEPROM and become the current values. While you will have to plug in the keyboard and change the clock values, the others will likely be still relevant.

What is particularly important is that you will not have lost the correction factors for the clock, tide and Moon timings.

Novelty Ducking!

Just as a gimmick (and to entertain himself) the author decided to add a silly picture to the tide display at high tide – a bird (duck or seagull?) taking advantage of the high water. It's a descendent of the swimming bird shown in Demo 8 of the *Using Graphics L.C.D.s* article referred to earlier. It's something that might entertain kids (as well as the author!).

At the moment of high tide, this image is superimposed on the tide display area. As the tide then falls, the bird is gradually erased until it is totally gone at low water ("Mummy – help – Quacky's drowning!" Oh dear – ah well . . .). But it reappears at the next high tide!

There is, though, the option to "exterminate" the bird so that it does not appear. Simply press keyboard letter B (for bird) and the flag which activates the display at high tide is toggled between on and off for each pressing. If the bird is "active", the letter B is shown immediately prior to the time of the last high tide, on line 7.

Final Ebb

Predicted tide times for your favourite coastal area are usually posted on public notice boards along the promenade at the nearest resort. Yearly tide tables for selected regions are available from local newspaper shops in that locality. National daily newspapers frequently publish information in respect of tide times at London Bridge, along with "tidal constant" differences between there and selected coastal towns. Any of these sources will help you to set the tide factor for the UK region of your choice.

Valuable UK tide information can also be obtained from the Proudman Oceanic Laboratory (POL) via www.pol.ac.uk. POL is the UK's official tide prediction organisation.

Additionally, there is an excellent web site which has downloadable tide data software for thousands of locations worldwide, and which includes graphical displays of real-time tide predictions. Browse www.geocities.com/SiliconValley. Horizon/1195/wxtide32.html.

Moon phase data is obtainable from all the above sources, with the possible exception of POL.

Old Moore's Almanac is also a useful source for UK tide and Moon state predictions throughout the year.

Finally, do note the use of the word *prediction* in the above. Precise statements about forthcoming tide states can never be given. The actual states depend on many natural phenomena, including weather conditions, the erosion of the coasts, and the silting of river estuaries.

All any tide predictor can ever be is a *guide* to likely conditions. Fortunately, Moon phase prediction is likely to be far more reliable, at least in terms of conditions over the next several thousand millennia!

Resources

Software, including source code files, for the PIC Moon-Tide clock is available on 3.5-inch disk from the Editorial office (a small handling charge applies – see the EPE PCB Service page). It can also be downloaded free from the EPE Downloads page, accessible via the home page at www.epemag.wimborne.co.uk. It is held in the PICs folder, under PIC MoonTide. Download all the files within that folder.

This month's *ShopTalk* provides information about obtaining pre-programmed PICs, and the sourcing of components.

The PIC program source code (ASM) was written using *Toolkit TK3* software (also available via the Downloads page) and a variant of the TASM dialect. It may be translated to MPASM via *TK3* if preferred.

The run-time assembly is supplied as an MPASM HEX file, which has configurations embedded in it (XT oscillator, WDT off, POR on, all other values off). If you wish to program the PIC yourself, simply load this HEX file into the PIC using your own PIC programming software and hardware.



Showing the optional novelty "duck" that can be set to appear at high tide!

A roundup of the latest Everyday
News from the world of
electronics

PHONES THEY ARE A'CHANGING – AGAIN!

The old "dog n' bone" is undergoing a 21st century makeover, as Barry Fox reports

THIS is the first time Alexander Graham Bell would look at a phone system and ask "what have you done to my idea? – I don't recognise it", admits Matt Beal, the man now responsible for making BT's phone system unrecognisable to the inventor of the telephone.

After 18 months of secret planning, BT is spending £10 billion over five years on becoming the first telecoms giant in the world to convert a traditional PSTN (public switched telephone network) to an IP (Internet Protocol) network. Instead of switching direct voice connections between users, the new 21st Century Network will carry speech end-to-end as packets of Internet data.

To make it more reliable, the 21CN will be quite separate from the Internet.

Market analyst Gartner Dataquest spots this as a "major lift" for Marconi, because the once-beleaguered company has been chosen to supply equipment.

BT says the sea change will improve the quality of service for those people who install new equipment, and cause no problems for people who want to stick with what they have got.

Emergency Problem?

But BT admits there is one key problem still unsolved – how to guarantee an emergency phone service if the lights go, during a power cut or after a terrorist attack.

21CN trials start in Cambridge and London this October, with full UK changeover beginning in 2006 and finished by 2009. Extending the change worldwide will be easy because BT's Global network already uses IP to let large organisations share data through 126 countries in Europe, the Americas, Far East and Australasia.

Anyone with an Internet PC can already use it with a microphone or modified phone to make free voice calls to anyone else with a similar system (*New Scientist* 11 Oct '03, p 24). Speech is sent as packets of data, like instant email messages. Enterprising third parties, such as Comcast or Vonage in the US, charge a few cents a minute to connect IP calls to existing PSTN networks and phones. It is not even necessary to use a PC to make IP calls; a standalone Internet Access Device plugs by Ethernet network cable between a broadband modem and an ordinary telephone handset.

Money Talks

The incumbent telecoms giants have still been making money, because people were paying them by the minute to connect to the Internet. But all-you-can-eat broadband services, charged by the month at a flat fee, is cutting the telecoms operators out of the VOIP revenue stream. It also puts an ever-heavier burden on the Internet. Voice over IP calls can sound disjointed and distorted as packets of data are delayed and lost by congestion.

"The holistic change from PSTN to IP is now inevitable. So let's attack it and make it happen. All the other majors, like France Telecom and Deutsche Telecom are sitting on the touchlines. But once BT does it, the rest of the world will follow", says Matt Beal, who came to BT as Director, 21CN implementation and strategy, "bleeding" from the experience of running a small VOIP start-up company in the US, called NexBell.

By using its own IP network, and not relying on the Internet, BT hopes to sell the promise of a more reliable and cheaper voice service, along with broadband access, video on demand, FM quality radio, automatic re-direction of speech calls and data to mobiles – all down a single pipe into the home or office. Voice messages left on a network store can be listened to by phone, sent as sound files to an email address, or converted by speech recognition software into a text message in the same or different language.

The IP pipe can be existing copper wire, as used for ADSL, or fibre, or a Wi-fi 802.11 radio link, or even a 3G phone. New phones, looking and behaving like existing phones, will have computer and modem chips inside to decode IP data into analogue speech.

Surround Sound Phones?

Because 21CN is independent of the Internet, it can use the new IP Version 6, which allows more user addresses and gives speech priority over other data. Initially all BT's VOIP calls will get the same 64Kbps data rate used for today's digital PSTN calls. Higher data rates will upgrade mono to stereo or even surround sound, with FM radio quality.

Who could possibly want surround sound for a phone call? A conference call makes a lot more sense when speech from different callers comes from different parts of the room.

If people do not want to be part of the brave new IP world, the local exchange can convert IP to analogue and send it down existing copper wires, to ordinary phones.

If an IP network subscriber wants to call a phone in a country which is still in the PSTN age, the call is converted into PSTN format by gateways at the edge of the IP network.

Power-Loss Emergencies

But what happens when mains power is lost? Conventional phones are powered by a 50V low current supply fed down the copper wire. So they work in a power cut.

Glass fibres cannot carry power, so need copper wires wrapped round them. But VOIP phones and PCs will often need more current than the current PSTN system delivers. Wireless links need power for reception as well as IP decoding, so will fail completely in a power cut.

BT is now researching the use of rechargeable batteries, to go in all consumer IP hardware. The batteries are continually topped up from the mains. But rechargeable batteries degrade; after a year or so they hold less charge. People cannot be relied on to replace them.

"We are confronting the issue and are confident we can solve it", says Matt Beal. But BT is not yet sure how.

Telecoms regulator OFCOM is blunt. "It is an absolutely essential legal obligation that anyone using a telephone phone service must be able to access lifeline services like 999, 112, the operator or directory enquiries" says Matt Peacock, OFCOM's Director of Communications. "We don't care how they solve the problem but they have to do it to stay within the law. This is not a grey area. It is not negotiable".

FLIPPER DISC

The music industry now has the green light for a new kind of hybrid disc, nick-named the "flipper", that plays like a music or data CD from one side and a video or data DVD from the other.

At a meeting in Seattle recently the DVD Forum formally approved the idea of bonding a thinner-than-usual CD, back to back with a thinner-than-usual DVD. The complete sandwich is still thicker than ordinary CDs or DVDs, though, and may jam in some players. The laser optics in the player may not be able to focus accurately.

The Forum is still trying to decide whether the new discs can carry the familiar DVD logo. Philips, which controls CD licensing, has already decided.

Says Philips: "A CD of about 0.9mm thickness does not comply with the CD specification. So the CD logo cannot be used. We strongly advise the (record) labels and disc manufacturers to apply a clear warning on the package that informs consumers of possible playback problems."

METER POWER



Lascar Electronics has recently launched a meter module that can measure any voltage from 4V to 25V d.c. with no extra supply needed. As the meter is signal powered, most applications can be measured using only two connections to the easy-to-use terminal blocks located on the rear of the module.

Housed in an attractive slimline design, the meter also has a vivid l.e.d. backlight that can be powered from an external 5V source. The l.c.d. features an 18mm digit height. The basic price is £24.95 + VAT, with discounts available for volume orders. The SP5 1200-BL is IP67 rated and splashproof.

For more information contact Lascar Electronics Ltd, Dept. EPE, Module House, Whiteparish, Salisbury, Wilts SP5 2SJ. Tel: 01794 884567. Fax: 01794 884616. Web: www.lascar.co.uk.

FAST COMPONENTS

FAST Components Ltd is a new component supplier targeting hobbyists with its brand new range of component kits, offering "a fresh and more economic way to buy components".

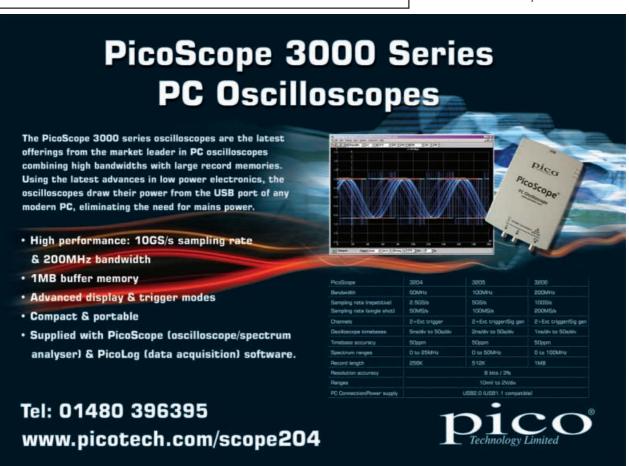
"We sell brand new component kits," say co-founders Thomas Arundel and Tsuyoshi Kihara. "As former product designers we were frustrated with the time and effort it took to build up a decent stock of bench-top components. Once we started investigating a supply chain, we realised that we could sell kits of commonly required components at one-third of the price that those same components would cost separately."

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For more information contact Fast Components Ltd, Dept EPE, Winchester House, Winchester Road, Walton-on-Thames, Surrey KT12 2RH. Tel: 0870 750 4468.

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TEGHNO-TALK ANDY EMMERSON

Daddy or Chips?

With rich content and bloatware demanding ever more bandwidth – how can we cope? asks Andy Emmerson.

OR the little girl in the TV commercial, the choice was simple – daddy or chips. As decisions go, it was a tough one but at least she had all the facts at her fingertips. The choice for people looking to cable their homes and offices is one of three alternatives, not two, in a world where the technology options are by no means clear-cut.

Glossy Home Automation

A look along the shelves of most newsagents will reveal at least two glossy monthlies devoted to the automated home, a key element of which is some kind of fancy network systems for audiovisual entertainment, PC networking, Internet access and general domestic automation of heating, lighting and alarms.

The appeal is largely "aspirational" and for many readers the notion of fully-equipped homes is little more than a distant dream, but there's enough trade in these systems to keep a number of firms in business. On a smaller scale, many computer users have already data-networked their homes using either cables or a wireless system and of course the same applies to just about every commercial business.

But where is this all leading? Are cabled solutions old hat? Is wireless the answer? Or should we be looking along the lines of optical fibre? And is the answer the same for all users?

One Size Fits All?

Much as everyone might like, where networking's concerned, the universal solution doesn't exist. The separate technologies of copper cable, optical fibre and wireless networking exist in their own right for a mighty good reason, simply because each solution has its own merits and applications where neither of the others would be as appropriate. Each also has its limitations.

Let's take wireless first. It's sexy and has considerable appeal to anyone looking for an easy, quick-fix solution. There's no drilling through walls and if you move, you just unplug the wireless kit and go. It's great for home users and even better for commercial users, eliminating the spagheti junction of under-desk cabling, making offices tidier and avoiding the cost and time overheads that moves and changes bring.

Equipping two or three PCs at home for wireless networking won't break the bank for most people, but Wi-Fi systems are not cheap to install on a commercial scale. Industrial-quality kit is not cheap and to achieve rock-solid wireless coverage additional apparatus is often needed to eliminate "black hole" reception areas.

What's more, wireless LANs lag behind copper and fibre in terms of data rate capacity and there are no signs of this changing. Many users find difficulty in managing the security aspects of WLANs too, making them a decidedly risky option.

Fibre is Good For You

This is true, although few nutritionists would advocate an all-fibre diet. The same applies to networking and most observers consider a system using optical fibre cabling technically elegant but otherwise unwieldy. For home users optical cable is hardly a practical proposition; the connections require skills that only trained installers possess. In the office environment an all-optical system, with fibre cable installed from the server all the way to the desk or workstation could make economic sense if not for the cost of the adapter equipment that interfaces user devices to the fibre.

Most active equipment (switches and servers) ship currently with copper interfaces, as does nearly all desktop equipment. The cost of fitting fibre adaptors to all this equipment would wipe out any operational benefits.

This is not to say that fibre has no place at all in the commercial networking environment and fibre is definitely the appropriate solution for the "backbone" links between floors of a building and from one building to another on larger sites. In other applications copper still dominates the world market for voice and data cabling and all indications point to this tried and trusted technology remaining the safest strategy for most users.

Copper Bottomed

Copper has in fact been synonymous with cabling from the very beginning of electrical communication in the 1830s. Some 150 years later copper began to lose out to optical fibre and its greater bandwidth, which was fine for inter-city and intercontinental communication but hardly the tool for cabling homes or offices.

This summer copper caught up to a major extent and the launch of a copper cabling system delivering 10 Gigabit/s (Gbit/s) data transmission now means that copper is neck-and-neck with fibre for speed for local networking. Factor in the installation advantages of copper and you can see why commercial users are having to reconsider their networking strategies.

Brand-named CopperTen and made by the international company Krone, this remarkable cabling product is able to carry 10Gbit/s Ethernet signals for spans of 100 metres, adequate for any normal office or industrial application. Being a direct replacement for the existing standard for office systems, Category 6, full backward compatibility is assured. No new installation techniques are involved, so deployment will not be delayed by the need to learn new skills.

If, like me, you were wondering just how many applications for 10Gbit/s networking exist in the real world right now, I'll tell you the honest answer is probably none. But that's irrelevant. Moore's law, stating that computing power doubles every 18 months, has a powerful knock-on effect on networking. Computers are being changed every two to four years, and active network equipment replaced every four years.

You wouldn't expect to change your plumbing this frequently, though, and nor do users want to change their cabling infrastructure, simply because of the disruption that renewal creates. In fact most organisations find the opportunity to cable a building or floor only once every seven to ten years, meaning they must seize any available opportunity to futureproof their installation. On that basis, installing data cable that's good for 10Gbit/s makes sound economic sense, particularly when you realise that cabling costs are only a minor factor in the total cost of networking.

Wider World

So much for communication within the home or office – what about the world outside? The answer once again is horses for courses.

A decade or so ago technology guru Nicholas Negroponte set the cat among the pigeons when he claimed that wires and wireless would change place. Tasks traditionally performed by radio (such as broadcast entertainment distribution) would turn increasingly to cable, he argued, whilst a wirefree future beckoned for communication functions previously handled exclusively by wired means.

Modestly, he called this turnabout the "Negroponte Switch" and to a degree his prediction has come true. More recently he has clarified his reasoning and now he argues that both wired and wireless technologies have their place, with wireless being the ideal "transport mechanism" for short-haul communication that's essentially personal or private.

And this of course just has to be right. There's a huge network of copper cable installed already and the huge success of ADSL delivery of broadband shows that there's plenty of potential left in copper. For users on the move there's no real alternative to wireless; no other technology can satisfy customers' growing demands for access to an increasing range of services while retaining their personal mobility.

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High-Efficiency L.E.D. Torch - Doubly Flashy

THE high forward voltage of l.e.d.s is a challenge for efficient battery use. In the circuit shown in Fig.1a, a 74HC14 inverter is used to "double" the voltage of a 6V rechargeable ex-mobile-phone battery and drive a string of three 5mm l.e.d.s. The oscillator around IC1a and IC1b generates a square wave at about 2-8kHz, and its output is buffered by IC1c and IC1d used in parallel to maximise current to the doubler.

two spare inverters, IC1e and IC1f, which can be wired as shown in Fig.1b. They provide variable-duty voltage pulses which directly drive a logic-level MOSFET and the string of three l.e.d.s, D7 to D9, via current limiting resistor R8.

With resistor R7 and potentiometer VR1 setting a total resistance of $3.9M\Omega$, the frequency is 15Hz at 20% duty cycle. At 3m

distance from the subject the illumination appears continuous, but closer in the flicker is very obvious, like a strobe. Negotiating confined spaces, e.g. a spiral staircase, by this light is a challenge as the walls move as you climb. The output voltage is about 10-8V with a 0-8V ripple at the pulse frequency. The output current is limited by the inefficiency of the doubler.

Increasing the frequency to 35Hz (R7 + VR1 = $1\cdot 6M\Omega$) results in 35Hz, producing a good light, even to read by, though some flicker can still be seen as the duty cycle is 30%. Average current consumption is 21mA at $6\cdot 3V$, with an l.e.d. current about 28mA.

Right Choice

In this application, 40106 or 74C14 CMOS chips are so markedly inferior to the 74HC14 that they are unsuitable. The published maximum voltage rating of an 74HC14 is 7·0V. The high initial voltage of a freshly charged battery may be tamed to within this limit by switching in a diode (e.g. 1N4001) until the terminal voltage has fallen somewhat.

John Crichton, Orange 2800, Australia

Booster

Using a 6V supply and with 1N4148 diodes in the rectifier, which comprises D1 and D2 plus capacitors C2 to C4, results in a rectified voltage of 9.6V having a 12mA output capability for a 24mA supply current – an electrical efficiency of over 80%. Using a 6.3V battery the output voltage is 10V and the available current increases to 16mA.

If Schottky diodes are used for D1 and D2, there is an increase in voltage of 100mV, resulting in a 20% increase in available current, to 19mA (6-3V supply), at similar efficiency, and a much brighter light. Using these diodes there might be benefit in increasing the oscillator frequency substantially. This is set by the values of R1 and C1.

At night, the torch casts a pool of white light two metres wide to a range of over 6m without a reflector, perfect to walk by. Side leakage is useful but seal the l.e.d.s at the rear as back-scatter is irritating. Nearby colours are vibrant compared with the gloomy monochrome of incandescent torches

Pulsed Light

Anecdotal evidence suggests that a subjectively brighter light is obtained by low-frequency pulsing the l.e.d.s at currents higher than their continuous rating. There are

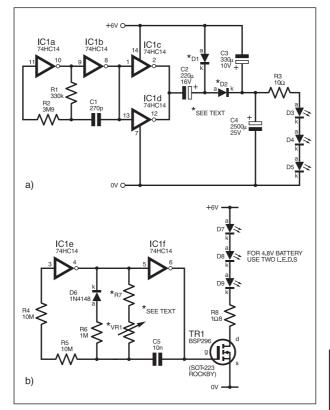


Fig.1. Circuit diagram for a High Efficiency L.E.D. Torch. a) basic circuit. b) pulsed circuit.

Warning

Be warned that flashing at frequencies in the 7Hz to 10Hz range is irritating and should be avoided as it could trigger an epileptic attack.

Temperature Controller - Hot Savings

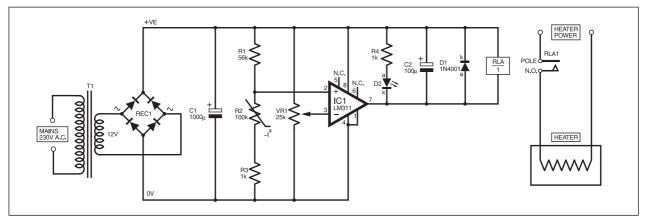


Fig.2. Circuit diagram for a water Temperature Controller. The relay contacts must be rated according to the appliance being controlled.

The circuit diagram in Fig.2 shows a simple yet highly efficient circuit for controlling water temperature. The heart of the controller is an LM311 op.amp used as a comparator, and having an open-collector output, allowing a relay to be driven directly from it. The op.amp senses the voltage across thermistor R2, which is a $100k\Omega$ at 25° C device. Potentiometer VR1 sets the comparator's "window" for a temperature

between 5°C and 95°C. The set temperature can be maintained within a variation of ± 0.2 °C.

Initially adjust VR1's wiper towards the 0V end. To set a temperature of, say, 40°C, heat the water to this temperature, measuring it with a normal thermometer. Then carefully adjust the wiper of VR1 slowly towards the +VE end until the output state of IC1 pin 7 goes low.

Once fully wired up, the circuit will turn on the relay when the temperature is below the set point, and turn it off again once the temperature has reached the required setting. If desired, the potentiometer's knob can be calibrated.

This mains electricity circuit must only be constructed by those who are suitably qualified or supervised.

Ejaz ur Rehman, Islamabad, Pakistan

Glitch-Free Switching - Clean Toggling

AREQUIREMENT in an aircraft simulation clock pulse each time the operator changed the position of a single-pole changeover switch (actually the undercarriage lever in a cockpit). This resulted in the development and verification of the simple circuit shown in Fig. 3.

It has generalised application to any situation where events must be triggered from a changeover switch, especially if the designer is obliged to use whatever switch already exists and has no choice regarding its specification.

Switch S1 is the toggle, its two detented positions being referred to as Up and Down. To the human eye, it appears to be functionless as either position will connect the selected output pole to 0V (ground). However, ingenuity in the design recognises that logic circuits react vastly more quickly than humans. What is actually seen at IC1 pin 1 (trigger input) is a low voltage changing briefly but significantly to a high while the switch is in transit (the pull-up resistor R2 sees to this) and then reverting to low again when the switch once more comes to rest.

Each high to low transition triggers the monostable IC1 whose output $\overline{\mathbb{Q}}$ is normally high. The monostable's output pulse here is low and very short, determined by capacitor C1 and resistor R3. This is still long enough to cover S1's contact-bounce, the monostable being a non-retriggerable type.

Summary

In summary, the instant that S1's moveable contact touches a pole, it triggers a negative-going pulse, typically of many tens of milliseconds, from IC1. This pulse can then trigger any further logic as required, such as a 555 timer to produce a long time period, for example. The positive-going pulse from output Q can also be used, depending on the application.

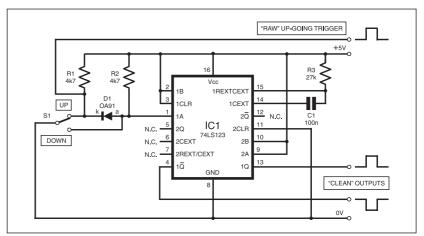


Fig.3. Glitch-Free Switching circuit diagram.

To demonstrate the flexibility stemming from this approach, another (non-clean, "raw") trigger has been tapped off from the UP side of \$1. This means that another monostable could be triggered selectively at the upgoing switch movement, isolated from the effects of the down-going movement by

diode D1. Whilst in isolation, this signal is still pulled up by R1. A similar extension could offer selective triggering on downgoing movements only.

Godfrey Manning G4GLM, Edgware, Middx

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The emulator system runs on the Windows-based MPLAB Integrated Development Environment (IDE), which gives devel-

opers the capability to edit, compile and emulate from a single user interface. The MPLAB desktop provides the development environment and tools for developing and debugging applications as a project, allowing quick movement between different development and debugging modes.

The new **PICkit 1 Flash Starter Kit** is a low-cost (\$36), easy-to-use programmer, evaluation and development kit for the company's 8-pin and 14-pin Flash-based microcontrollers. Supporting the PIC12F629, PIC12F675, PIC16F630 and PIC16F676 devices, the PICkit 1 Flash Starter Kit gives users the benefits of creating advanced microcontroller designs for a very low price.

The kit includes a 3-inch by 4·5-inch printed circuit board featuring a USB-powered programming tool that runs on a personal computer. The board's evaluation area features eight l.e.d.s, one potentiometer, one switch and a sample 8-pin PIC12F675 Flash device. The development area features a snap-off prototype board that enables users to connect external circuitry to the microcontroller for quick set-up of an



application-specific prototype. Programming the PICmicro Flash microcontroller is provided by the standalone graphical user interface.

The kit also includes a CD-ROM containing a user's guide with tutorials and example software code, the MPLAB Integrated Development Environment with HI-TECH PICC Lite C compiler, Microchip's *Software and Hardware Tips n' Tricks for 8-Pin Microcontrollers* and a USB interface cable.

Competition entries must be made on-line before November 15th 2004.

The names of the prizewinners will be published in the January 2005 issue of EPE.

INTERFACE

Robert Penfold ___



INTRODUCING VB.NET AND VB 2005 EXPRESS

T was originally intended to start a series of parallel port add-ons this month, but developments on the software front have delayed the start until next time. The *Interface* articles have been dominated by software issues for some time now, and this has been mainly in response to requests from readers for information or help in sorting out problems.

It has to be pointed out that much of the requested information has been covered in previous *Interface* articles. In particular, the basics of using various versions of Visual BASIC with add-on projects have been given a lot of coverage in the past. Delving through back issues will provide answers to many of the questions we are asked.

VB.Net

As regular readers of these articles will know, most of the example software is written using Visual BASIC 6.0. This is well suited to our purposes, as is Visual BASIC 5.0.

There is a slight problem with these languages in that they are no longer current, and have been replaced by Visual Basic Net (VB.Net). Unfortunately, the new version has gained features that are irrelevant to project interfacing, and lost some that were very useful in this application.

As explained briefly in reply to last month's *Readout* letter "Interfacing to VB", it is difficult to use VB.Net with an add-on such as **Inpout32.dll** that permits the ports to be easily accessed. The problem is simply that VB.Net seems to have no way of importing the BAS file that is used to tell the program how to use the added commands.

In the case of Inpout32.dll, there are just two added commands. These are the Inp and Out commands, which are used in exactly the same way as the same commands in GW BASIC, QBASIC, and Quick BASIC. Unless the BAS file is imported into VB.Net, it cannot use the routines in Inpout32.dll, and trying to use the Inp and Out commands will just produce error messages.

Through the Backdoor

A ploy "borrowed" from the Internet seems to find a way around this problem, and gets the BAS file loaded "via the backdoor". The basic idea is to load into VB.Net any program that uses Inpout32.dll, and already has the BAS file imported. A program such as this will not be compatible with VB.Net, but it can still be loaded. VB.Net will convert the program into its own format and then load it.

Once the program is in VB.Net it can be "fine tuned" in much the same way as if it was in Visual BASIC 6.0. Things can be taken a stage further, and you can obliterate most of the original program and use the remaining shell as the basis of your own program.

Converting

The conversion process is quite straightforward, and it starts by opening the project via the usual route (File – Open – Project). Using the browser to select and open the project results in the program detecting that it was written using Visual BASIC 6, and it then automatically launches the Visual BASIC Upgrade Wizard.

After the usual Welcome screen, you have to select the type of project that you wish to upgrade to, and the EXE option must be selected here (see Fig.1). The upgraded project

does not overwrite the original version, so the next screen is used to specify a location for the new version (see Fig.2).

After a warning that the process is about to start and may take a few minutes, the conversion begins. If all goes well, the conversion should end with the upgraded project loaded in VB.NET. In Fig.3 the demonstration program provided with

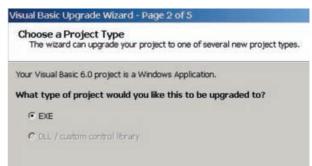


Fig.1. The EXE option must be selected when this page of the wizard appears.

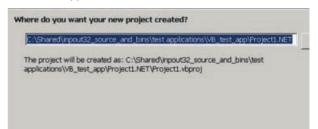


Fig.2. Choose a folder for the files produced by the upgrade. Several files and folders will probably be generated.

the new version of **Inpout32.dll** has been successfully loaded into VB.Net.

The process does not always go entirely smoothly, and on one occasion it caused the mouse to "freeze". However, when experimentally upgrading several simple projects the process never actually failed.

Once a project has been upgraded and loaded into VB.Net, it is possible to use it

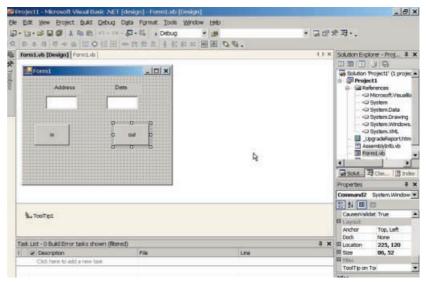


Fig.3. The Inpout32 test program has been successfully loaded into VB.Net. Upgraded programs can be edited and compiled in the usual way

much like a "real" VB.Net project. It can be edited, and there seem to be no problems if further Inp and Out instructions are added to the code. Compiling projects, or "building" them as it becomes in VB.Net terminology, seems to produce fully working EXE files, although I have not given this aspect of things extensive testing.

From Scratch

This method is fine if, for example, you would like to modify the software for an *EPE* project, or use it as the basis for one of your own projects. It is less satisfactory when starting from scratch. It then becomes necessary to delete everything in the program that you do not need, while being careful not to damage any code that is important.

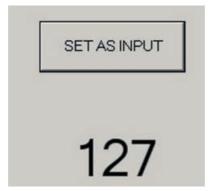


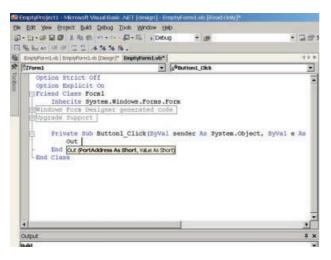
Fig.5. A simple test program based on the "empty" VB.Net program has compiled and run successfully

In use VB Express looks much like VB.Net (see screen shot Fig.6). It presumably lacks many of the more advanced features of VB.Net, but it has plenty of features, including a full range of components that can be added to forms.

It will upgrade and load VB6 and VB.Net programs. It worked fine with the test application provided with the new version of **Inpout32.dll**, and this is shown loaded in Fig.6. When the finished version is available it should provide a low cost way of writing the support software for PC projects.

INPOUTV4.BAS

Loading Inpout32.dll's supporting BAS file seems to cause more than its fair share of correspondence. The file for the new version of Inpout32.dll is called



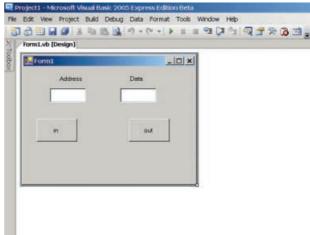


Fig.4. Using the Out command has caused VB.Net to respond with the appropriate hint-text

Fig.6. The Inpout32 test program can be loaded into VB Express, which is essentially a cut-down version of VB.Net

Life would be much easier if an "empty" program complete with the BAS file could be loaded. This would remove the need to delete anything before starting to add your own code. It would also guarantee that there would be no "left-overs" that could give problems with your program.

I tried loading the supporting BAS file for Inpout32.dll into VB6, and saving the otherwise empty program. This was then loaded into VB.Net, upgraded, and saved. The resultant project gives the desired result. It can be loaded into VB.Net, which will then recognise and use the Inp and Out commands. The screen shot in Fig.4 shows that the BAS file is listed in the Window near the top right-hard corner of the screen (INPOUTV4.vb), and typing "Out" into the code window has resulted in VB.Net responding with the correct hint-text for this command.

Proof of Pudding

As an initial experiment I tried producing a simple program that sets the printer port to operate as an input type when a button is pressed. It also displays a fresh reading from the printer port's data lines ten times per second. The program therefore uses both the Inp and Out instructions.

There were no problems when adding the code for the program, but VB.Net does seem to insist on adding brackets that would not be required when using Visual BASIC 6.0. The test program compiled properly and the resultant EXE program file ran perfectly on a computer running Windows XP (see screen shot Fig.5).

In fact everything seems to work well whether using the "empty" program or upgrading a complete Visual BASIC 6 program. There is obviously no guarantee of perfect results every time, but this method appears to be a practical approach for users of VB.Net.

The files for the "empty" program will be made available on the *EPE* web site in the Interface folder, and this should provide an easy starting point for anyone wishing to experiment with VB.Net and interfacing.

VB 2005 Express

One of the problems with VB.Net is that it is an extremely complex piece of software that uses large amounts of hard disk space. Microsoft seems to have realised that it is a bit "over the top" for many purposes, and a simpler version called VB 2005 Express should be available in due course. Information about downloading a free beta test version was given in last month's Readout feature. It is well worth downloading and trying this new program, but do bear in mind that it is a beta test program, and that it is not guaranteed to be fully working and stable. Just the opposite in fact, and it is guaranteed to contain a few bugs. Never run beta software on a PC that is used for any important purpose.

INPOUTV4.BAS and after unzipping the downloaded file it will be found in the \inpout32_source_and_bins\test applications\vb_test_app subfolder.

tions\vb_test_app subfolder.

When using Visual BASIC 6.0, it is loaded by selecting Add File from the Project menu and then using the browser to locate and open the file. Once loaded, it will be listed in the Project window as a module called "inpout".

It is possible to use **Inpout32.dll** with VBA (Visual BASIC for Applications), as supplied with various programs such as Microsoft Word and Excel, Autocad, etc. Obviously VBA is not intended for making standalone executable files, so there will be limitations on the ways in which it can be used.

There should be an Import or Import File option available from the File menu, and this is used to load INPOUTV4.BAS. If there is no Import function available, it is unlikely that Inpout32 can be used with that version of VBA. Note that there are differences between VB and VBA code, so most VB programs will have to be modified in order to run under VBA.



Volts Checker

Anthony H. Smith, BSc. (Hons)

A handy tester suitable for universal a.c. and d.c. supplies

HEN measuring voltages around the home, in the lab, or out in the field, some kind of voltmeter – either a traditional moving coil type, or a digital "multimeter" version – is usually indispensable. However, there are many instances where a basic "go/no go" indication is more than sufficient. For example, when tracing a fault in a car electrical system, or when checking a mains circuit to see whether it is "live", a simple "yes" or "no" visual indication can be the easiest and quickest way of checking for a healthy voltage supply.

In this respect, an l.e.d. (light emitting diode) provides an ideal solution. Unlike an incandescent filament bulb, the l.e.d. does not require much power, and it can operate at very low voltages where traditional neon indicators have not got a chance.

However, using an l.e.d. to indicate the presence of a voltage ranging from less than 3V to more than 200V – both d.c. and a.c. – requires something more than just a resistor and a suitable l.e.d.

Handy Size

Housed in a small plastic case, the Volts Checker is a handy, pocket-sized instrument that can be used to check an a.c. or d.c. voltage as low as 3V or as high as 240V r.m.s. (root-mean-square). Using the unit could not be easier – just connect the two leads to the circuit under test and the l.e.d. will illuminate if there is a healthy voltage present.

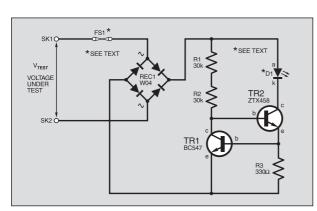


Fig.1. Circuit diagram for the Volts Checker.



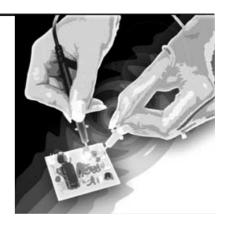
Furthermore, there is no need to worry about which node is positive and which is negative, the unit automatically senses any d.c. voltage regardless of polarity. Alternating voltages, such as those produced by step-down transformers in consumer equipment or high-voltage mains domestic circuits, are dealt with just as easily.

The circuit is based on the two-transistor l.e.d. current regulator described in *Light Emitting Diodes – Operation and Applications* Part Two (elsewhere in this issue), and makes good use of a "low current" l.e.d. to minimise power consumption

and permit operation at high voltages. The circuit diagram of this Volts Checker, shown in Fig.1, is almost identical to the aforementioned circuit, but with some important additions, the most obvious of which is bridge rectifier, REC1.

A.C./D.C.

Full-wave a.c. to d.c. conversion is a familiar function in many mains-operated power supplies, where a bridge rectifier, comprising



four power diodes, converts each cycle of the mains sinusoidal waveform to a *unipolar* voltage. In Fig.1, REC1 provides just the same function, in that an a.c. voltage waveform applied to the circuit's inputs via sockets SK1 and SK2, will be full-wave rectified so as to present a unipolar voltage to the current regulator.

This is illustrated by the graph in Fig.2, where the full-wave rectified waveform (dashed line) has a "half-sinewave" shape and reaches a peak on every half cycle of the mains waveform.

For a sinewave, the peak value, V_{PK} , of the waveform is given by:

$$V_{PK} = \sqrt{2} \times V_{RMS}$$
 (V)

where V_{RMS} is the root-mean-square value of the waveform, represented by a solid line in Fig.2.

Clearly, for a mains voltage with $V_{RMS} = 240V$, the waveform will peak at around 340V. The full-wave rectified waveform peaks at roughly the same value (actually, just slightly less due to the voltage drop across the bridge rectifier diodes).

Note that the bridge also ensures that a d.c. voltage of any polarity will also be "rectified" such that the l.e.d., D1, will illuminate regardless of the polarity presented via SK1 and SK2. The only price to be paid for this convenience is a slight increase in the minimum operating voltage due to the voltage drop across the rectifier diodes.

Current Regulator

Transistors TR1 and TR2 combine to provide the negative feedback loop required to maintain a constant current in l.e.d. D1. However, bias resistors R1 and R2, and transistor TR2 must be chosen very carefully to accommodate mains voltages safely

At each peak of the mains waveform, when the "+" terminal of bridge rectifier REC1 is at 340V with respect to the "-" terminal, the voltage across the collector-emitter terminals of TR2 is given by:

$$V_{CE2} = 340V - V_F - V_{BE1}$$
 (V)

where V_F is the l.e.d.'s forward voltage, and $V_{\rm BEI}$ is the base-emitter voltage drop of TR1.

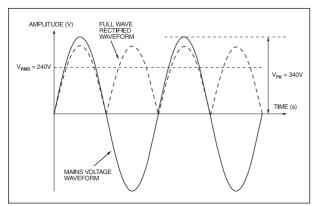


Fig.2. Effect of full-wave rectification on mains voltage waveform.

Assuming V_F is around 2V, and taking $V_{BE1} = 0.7V$, we see that TR2 must be able to withstand a maximum collector-emitter voltage, $V_{CE(max)}$, of around 337V! This demands a *high-voltage* device for TR2; common-or-garden devices like the BC108 and BC547 simply will not do. Fortunately, there are several high-voltage *npn* transistors on the market, and the one best suited to this application is the Zetex ZTX458.

Important Characteristics

The ZTX458 has a maximum collectoremitter voltage rating of $V_{CE(max)} = 400V$, allowing it to handle peak a.c. voltages of 340V with plenty of margin. However, power dissipation is also an important consideration; the ZTX458 has a maximum power rating, $P_{D(max)}$, of 1W, quite impressive for a relatively small package. The power dissipated in TR2 is given by:

$$PD = I_{C2} \times V_{CE2} (W)$$

where I_{C2} and V_{CE2} are the r.m.s. values of collector current and collector-emitter voltage, respectively. To see why power dissipation is important, we can rearrange this equation to determine the maximum permissible collector current when $V_{CE2} = 240V$:

$$I_{C2(max)} = P_{D(max)}/V_{CE2} = 1W/240V = 4.17mA$$

In practice, the collector current is set much lower than this to allow a healthy safety margin. The value of resistor R3 determines the magnitude of I_{C2} by the simple relationship:

$$I_{C2} = V_{BE1}/R3 \text{ (A)}$$

where V_{BE1} is TR1's base-emitter voltage. With $R3=330\Omega$, and taking $V_{BE1}=0.7V$, the nominal collector current in TR2 is:

$$I_{C2} = 0.7V/330\Omega = 2.12mA$$

Low Current Diet

These results highlight the need for a low current l.e.d. Attempting to use a standard l.e.d. needing a forward current of, say, 15mA for adequate brightness, would require a transistor capable of dissipating over 3-6W when measuring mains voltage! Instead, by using a low current device for l.e.d. D1, the required power rating is reduced enormously. In fact, with $I_{\rm C2}$ =

 $2 \cdot 12 \text{mA}$, the dissipation in TR2 is just over half a watt when $V_{\text{CE2}} = 240 \text{V r.m.s.}$, well below the 1W maximum rating of the ZTX458, thereby allowing plenty of margin.

CAUTION: do not substitute another transistor for the ZTX458 unless it can match or exceed the $V_{CE(max)}$ and $P_{D(max)}$ specifications!

Power and voltage ratings must also be considered when selecting bias resistors

R1 and R2. Using two equal-value resistors in series rather than just a single resistor has two advantages.

First, it means that the required maximum voltage rating is effectively halved. Since the peak voltage appearing across R1 + R2 is roughly the same as that across TR2, namely 340V, it follows that each of the two resistors need only be rated to 170V.

The second advantage concerns the resistors' power rating, which itself is dictated by the total resistance value required to ensure adequate bias current for TR2. As we shall see in a moment, connecting two resistors in series halves the dissipation in each one, *provided* they are equal in value.

Biased opinion

For proper, reliable operation, the bias current flowing into the junction of TR1's collector and TR2's base should be at least twice the maximum base current, $I_{B2(max)}$, required by TR2.

Now, $I_{B2(max)} = I_{C2}/h_{FE2(min)}$, where $h_{FE2(MIN)}$ is the minimum base-collector current gain of TR2. Since the ZTX458 has $h_{FE(min)} = 100$, it follows that $I_{B2(max)} = 2.12$ mA/100 = 21.2μ A. Therefore, the current flowing in R1 and R2 should be at least 42.4μ A. However, in order to calculate the values of R1 and R2, we need to know the minimum voltage across them.

If we follow the current path from terminal SK1, through one of the diodes in REC1, through R1 and R2, through the base-emitter junctions of TR2 and TR1, respectively, then through the opposite diode in REC1 out to terminal SK2, we see that the voltage across R1 + R2 is:

$$V_{R1+R2} = V_{TEST} - 2V_D - V_{BE2} - V_{BE1} (V)$$

where V_{TEST} is the terminal voltage, $2V_D$ is the voltage drop across the two bridge diodes, and V_{BE2} and V_{BE1} are the transistors' base-emitter voltages.

If we take a minimum value of 5V for V_{TEST} , and with V_D , V_{BE2} and V_{BE1} each equal to 0.7V, we find that $V_{R1+R2}=2.2V$. Therefore, it follows that the required total value of R1 + R2 equals $2.2V/42.4\mu$ A = $51.8k\Omega$. This suggests that we make R1 and R2 each equal to around $26k\Omega$. However, we must first check the power rating.

Power and Voltage Ratings

We know that the maximum r.m.s. voltage across R1 + R2 is roughly 240V, or

120V r.m.s. across each resistor. Therefore, the *minimum permissible* resistance value (for each resistor) is given by:

$$R_{min} = 120^2/P_{D(max)}(\Omega)$$

where $P_{D(max)}$ is the maximum power rating of the resistor. If we use 0.5W types, we find that $R_{min}=120^{2/0.5}=28.8k\Omega\cdot$ The nearest larger preferred value is $30k\Omega.$ Although this is some $4k\Omega$ greater than the ideal value $(26k\Omega)$ calculated above, it is essential to use a value of $30k\Omega$ to ensure R1 and R2 do not overheat when the unit is being used to test mains voltage.

Maximum ratings must also be considered when selecting an appropriate bridge rectifier for REC1. Thanks to the current regulator, the maximum current through the bridge will never be more than around 2.5mA under normal circumstances, so current ratings are not an issue. The bridge's peak reverse voltage rating, however, is most important.

Bridge rectifiers are used in linear power supplies where they are usually found on the secondary side of a mains transformer, and in this role they rarely have to deal with reverse voltages greater than 100V, or so. However, in the Volts Checker, the diodes in REC1 may encounter peak reverse voltages as high as 340V, and so a suitably rated bridge is essential.

The recommended part, a W04, can tolerate maximum reverse voltages of 400V, which is more than sufficient. It is important not to substitute a bridge rectifier with inferior voltage specifications as it could break down when checking mains voltages!

Protection

Fuse FS1 may seem unnecessary given that the circuit regulates the current to just a few milliamperes. However, a circuit fault, such as a short-circuit in the bridge rectifier, could result in catastrophically high current flow, even when testing relatively low voltages. Therefore, the fuse, connected "upstream" of all other components, provides an essential safety function and will rupture in the unlikely event of a circuit fault.

Component Types

Resistors R1 and R2 may be 0.5W parts, such as the Welwyn MFR4 series, or 0.6W such as the Philips MRS25 series. A 0.25W resistor is adequate for R3, although a 330Ω part from the MFR4 series or MRS25 series will fit just as well.

Most small-signal *npn* transistors with good current gain are suitable for TR1; although a BC547 has been specified, other types such as the BC108 should be suitable. The device specified for TR2, however, should not be substituted for another type.

A low-value, quick blow fuse should be used for FS1. The exact rating is not critical, but should be in the range 50mA to 100mA. Higher values should *not* be used.

The white, molded ABS case used for the prototype is inexpensive and was chosen because it accommodates the p.c.b. and 4mm connectors without crowding and is easily machined. If a smaller case is desired, it may be necessary to omit the connectors and, instead, hard wire the test leads directly to the board.

CAUTION: The case must be a plastic type with no metal parts passing through it.

A metal case, such as diecast aluminium, is not suitable since a fault within the unit could cause the case to become "live" when testing mains voltages.

An Agilent HLMP-D155, 5mm red l.e.d. is recommended for D1. although other low current types could be used. The HLMP-D155 provides very good intensity at the nominal 2mA operating current, although it suffers somewhat from a relatively narrow viewing angle, typically 24°.

Some alternative low current Agilent and Kingbright 1.e.d.s are shown in Table 1. This list is not exhaustive – several other manufacturers produce low current 1.e.d.s which may be suitable. Whichever part is used will depend on personal preference, but the chosen type should have low forward voltage, V_F , and good luminous intensity, I_V , at 2mA.

Construction

Assembly of the p.c.b. is straightforward and requires no special techniques. The component positioning and track layout details are shown in Fig.3. This board is available from the *EPE PCB Service*, code 468.

Solder the three resistors in place first, then the transistors and bridge rectifier. Take care with the orientation of REC1 – make sure the positive ("+") terminal is next to the l.e.d.

Fit the fuse clips next, then solder the l.e.d. in place leaving approximately 15mm of lead length between the top surface of the board and the base of the l.e.d. Take care with polarities, ensure all component leads are properly cropped and check carefully for solder splashes.

Testing and Troubleshooting

When assembly is finished, and thoroughly checked, the circuit should be tested before fixing the board in its case. Fit the fuse into the fuse clips and connect leads via sockets SK1 and SK2 to a d.c. voltage source, such as a bench power supply or battery of around 5V to 10V. The l.e.d. should illuminate. If it does not, check that the fuse is intact and that the l.e.d. has been inserted with correct polarity. If the l.e.d. still does not light, check that the bridge rectifier has been inserted correctly.

Next, reverse the polarity of the voltage source and check that the l.e.d. still glows. If it does not, the problem is almost

Table 1: A selection of l.e.d. comparisons

L.E.D. Type	Colour	Luminous Intensity, I_V (mcd)	Forward Voltage, V _F (V)	Viewing Angle
Agilent HLMP-D150 Agilent HLMP-D155	Diffused red Clear red	3 typ. @ 1mA 10 typ. @ 1mA	1.8 max. @ 1mA 1.8 max. @ 1mA	65° 24°
Agilent HLMP-4700	Diffused red	2.3 typ. @ 2mA	2·0 max. @ 2mA	50°
Agilent HLMP-4719	Diffused yellow	2·1 typ. @ 2mA	2.5 max. @ 2mA	50°
Agilent HLMP-4740	Diffused green	2.3 typ. @ 2mA	2.2 max. @ 2mA	50°
Kingbright W53LID	Diffused red	5 typ. @ 2mA	2.5 max. @ 20mA	30°
Kingbright W53LGD	Diffused green	2 typ. @ 2mA	2.5 max. @ 20mA	30°
Kingbright W53LYD	Diffused yellow	2 typ. @ 2mA	2.5 max. @ 20mA	30°
Kingbright W53LSRD	Diffused red	20 typ. @ 2mA	2.5 max. @ 20mA	30°

certainly a faulty bridge rectifier. It is advisable to check the operating current, especially if the l.e.d. seems very dim or too bright. Connect a d.c. ammeter in series with one of the supply leads and measure the current. It should be in the region of 1.5mA to 2.5mA; if the value is outside this range, check that R3 is the correct value.

Completion

When the p.c.b. has been successfully tested it can be bolted into place in the plastic case. First, four holes must be drilled in the base of the case. Positioning of the two holes in the bottom is fairly critical to ensure correct alignment of the l.e.d. The two small holes should be approximately 3mm in diameter – countersink if required. The single lid hole should be just over 5mm in diameter so as to accommodate a 5mm l.e.d.

In the short side of the base at the opposite end to the p.c.b., drill two holes to accommodate the 4mm sockets. The holes should be symmetrically spaced, approximately 20mm apart, with a diameter of about 8mm.

Solder two wires each approximately 20mm long to the supply terminals on the p.c.b., then screw the board into place using nylon screws and nuts, fitting two nylon or fibre washers underneath it, then solder the wires to the sockets. Position the lid on the case to check that the l.e.d. protrudes through the hole correctly.

If the lid cannot be seated properly, remove the board, de-solder the l.e.d., shorten the lead length above the board, resolder and repeat the process until the l.e.d. is at the correct height. If this process proves troublesome, there is no reason why the l.e.d. should not be connected to the

COMPONENTS

Resistors R1, R2	30k 1%, 0.5W (or 0.6W), 200V (or greater), n	See SHO[TALK page
R3	(2 off) 330Ω 1%, 0 film	

Semiconductors

וט	rea i.e.a.,5mm low
	current (see text)
TR1	BC547 (or similar) npn
	transistor
TR2	ZTX458 high voltage npn
	transistor (see text)
REC1	W04 bridge rectifier, 400V

Miscellaneous

FS1 fuse, quick blow , 20mm x 5mm (see text) SK1, SK2 4mm socket (2 off)

Printed circuit board, available from the EPE PCB Service, code 468; 20mm p.c.b.-mounting fuse clips (pair); 6BA 12mm countersunk screws, non-metallic (2 off); 6BA nuts (2 off); 6BA fibre (or nylon) 1mm-thick washers (2 off); 6BA lock washers (2 off); plastic case, 25mm x 73mm x 51mm (see text); connecting wire, solder etc.

Approx. Cost Guidance Only £9

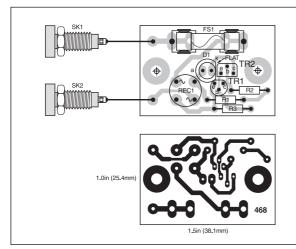
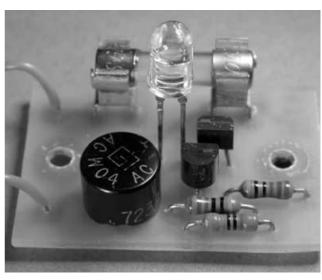


Fig.3. Component layout and master track pattern for the Volts Checker.



board using short flying leads and fixed into the lid using either adhesive or a suitable 5mm mounting clip.

Before screwing the lid into place, labelling and symbols could be applied to it as desired. Having screwed down the lid, the Volts Checker is then ready to use.

In Use

Measuring of a.c. mains voltages should only be carried out by those who suitably qualified supervised.

No special techniques are required to operate the Volts Checker, although the following precautions must be observed when testing high voltages, especially mains supplies.

Only properly insulated sockets, plugs and test leads should be fitted, and on no account should the unit be used to test high voltages with the lid removed. Furthermore, the l.e.d. is effectively at a very high potential when the unit is connected to mains voltage. Normally, this is not a problem, since the l.e.d.'s plastic casing provides adequate insulation. However,



a faulty l.e.d. could cause the insulation to break down. As a precaution, it is best to avoid touching the l.e.d. when testing high voltages.

Tests on the prototype showed that the 1.e.d.'s forward current, I_F, remained extremely stable over a wide range of terminal voltages. For example, at 5V, I_F was 1.66mA, whereas at 50V, the forward current was 1.91mA. In other words, for a tenfold (900%) increase in terminal voltage, I_E increased by just 15%.

Note that \check{I}_F was slightly lower than the nominal 2.12mA calculated earlier because TR1's base-emitter voltage was found to be somewhat lower than the assumed value of 700mV. When measuring the Volts Checker's operating current, remember that the terminal current equals the sum of I_F plus the bias current through R1 and R2. However, even at 50V, the terminal current was just 2.7mA. Even though this is much higher than the input current taken by high impedance voltmeters, it is still small enough to be negligible when checking most voltage sources.

The prototype unit worked well down to a minimum terminal voltage of 3.5V, where the l.e.d.'s forward current was 1.23mA. At 3.0V, I_E was just 0.48mA, and the l.e.d. was fairly dim, whereas at 2.5V, it was barely visible.

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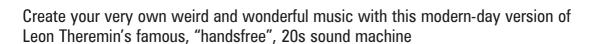
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Constructional Project

EPE Theremin

Robert Penfold



HE ORIGINAL Theremin was produced in the 1920s by Leon Theremin, and the basic idea has been periodically reborn since then. The Theremin is probably more popular now than at any time in the past, and still manages to keep some of its mystique in this technological age when just about anything seems possible.

A Theremin is an electronic musical instrument that is played without any physical contact being made with the instrument. The pitch is controlled by moving one hand closer to an "aerial" or sensor plate to produce an increase, or moving it further away to provide a reduction in pitch.

A similar method is used to vary the volume. Moving the other hand closer to a second aerial gives an increase in volume, and moving it further away produces a reduction in volume.

Hand Capacitance

Changes in pitch and volume are reliant on "hand capacitance", which is something that is normally a problem and has to be avoided when designing radio frequency equipment. In this case hand capacitance is exploited and aerials are used to greatly increase the effect.

Each "capacitor" is comprised of an aerial and one of the user's hands. These are effectively the plates of the capacitor with the air between each hand and its aerial acting as the dielectric. Placing a hand closer to an aerial gives an increase in capacitance.

This gives a form of variable capacitor, but the maximum capacitance obtained is extremely small. It is far too small to permit an audio oscillator to be tuned over a sufficiently wide range of frequencies. In order to obtain a wide enough pitch range it is necessary to use two high frequency oscillators with one operating at a fixed frequency and the other being "pulled" by hand capacitance. The general scheme of things used for a Theremin's tone generator is shown in Fig.1.

Sum Difference

The output signals of the two oscillators are fed to a mixer

where sum and difference frequencies are generated. For example, suppose the oscillators are operating at 900kHz and 901kHz. The sum frequency is 900 plus 901 = 1801kHz, and the difference frequency is 901 minus 900 = 1kHz.

The sum frequency is very high and is of no use in the current context. It is therefore removed, together with signals at the input frequencies, by a simple lowpass filter at the output of the tone generator.

The difference frequency is at an audio frequency, and provides the required audio tone. Placing a hand close to the aerial results in more capacitance being introduced to the tuned circuit of one oscillator, which slightly reduces its output frequency.

On the face of it, the user's body must be "earthed" for this to work properly, but in practice this is not necessary. The user's body acts as a sort of miniature artificial earth, and the extra capacitance is added across the existing capacitance in the tuned circuit.

The reduction in frequency will not be very high in percentage terms, but even 0.1 percent or so will give a reduction of about 1kHz. This is the point of using high frequency oscillators and mixing their outputs to produce the difference frequency via what is called the heterodyne effect. A small change in the frequency of one oscillator gives a large shift in the pitch of the audio output.

Suppose the 900kHz oscillator is the one that is altered. Its output frequency will reduce to 899kHz, giving a difference

frequency of 901 minus 899 = 2kHz. This represents a *doubling* of the output frequency from only a minute change in the frequency of one oscillator. In practice this system enables a range of several octaves to be obtained from small changes in hand capacitance.

In this example a reduction in the frequency of one oscillator was used to provide an increase in pitch at the output. Moving a hand towards the aerial sensor would therefore produce an increase in pitch. Probably most people find this more instinctive than having the pitch reduce as their hand is moved towards the aerial, but both methods are possible. It is just a matter of adjusting the oscillators for suitable starting frequencies.

If the 900kHz oscillator was set at 903kHz initially this would give a difference frequency of 903 minus 901 = 2kHz. Reducing its frequency to 902kHz would reduce the output frequency to 902 minus 901 = 1kHz. In other words, the same frequency range as before, but with the pitch reducing as the user's hand approached the

Volume Control

A real world Theremin tends to be substantially more complex than the block diagram of Fig.1 would suggest, mainly due to the inclusion of circuitry to control the volume via the second aerial. The full block diagram for the EPE Theremin design is shown in Fig.2.

The tone generator has two high frequency *LC* Oscillators with one

quency *LC* Oscillators with one having variable tuning and the other incorporating the aerial. The tuning control enables the required tone range to be set, and in practice a range of around 100Hz to a few kilohertz can be achieved. The outputs of the oscillators are mixed, filtered, and then amplified to produce a strong tone signal.

This is fed to a VCA (voltage controlled amplifier), a buffer stage, and then a small power amplifier that drives an internal loudspeaker. The volume is governed by the control voltage applied to the VCA. There is approximately unity voltage gain

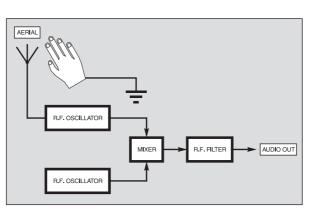


Fig. 1. Block diagram for a basic Theremin.

when the control voltage is equal to the supply potential. Reducing the control voltage produces increasing losses, and the output signal is cut off when the voltage falls to about 0.5V.

There are numerous ways of generating a suitable control voltage, and some of them do not involve hand capacitance at all. For example, the amount of reflected light or infra-red from a pulsed l.e.d. can be used. In this case the design is reasonably true to the original concept and it does use hand capacitance to control the volume.

Like the original, it is also based on a damped oscillator. In this case the oscillator is a *CR* type operating at an ultrasonic frequency. It has a Gain control that is adjusted so that the circuit is barely able to sustain oscillation.

and it gives a finished unit that is based on the same principles as the original Theremin. One drawback is that when applied to a low voltage semiconductor design it does not give very precise control of the volume. The range of hand movement needed to vary the volume from maximum to minimum is quite small.

On the upside, this makes it easy to produce rapid changes in volume and to produce a tremolo effect by wiggling one's hand. There is a potential problem of interaction when using any dual aerial system. In other words, varying the volume tends to produce small changes in the pitch of the output signal. This does not seem to be a major problem with this design, and it is probably aided by having the volume controlled by a relatively limited amount of hand movement.

the circuit a reasonably quick response time. Using a mid-audio frequency it can take a second or two for oscillations to build up or die away.

The "aerial" is coupled to the *CR* network via capacitor C4, and the hand capacitance is effectively in parallel with C3. Increased hand capacitance pulls the operating frequency slightly lower, but of greater importance it tends to damp oscillations. The damping will have no significant effect if the circuit oscillates strongly, as it will be insufficient to reduce the amplitude of the oscillations.

Potentiometers VR1 and VR2 control the amount of negative feedback applied to IC1, and therefore control its closed loop voltage gain. In practice the amount of gain has to be critically adjusted so that the

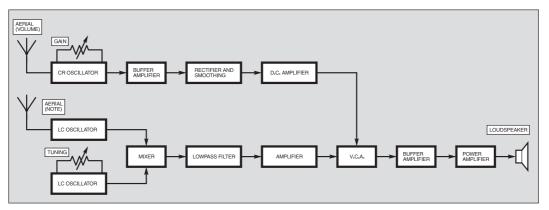


Fig.2. The full block schematic diagram for the EPE Theremin showing a general breakdown of the "Volume" and "Pitch" sections.

The aerial and hand capacitance are used to damp oscillations as the user's hand approaches the aerial. This gives reduced output from the oscillator, and oscillation will actually cease if the user's hand is placed very close to the aerial.

output from the oscillator, and oscillation will actually cease if the user's hand is placed very close to the aerial.

The output of the CR oscillator is fed to a buffer amplifier and then to a rectifier and smoothing circuit. The output from the smoothing circuit is a positive d.c. voltage that is roughly proportional to the strength of the oscillations from the CR oscillator.

This signal is fed to a d.c. amplifier that

This signal is fed to a d.c. amprifier that has only a low level of voltage gain. An inversion is provided by this amplifier, and it

therefore provides practically zero output voltage under standby conditions. The output voltage from the rectifier circuit falls when the oscillations subside, causing the output voltage from the amplifier to increase. The output voltage reaches maximum when oscillations cease.

This gives zero audio output under standby conditions, with increasing volume as the user's hand is placed closer to the aerial. For most users this gives a more natural form of control than having the volume decrease as their hand is moved towards the aerial.

Using a damped oscillator is a relatively simple means of controlling the volume,

Circuit Operation

The full circuit diagram for the EPE Theremin is shown in Fig.3. The top half of the circuit contains the volume control and audio output stages, and the tone generator section in the bottom half.

Starting with the volume control circuit, the *CR* oscillator is a Wien type that is based on IC1. Positive feedback is provided via the network comprised of resistors R3, R6 and capacitors C3 and C5. This controls the operating frequency, which is not too important in this application.

However, the operating frequency needs to be above the audio range in order to give closed loop voltage gain of IC1 is only just sufficient to sustain oscillation. VR1 and VR2 respectively operate as Fine and Coarse Gain controls.

Loading on the output of the CR oscillator can result in the of cuit being reluctant to resume oscillations after they have ceased, which gives an awkward control characteristic. The buffer stage, IC2, ensures that there is minimal loading on the output of the oscillator and thus helps to avoid this problem.

Capacitor C6 couples the output signal of IC2 to a basic half-wave rectifier circuit based on diodes D1 and D2. Germanium

diodes are used due to their lower forward voltage drop than the silicon variety. Schottky diodes should also work well in this circuit.

The smoothing circuit is comprised of resistor R7 and capacitor C7, and the values have been chosen to give reasonably fast attack and decay times. The circuit responds quite rapidly to variations in the output level from the oscillator, enabling rapid changes in volume to be obtained.

The d.c. amplifier is based on IC3, which is an operational amplifier used in a standard inverting mode circuit. Diode D3 is an l.e.d. indicator that switches on when the output of IC3 goes high. It is not essential, but it can be



useful when adjusting controls VR1 and VR2.

Voltage Controlled Amplifier

The VCA is a conventional design that is based on operational transconductance amplifier IC4. Like an ordinary operational amplifier it has inverting (pin 2) and non-inverting (pin 3) inputs, but it is current rather than voltage operated.

The output current is a function of the relative input currents and the bias current fed to pin 5. This bias input has no equivalent in an ordinary operational amplifier, and it enables the gain of the device to be controlled.

Although IC5 is current operated, resistors R13 and R18 are respectively used in series with the bias and audio inputs, effectively converting them to voltage operation. Resistor R17 is used as a load resistor at the output of IC4, giving an output voltage that is proportional to the output current that flows through R17. IC5 is a simple voltage follower buffer stage at the output of the VCA.

Some lowpass filtering is provided in the tone generator circuit, but R19 and C9 are used to provide some additional filtering. VR3 is a conventional volume control that is used to set the required maximum volume from the instrument.

The output signal from the wiper (moving contact) of VR3 is fed to the non-inverting input (pin 2) of a simple audio power amplifier based on IC6, which is the ever popular LM380N. This normally drives a miniature built-in loudspeaker (LS1), which, if preferred, can be switched off using S2. The signal available at output socket SK1 can then be fed to an external amplifier and loudspeaker. The direct output from IC6 is likely to be slightly excessive for use with an external amplifier, so resistors R21 and R22 are used to provide a modest amount of attenuation.

Tone Generator

The two high frequency oscillators in the tone generator circuit are based on transistors TR1 and TR2, and they are essentially the same. Operation of the circuit based on TR2 will be described here, but obviously they both operate in the same manner.

A form of Colpitts oscillator is used, and this relies on a capacitive tapping on the tuned circuit. This avoids the need for a tapping on the inductor, which can therefore be a standard "off the shelf" item. Inductor L2, with the series capacitance of C20 and C21, provides the tuning capacitance and the capacitive tapping. The latter connects to earth.

The tuned circuit acts as a basic single-wound transformer, and the important point is that a signal fed to one end of the tuned circuit produces an out-of-phase signal at the other end. It is connected between the input and output of a common emitter amplifier based on TR2. The input and output of a common emitter amplifier are out-of-phase, but the inversion through the tuned circuit results in positive feedback being provided. The circuit therefore oscillates at the resonant frequency of the tuned circuit, where the most feedback is produced.

Capacitor C23 couples the tone generator's aerial (Aerial 2) to the output end of

COMPONENTS

Approx. Cost Guidance Only

vel case & batts

			excl. case & batts.
Resistors R1, R2, R14 R15, R24, R28 R3, R6 R4, R7, R9 R5 R8 R10 R11 R12 R13	6k8 (6 off) TALK 470k (2 off)	C12 C13 C14, C19 C15, C21 C17, C22 C20 C23 VC1	100n polyester 470μ radial elect. 10V 47μ radial elect. 25V (2 off) 47p polystyrene (2 off) 1n mylar (2 off) 33p polystyrene 100p ceramic plate 5-6 to 65p miniature polypropylene dielectric, single turn, preset trimmer
R16	270Ω	Semiconduc	tors
R17	22k	D1, D2	OA91 germanium signal diode (2 off)
R18, R19, R21	10k (3 off)	D3	red panèl l.e.d.
R20	2Ω7`	TR1, TR2,	20742 "
R22	3k3	TR3	BC549 <i>npn</i> silicon
R23, R27	1M8 (2 off)	IC1, IC2,	transistor (3 off)
R25, R29	4k7 (2 off)	IC1, IC2,	TL071CN j.f.e.t. op.amp
R26, R30	560Ω (2 off)	103	(3 off)
R31, R33 R32	39k (2 off) 2M2	IC3	CA3140E MOSFET
R34	3k9		op.amp
All 0.25W 5%		IC4	CA3080E
7 0 2011 0 70			transconductance
Potentiomete		IC6	op.amp
VR1	10k rotary carbon, lin	IC7	LM380N audio amp i.c. 78L05 +5V 100mA voltage
VR2	22k rotary carbon, lin	107	regulator
VR3	10k rotary carbon, log.		rogalator
VR4	1k rotary carbon, lin	Miscellaneou	
Capacitors		L1, L2	1mH radial inductor (2 off)
C1, C25	100μ axial elect. 10V	S1, S2	min. s.p.s.t. toggle switch (2 off)
Co	(2 off)	SK1	phono socket
C2 C3, C5,	100μ radial elect. $10V$	LS1	miniature 8 ohm
C3, C3, C16	22p ceramic plate (3 off)		loudspeaker
C4, C18,	22p ceramic plate (5 on)	B1	9V battery pack (6 x AA
C24	330p ceramic plate (3 off)		size cells in holder)
C6	220n polyester	Stripboard,	size 34 copper strips by 82
C7, C26	1μ radial elect. 50V (2 off)		.i.l. socket (5 off); 14-pin d.i.l.
C8	220μ radial elect. 10V		copic radio aerial (see text) -
C9, C27	4n7 polyester (2 off)	2 off; PP3 typ	be battery connector; control
C10	2μ2 radial elect. 50V	knob (4 off);	large plastic case (see text);

the tuned circuit. The hand capacitance is therefore in parallel with C21 and tends to pull this oscillator lower in frequency.

100n ceramic (3 off)

C11, C28,

C29

One of the oscillators must be tuneable so that the required starting pitch can be set. Trimmer capacitor VC1 enables the operating frequency of the oscillator based on transistor TR1 to be tuned over a fairly wide range of frequencies. In theory it should be sufficient to set the required tone using this trimmer, and then make no further adjustment.

In practice, even though the two oscillators are basically the same, their frequencies are likely to drift apart over time. It is therefore advisable to have an externally accessible tuning control so that the tuning can be periodically readjusted.

Unfortunately, suitable variable capacitors, at a reasonable price, are difficult to obtain these days. Therefore, capacitor C16 and potentiometer VR4 are used to effectively provide a crude variable capacitance in parallel with trimmer VC1.

This arrangement gives only a relatively limited tuning range, but it is perfectly adequate in this respect. Coarse

tuning is carried out using VC1, with VR4 only being needed to make minor adjustments.

connecting wire; solder pins; nuts, bolts

Be Stabilised

and spacers: solder etc.

There can be a major problem with any circuit that uses two oscillators operating on similar frequencies. Any coupling between the two oscillators tends to lock them onto the same frequency.

This has to be largely avoided with a Theremin circuit as it would prevent an audio beat note from being produced. In this case any coupling is minimised by using the low impedance output signals at the emitters or TR1 and TR2, plus a simple passive mixing circuit that uses relatively high value series resistors (R31 and R33).

Transistor TR3 is used as a common emitter amplifier that compensates for the losses through the passive mixer. The slight non-linearity of TR3 helps to generate the sum and difference frequencies, with capacitor C27 filtering the input and sum frequencies to leave the required difference signal.

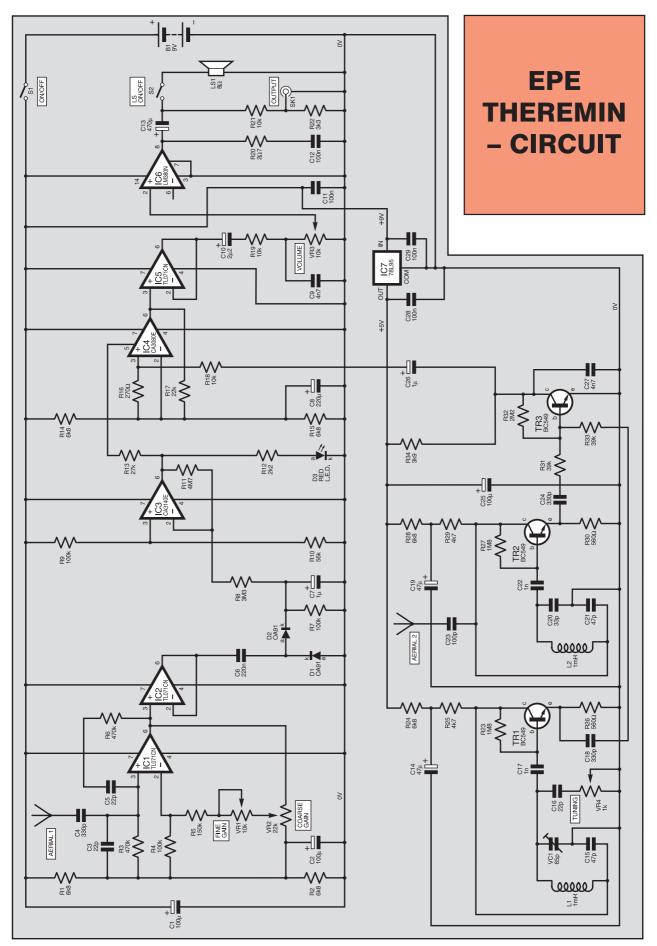


Fig.3. Complete circuit diagram for the EPE Theremin. The two distinct sections of the "instrument" can be seen.

A stabilised 5V supply for the tone generator is provided by voltage regulator IC7. Using a well stabilised supply for the oscillators helps to minimise any problems with tuning drift. The supplies to the oscillators have separate decoupling networks (R24/C14 and R28/C19), which helps to avoid problems with the oscillators locking on to the same frequency.

The basic current consumption of the Theremin circuit is a little less than 20mA, but it rises considerably when the "instrument" is played at high volumes. It is therefore advisable to use a fairly high capacity 9V battery, such as six AA size cells in a holder.

Construction

The EPE Theremin is constructed on large piece of 0.1 inch pitch stripboard having 82 holes by 34 copper strips. This is not a standard size for stripboard so a larger piece must be trimmed to size. The top-side component layout for the board appears in Fig.4, together with details of the cuts required in the copper tracks on the underside.

Stripboard is easily cut along rows of holes using a hacksaw, but some boards are quite brittle so it is advisable to proceed gently. Cutting along rows of holes tends to leave rough edges, but these can be carefully filed flat. If you use a board that has around 36 to 39 copper strips it is probably not worthwhile trimming off the extra strips. Just ignore the unnecessary strips along one edge of the board.

The cuts or breaks in the copper can be made using a hand-held twist drill bit of about 5mm in diameter or you can purchase a special tool for track cutting. A substantial number of cuts are required so make sure that none are overlooked and be care-

ful with their placement. Double-check the placement of each one before actually cutting the strip. Do not cut deeply into the board, but make sure that each cut fully severs the copper strip.

Board Assembly

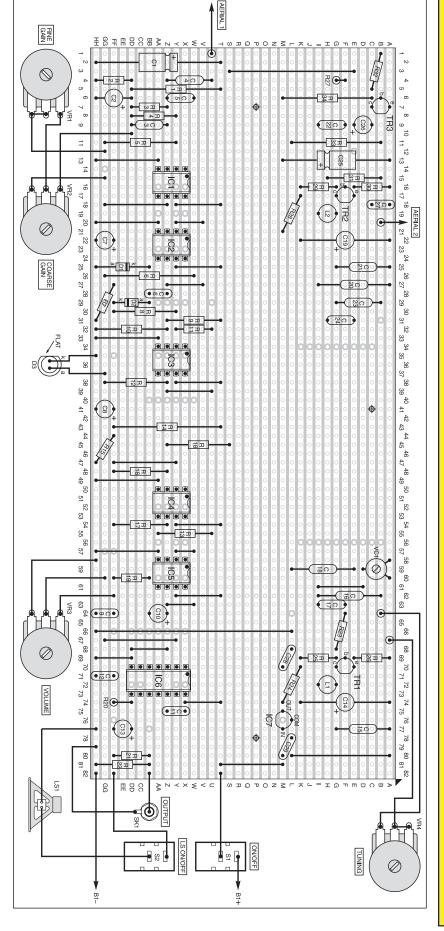
Construction of the board is not particularly difficult and it presents little that is out of the ordinary. However, with a board of this size it is obviously very easy to make positional errors when fitting the components, so take due care with the placement of each component; working from the smallest up to the largest. Dual in-line (d.i.l.) sockets are recommended for all the i.c.s.



Top-left of the circuit board showing component layout for the tone generator.

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EPE THEREMIN – CIRCUIT BOARD CONSTRUCTION



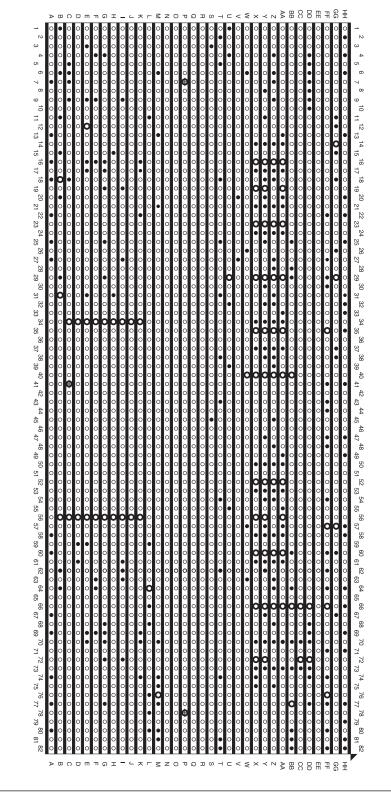


Fig.4. Stripboard topside component layout, interwiring details to off-board components and underside view showing the breaks required in the copper tracks. Double-check all copper breaks and positioning of all topside wire links before mounting any components.

Inevitably, a fair number of link wires are required, and due care has to be taken in order to avoid overlooking any. Some of the link wires are quite long. In order to avoid short circuits these links must either be very taut or insulated with pieces of plastic sleeving. Fit single-sided solder pins at the points where connections will eventually be made to the controls and other off-board

The two inductors used in the prototype are radial components, but the circuit should work just as well using axial ones mounted vertically. The trimmer capacitor specified for VC1 has a pin layout that matches up with holes in the board, but the three pins are too large to fit into the holes.

Rather than enlarge the holes and risk damaging the board it is better to fit single-sided pins and then solder the tags of the trimmer to these. This should be straightforward provided both sets of pins are tinned with solder first.

Any trimmer capacitor having a maximum value of around 40pF to 65pF is suitable electrically, but fitting alternatives into this layout could be awkward. Also, in order to fit into this layout easily the polyester capacitors should be printed circuit types having a pin spacing of 5mm (0·2in).

Only one static-sensitive device is used in this design, and it is the CA3140E MOSFET op amp used for IC3. The normal anti-static handling precautions should be observed when dealing with this component, and the most important of these is for it to be mounted in a socket.

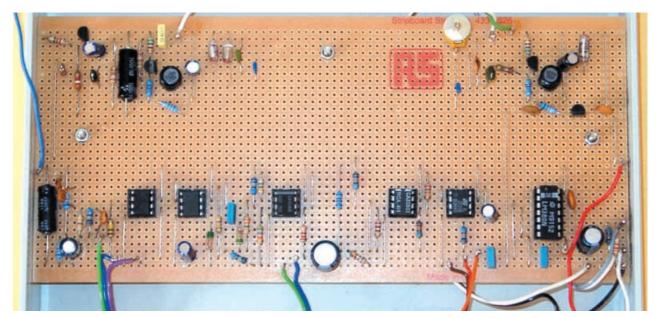
Diodes D1 and D2 are germanium diodes and they are consequently more vulnerable to heat damage than normal silicon semiconductors. It is not essential to use a heat-shunt when connecting these two components, but each soldered joint must be completed reasonably quickly.

Casing -Up

Due to the size of the circuit board it is necessary to use a large case for this project, but this is essential anyway due to the need for a reasonable distance between the two aerials. The



Top-right corner of the circuit board showing layout of components for the "tuneable" oscillator, including the miniature trimmer capacitor.



Component layout on the large circuit board. It is recommended that sockets be used for all the i.c.s.

aerials used on the prototype can be angled away from each other, which enables satisfactory results to be obtained with a relatively small gap between them. Normally a gap of at least 300mm is required, and it should preferably be nearer 500mm.

A telescopic aerial or a metal rod about 300mm long is probably the best choice for Tuning Aerial 2. In theory at any rate, a stout rod will give better operating range than a relatively thin telescopic aerial. In practice a telescopic aerial gives good results though. Bear in mind that having a large operating range for this aerial makes it necessary to have the other aerial mounted further away.

Although a second telescopic aerial was used for Volume Aerial 1, with hindsight a metal plate about 150mm square would have been a better choice. The operating range using any form of rod aerial is likely to be quite small, making the instrument relatively difficult to use. The plate can be a piece of aluminium with the connection made via a solder tag bolted to it.

Alternatively, a piece of copper laminate board as used for printed circuit boards will also work well. A connecting wire can, of course, be soldered direct to the copper side of this material.

It is possible to use a metal case, but construction is much easier using a case made of plastic or some other non-metallic material. If a metal case is used it is *essential* to insulate the aerials from the case.

This is not too difficult with the aid of a couple of grommets having the same inside diameter as the base section of the aerial. One grommet is fitted into the top panel of the case and the other is mounted just below it in an aluminium bracket.

Aerial Mounting

The aerials used on the prototype have a mounting bush at the base which makes it easy to mount them on a non-metallic case. It is just a matter of drilling a 10mm diameter mounting hole for each aerial and then securing them to the case with the supplied fixing nuts.

Again, things are more awkward if a

metal case is used. A large cutout would have to be made for each aerial, with a plate made from plastic or some other insulating material being mounted behind each cutout. The aerials would then be mounted on these plates.

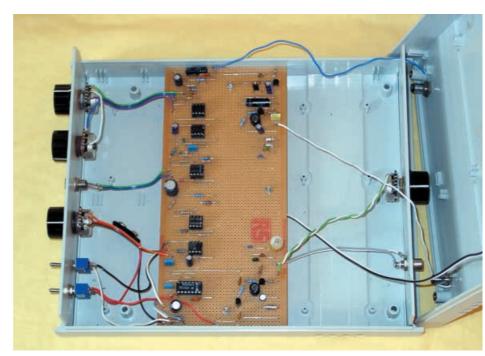
If a metal plate is used for Aerial 1, it will not work properly if it is mounted behind a metal case. It would either have to be fitted externally or behind a cut-out in the top panel, taking care to ensure that it is properly insulated from the case. Using a non-metallic case is the easier and more practical approach.

With most large cases it will be possible to mount the controls, l.e.d. D3, socket SK1, and the loudspeaker (LS1) on the front panel. There was insufficient space to permit this with the low-profile case used for

the prototype, and the output socket SK1 was mounted on the rear panel together with tuning control VR4. The latter should need little readjustment once the unit has been set up correctly, so you may prefer to relegate it to the rear panel anyway.

Likewise, due to lack of space on the front panel the loudspeaker had to be mounted beneath the top panel, but this is probably the most effective position for it. Fitting miniature loudspeakers tends to be problematic due to their lack of built-in mounting brackets.

A speaker grille of some kind is also required. The simplest way of producing the grille is to drill a matrix of holes about four or five millimetres in diameter. It is difficult to make a really neat job of this, so try to drill the holes as accurately as



General layout of components and wiring inside the case. Allow plenty of room for the battery pack.

possible. It is probably best to drill small guide holes about one or two millimetres in diameter first, and then drill these out to the required final size.

With the grille completed, the loudspeaker is glued in place behind it using any general purpose adhesive or superglue. Try to avoid getting any adhesive onto the loudspeaker's diaphragm, which could seriously impair its performance.

Interwiring

With the circuit board mounted in the case the hard wiring can be added. This is shown in Fig.4, which should be used in conjunction with the accompanying photographs. The hard wiring is very straightforward, and there is no need to use any screened leads. Avoid having any of the wires longer than is really necessary.

The connections to the battery pack are made via an ordinary PP3 type battery clip. The cathode (k) lead of l.e.d. D3 will be shorter than the anode lead and (or) it will be indicated by a "flat" on that side of the encapsulation.

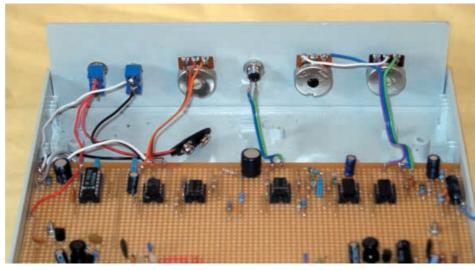
Tuning In

It is highly likely that the tuning will initially be so far out of adjustment that no audio tone will be produced. It is still possible to setup the volume control circuitry with the aid of l.e.d. indicator D3.

Start with feedback controls VR1 and VR2 adjusted well in a counter clockwise direction, which should result in l.e.d. D3 being switched off. Adjust the Coarse feedback control (VR2) in a clockwise direction until the l.e.d. switches on, and then back it off just far enough to switch it off again.

It is possible that the volume control circuit will then work quite well, but it is more likely that some fine adjustment using VR1 will be necessary. Adjust Fine Gain control VR1 in a counter clockwise direction to bring the circuit closer to the point of oscillation, or clockwise to reduce the sensitivity of the circuit.

Using high sensitivity gives a better operating range, but the circuit will probably show a reluctance to cease oscillating once it has started. VR1 must be fractionally backed off in order to correct this and give a more rapid response time.



Wiring from the circuit board to the front panel mounted components.

Making a Pitch

When adjusting the pitch controls it is a good idea set VR2 so that l.e.d. D3 is switched on without having to place a hand close to the volume control aerial. The VCA will then let the output of the pitch generator pass straight through to the output stage.

With switch S2 closed and Volume Control VR3 well advanced in a clockwise direction, use a small screwdriver to adjust trimmer capacitor VC1. It should be possible to find a small range of settings where there is a central null, with a tone of increasing pitch being produced if VC1 is adjusted either side of that setting.

Slightly offset VC1 from that central setting so that a tone of low pitch is obtained. If this produces a reduction in tone when a hand is placed near the pitch aerial, adjust VC1 through the central null and out the other side so that a low frequency is again produced. Placing a hand near the aerial should then produce an *increase* in pitch.

As and when necessary, Tuning control VR4 can be used to adjust the basic tone produced by the instrument. Bear in mind that the tone set using VR4 is the lowest one that can be obtained when playing the instrument. If you would prefer to have the pitch reduce as a hand is placed nearer

the pitch aerial, adjust VC1 to the other side of the central null and then set it for the highest note that you will require.

There should be minimal interaction between the volume and pitch sections of the instrument. However, there will be a small but noticeable shift in pitch if Aerial 1 (Volume) is accidentally touched. If this should be a problem it is just a matter of adding some insulation over this aerial.

Making a Play

In some respects a Theremin is very simple to play. It is strictly monophonic for example, so there are no chords to worry about

On the other hand, there is nothing to help you navigate from one note to another apart from the sound of the instrument. It has to be played "by ear", which is only a practical proposition for those possessing a reasonably good sense of pitch. As with any instrument, it is probably best to start with some scales and progress from there.

Some finger wiggling of the hands used to control the pitch and the volume will respectively introduce vibrato and tremolo effects. Vibrato is effective at giving practically any monophonic instrument a richer and more interesting sound, so it is worthwhile perfecting this technique.



Rear view of the Theremin showing the phono output socket, tuning control (VR4) and sensor "aerials" mounting.



Front view showing layout of the most used controls. The loudspeaker is glued to the underside of the top panel.

READOUT

Email: john.becker@wimborne.co.uk

John Becker addresses some of the general points readers have raised. Have you anything interesting to say? Drop us a line!

All letters quoted here have previously been replied to directly.

WIN AN ATLAS LCR ANALYSER WORTH £79

An Atlas LCR Passive Component Analyser, kindly donated by Peak Electronic Design Ltd., will be awarded to the author of the Letter Of The Month each month.

The Atlas LCR automatically measures inductance from $1\mu H$ to 10H, capacitance from 1pF to $10,000\mu F$ and resistance from 1Ω to $2M\Omega$ with a basic accuracy of 1%.



Design and Technology Resource

Dear EPE.

Reaching the end of another school year, I should like to pass on my thanks to *EPE* and its contributors for providing such an excellent resource for Design and Technology teachers.

Most of my GCSE Electronic Products students have used circuit ideas, articles and data from projects published in your magazine in their research this year. This year's cohort designed and made a range of imaginative projects such as an infra-red beam lap timer for Scalectrix; a decorator's measuring and calculation aid; a password-operated keypad lock and an ultrasonic "radar" attachment for a blind person's cane. A far cry from the days of "steady hand" games and fuse testers.

The PIC-based content has increased the knowledge and understanding of my A Level

students to the extent that they are producing coursework projects with genuine commercial potential.

Interest in electronics amongst pupils is at an all-time high at my school, and I suspect at many others. It is sad then to find out that Examination Boards are considering dropping courses through lack of uptake and schools are finding it difficult to recruit teachers to deliver those that remain. Any electronics professional tired of the rat-race and seeking real job satisfaction could do a lot worse than entering the teaching profession.

Bernard Grabowski, Aylesbury, Bucks

More power to you at your school Bernard. But I've been aware for over 15 years that many schools lack teachers trained to teach Design and Technology, with, in some cases, Art teachers fulfilling that role. The connection between art and technology in this context is hard to fathom.

★ LETTER OF THE MONTH ★

Underripe?

Dear EPE,

As a sort of follow-up to Dave Jones in September *Readout* (Overripe Student), I guess I am at the opposite end and just slightly younger... I have been interested in electrical fiddling all my life, with a spell of domestic wiring, and mostly running a motor repair business.

Apart from the very early battery, bell and bulb circuits, my first venture was trying to put together a valve amplifier from the then *Hobbies* magazine, using a smallish fire-heated soldering iron. I don't think it ever worked.

In 1952, my late father pointed out an advertisement in a national newspaper by the then *Practical Television* magazine saying "Build a Television for £20". So I bought the magazine and got stuck in, but with no previous experience and no idea of how a TV worked. However, a blueprint was supplied, and the building and fitting of components was really a matter of following this.

The TV consisted of five separate chassis that I cut, formed and drilled in my father's garage, and then bolted together. These were vision, sound, timebase, power supply and CRT unit. It had 21 valves and used a VCR97 ex-government radar tube six inches in diameter which produced a green and black picture size about 4.5×3.5 inches. The main transformer supplied 425V-0-425V at 200mA, the timebase transformer was 350V-0-350V at 150mA and a separate transformer supplied 2,500V for the tube (I still have these).

After the war, there was a glut of radio and other military components, and the parts were obtained from various suppliers, but unfortunately, due to some faulty items and my own inexperience, it was somewhile before I got results, and in fact I rebuilt the vision section and obtained ready-wound coils as against my home-wound ones, and this did the trick. This was in fact our first home TV, and gave my family much enjoyment because TV sets were then

a minority, with only one channel and using a large H aerial.

Later in the fifties, Practical Television brought out further and larger tube television circuits for the home constructor. These early home built and commercial sets had numerous controls that it was usually necessary to keep twiddling with during an evening's viewing. It was common for the picture to roll or break sideways into lines and multiple pictures due to poor signal and components altering in value during use – these duties now being done automatically with chips. Youngsters of today have no idea how lucky they are just switching a set on and pushing a few buttons on the remote.

But my point in writing is that, whereas in the era above, TVs, radios and so on simply weren't available, or if they were, they were rather expensive and scarce. Therefore, there was a great demand and opportunity for various magazines to provide home built circuits and kits. A couple of friends built their own ham radio equipment and hand-wound all the coils etc. But in today's world, it would be totally uneconomic and pointless to build a TV etc, and in my opinion, this situation has killed much of the enjoyment for the self-build hobbyist, as there simply is no sensible economic sense in building almost anything unless it is unavailable on the market.

John Kendall, via email

Thank you John, I share your views, and my early history as an amateur partly shadowed yours, although it was mid-60s when I first became interested, getting my components from old TVs bought down the local street market. I too actually built a TV, using my home-built scope and some cannibalised sections from one of those scrap TVs – a nice green picture in the days of black and white!

These days we at EPE largely try to keep you all supplied with circuit designs for items which cannot be bought commercially.

SuperAb Farads?

Dear EPE,

Godfrey Manning's comment on the International Farad (Aug '04) appears to stem from improvements in measurement capability of the physical quantities involved, i.e. length, weight, time, temperature, etc. This has been an ongoing thing, and probably will still as measurements and technology improve but less frequently. In 1948 the "International" values gave way to "Absolute" units, a difference of 0.05% in which capacitance increased, while resistance and inductance were reduced. SI units appear to be based on this, refined to 0.04902%. (What next? SuperAbs? Ultimate?)

When I attended evening classes in winter 1934/35 we were taught two systems concurrently, c.g.s. and f.p.s., all very confusing. Later came M.K.S., the upgraded c.g.s. An incident at the time of the 1948 change was almost embarrassing. A batch of 0·1% resistors had been delivered from a reputable supplier for inclusion in equipment under construction and in order to check them at incoming goods stage I arranged with the Measurements Lab for an operator to use their precision bridge; they all passed.

When the next batch arrived, the same arrangement but with a different operator; several rejects. I was called and confirmed the findings, and cross-checked some of the original batch still in store; sure enough, some rejects. Calling in the first operator he said, "This is not the same bridge"! A word with the head of the Lab, "Oh yes! This has just been delivered, it's calibrated in Absolute Ohms. The old one was International Ohms". As were the resistors. (Unprintable expletives.)

The Admiralty Handbook, in use until well after the Second World War still rated capacitance in Jars. Presumably in deference to lower ratings' capacity of about six pints with a good head.

Des Mayes, Maidstone, Kent

Fascinating Des, and very confusing. Just as well, perhaps, that at the hobbyist level we don't need to concern ourselves too much with absolute values of components.

Batteries

Dear EPE.

Got a thing called Battery Genie G200 battery charger that is supposed to rejuvenate old 1.5V zinc batteries. Often you get a zinc or deac cell that shows an l.e.d. rapidly flashing which is supposed to denote a chuck-away situation. However if I pulse them rapidly about 10 times across a 12V motorbike battery, with a meter on the 10A range in series, they get about a 2A rapid pulse charge. Then this fancy magic genie computerised charger often accepts them as worth saving and usually, after about 10 hours, they are up to scratch. Any that refuse after a second dose of pulses or get a bit warm actually are for the scrapheap.

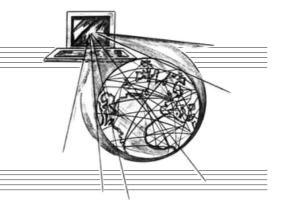
I wonder if anyone has experience of other dry battery chargers? There seems to be a complete lack of info in this area.

George Chatley, via email

Well, readers, what can you tell George?

SURFING THE INTERNET

NET WORK ALAN WINSTANLEY



N LAST month's *Net Work* the basic principles of Internet protocol (IP) addresses were outlined. The domain name system (DNS) translates human-readable domain names such as **epemag.co.uk** into a numerical IP address that identifies unique locations on a network, whether it's a web site, mail server or another type of network resource.

King of the Domains

Let's return to the topic of domain names themselves. In the UK, .co.uk domains are currently registered at a rate of approximately 90,000 per month (compared with just 3,000 per month in 1996). A .uk or a .com is known as a TLD or top level domain: they don't come any "higher" up in the pecking order of domain names. Below them are second-level domains (SLDs), such as the .co in .co.uk or the .org in .org.uk. And it's possible to split a domain down into sub-domains: the epemag in epemag.wimborne.co.uk is a sub-domain of the main wimborne.co.uk domain name.

The non-profit making body Nominet (www.nominet.org.uk) is responsible for managing the UK domain database. This is the definitive source for all UK domain name information, and you can search the database to check the ownership details of current domains, or search for the availability of a new domain.

Some users mistakenly search for evidence of a domain simply by looking for a web site associated with that domain. However, the fact that a web site is not launched in a browser does not necessarily mean that the domain has not been registered.

A search (often called a WHOIS lookup) at Nominet reveals basic details of domain name holders (where available) or *Registrants*. It should be mentioned that at no time is a Registrant the legal "owner" of a domain: being a Registrant merely entitles the holder to use the domain for as long as Nominet's fees continue to be met.

Rather than buy domains direct from Nominet, the cheapest way is for Registrants to pay their Internet Service Providers an annual fee, and the ISP will in turn settle Nominet's fees. Paying Nominet direct for a domain name is not only far more expensive but does not provide you with any web or email hosting service, for which you still need an ISP anyway.

Do-It-Yourself Domains

The remainder of this month's *Net Work* column is devoted to showing you how to buy a domain and set it up for your own use. We have chosen the popular UK domain seller **Easily.co.uk** because of the general ease of use of their system and the responsiveness of their technical support. Other domain name sellers include the mammoth German company Schlund/ 1&1 (www.one andone.co.uk) but in the writer's view, the 1&1 online control panel is much less user friendly and seems to be less suited for general internet users or beginners.

To help you buy and set up your own domain name, an Easily Search box has been added to the Net Work A-Z page of the *EPE* web site (**www.epemag.wimborne.co.uk/netwkaz.html**), which also contains hundreds of useful web site links. Simply type a domain name (without any ending – no .co.uk or .com etc.) and Easily's web site will search for availability.

If you find a name, it is simple to buy online using a credit card or send a cheque in the post. Select the required number of years – a .co.uk domain costs £9.99 for two years – and create a username and password to set up your own account. It really is as simple as that! Confirmations are sent by email, and Easily will send you a healthy number of reminder emails before the domain name is due to expire.

Having bought a domain name, what to do with it? At the very least, use *email forwarding* and *web forwarding* in Easily's control

panel to point email to your regular email and web addresses. These handy features let you create up to 20 usernames in the format of <any username you like>@your new domain.co.uk and point them to any external email addresses. Web forwarding lets you point to your domain name to a regular web site, providing you with a web site address of www.your new domain.co.uk.

For more advanced users, you can buy a mailbox account from Easily and allocate it to your domain name, which then provides a fully-fledged POP3 password protected mailbox. Web hosting can also be purchased from Easily, if you are looking for somewhere to host your web site. Note that the above procedures also apply to the purchase of *dot-com* domain names, and although it is prudent to shop around for best prices, the Easily system is amongst the simplest to use. Other sources of domains to look at include **ukreg.com**, **www.GoDaddy.com** and **www.register.com** or search Google. Now you can register your own domain name using any of these online services.

You can email your comments to alan@epemag.demon.co.uk.



Type a name into the Easily domain name box to search for availability or to buy online.



The coloured buttons in Easily's control panel are the key to navigation.



In the Control Panel, you can configure up to 20 email forwarding addresses for your new domain. Remember to "Save" the page before closing the window.

Hex Files Reopened: Record Viewer and Running Repairs

AST month we saw that although hex files appear to look just like a bunch of numbers – they are actually structured into a series of discrete records. The records depend on the format of the hex file, which in turn is produced with knowledge of the intended target, and it is the job of the device programmer to interpret them and make something happen.

Import Duties

Device programmers almost always include some capacity for importing and viewing hex files. When you import the file you usually get to see and/or edit the content as disassembled code, or perhaps more

likely hex opcodes, prior to programming the device – which can be useful if you are using a previously assembled project to which you don't have the source code.

Importing a hex file that contains configuration word and EEP-ROM data usually means that this information is set in the IDE also. *Toolkit TK3* doesn't go quite that far, but you can view the file as ASCII text with the View Hex button, and you can disassemble it too (Hex to MPASM).

There is no symbolic information stored in the hex file, so when a disassembler reconstructs the source code any CALL or GOTO statements will show the destination as absolute memory loca-

tions (or at least the lower eleven bits of them). Some software might assign an arbitrary label to this address to make reading the code less of a numbers game (*TK3* does this), but it's still not as clear as reading the original where, one hopes, the author has chosen something much more descriptive.

Exploring Alternatives

You almost never get to see the hex file in terms of its record structure, perhaps because it's not that useful unless you're actually interested in the mechanics of how the file is put together. So at the risk of producing something "not that useful", but nonetheless a bit different, the VB application that accompanies the column this month does just that.

The executable and full source code can be found in the usual place on the *EPE* website (access via **www.epemag.wim-borne.co.uk**, and within the PIC n' Mix folder).

Actually, I originally wrote this as a C++ shell extension that integrated into Windows Explorer, thinking it would be nice to show the used memory as a proportion of the capacity of the device, much like Explorer shows how much disk capacity remains. But then I found that the source contained more code to enable Explorer behaviour than it did to parse and present the hex, so it was promptly dropped.

C:\Epedisk5\WorldClock\WCLOCK450.HEX - EPE Hex File Viewer _ O X File Help Record Type Address Data Checksum_ Bytes 522 Data 16 2ADD 2B343934263454346F3472346F346E34 523 Data 16 2AE0 74346F342D3435342634563461346E34 86 524 Data 2AFO 63346E3475347634653472342D343834 30 16 26345634693465346E346E3461342B34 525 Data 16 2800 526 Data 16 2B10 3134263456346C346134643469347634 58 527 Data 16 2B20 6F34733474346F346B342B3431343034 40 528 Data 16 2830 26345734613472347334613477342834 2F 529 Data 16 2B40 31342634573461347334683469346E34 24 530 Data 16 2B50 673474346F346E342D34353426345734 3E 531 65346C346C3469346E34673474346F34 67 Data 16 2860 532 Data 16 2B70 6E342B3431342634573469346E346E34 29 533 Data 16 2880 69347034653467342D34363426345A34 10 534 Data 16 2890 61346734723465346234283431342634 2BAO 5A34753472346934633468342B343134 84 535 Data 16 536 4 2880 4B Data 26342334 537 Data 2 400F 313F 40 538 End O 0000 FF Split Classic

Fig.1. EPE Hex File Viewer showing an example hex file with a checksum error on record 536

There was another reason too – as VB code it fits more with *EPE*'s preferred standard and in particular allows the possibility of integration with *TK3* or your own programs. A command line option is included to make this task easier. (John Becker tells me that he will be integrating this code with next version of *TK3*, to be released in the Autumn.)

Checksum Corruption

The creatively titled "EPE Hex File Viewer" suddenly becomes very useful indeed if a file is discovered to contain errors. In fact while I was testing it, I "accidentally" discovered that MPLAB does not accept some hex files generated by an early version of *TK3*.

Problems like this are not trivial to track down without a little help, unless you have a good eye for numbers and too much spare time. A particular file runs to several hundred lines of hex digits that disassemble in *TK3* without any problems, and there's only so much staring at checksums you can do before everything starts to merge.

The screen dump in Fig.1 shows one such file opened in the hex file viewer, positioned to look at the end of the file. Using the application is simple enough, choose Open from the menu and select a hex file to view. The records within it are then parsed and each displayed as a single row of data,

split into columns that represent each field of the record. From the command line, you can issue "hexview [filename.hex]" to get the same result. Some of the columns implement "tooltips", which are the small pop-up windows with text offering additional information about a field over which the cursor is positioned. In screen order, the columns are:

Record: Useful indicator for showing the order of records as written to the file by the assembler or compiler.

Type: The record type, shown abbreviated. Tooltips give a full description.

Bytes: The number (in decimal) of bytes that appear in the data field.

Address: The load address of the data in hexadecimal. Tooltips show the same address in decimal in case you prefer things base 10.

To break the hex up a little, alternating colours are used to show where discontinuous data records appear. These give a feel for the memory organisation and show up instances where the author of the code has set the program origin to a new location, or where configuration word or EEPROM data has been embedded within the file.

Be aware that the programmer software decides how to fill memory locations on the device for which there is no object code. For some PICs it is recommended that a bulk erase be performed prior to programming, which will clear program memory locations to 0x3FFF. Some programmers, like Microchip's PicStart Plus, erase and program every location in a given range regardless, while others like TK3 program only the locations for which there is object code (which actually makes it faster by comparison).

Data: Data bytes for the record shown in hexadecimal character pairs. Tooltips show the same bytes as ASCII characters, if they are within the printable range. While this doesn't make a lot of sense for the majority of program memory (although you might be able to pick through tables that contain lots of retlw instructions, for instance, where the 8-bit literal is an ASCII character destined for an l.c.d. or similar), it is most useful for mining embedded EEPROM data. Bytes that are not printable are shown as space characters, "".

Checksum: Shows a tick or a cross, depending on success or failure of the checksum calculation respectively, followed by the value. In failure cases, the repair menu function is enabled which allows you to save the file with the corrected checksum value. (If you use this you'll be prompted with the "Save As" dialog in case you don't want to overwrite the original file.)

As a nod to tradition, the tab strip at the bottom of the screen lets you choose to look at your hex file in the classic Notepad style with which you might be more comfortable.

The screen dump in Fig.1 shows that the problem with the example hex file is the checksum on record 536. A little further digging reveals that the problem originates from a checksum generation error from an early version of TK3. Checking the "updates history" file for the latest TK3 showed that this problem was fixed in March '03.

This file error could be corrected either by re-assembling with the latest TK3, with the repair option from the file menu of the Hex File Viewer. Whichever way, the MPLAB IDE then import it.

It is possible that files from other sources might be reported by MPLAB as being corrupt for a variety of reasons, including general corruption of data on a disk. If so it is worth running the file through the Hex File Viewer.

Be aware, though, that if a checksum is shown to be incorrect, this may be because corruption exists in any one or more of the several other bytes in that line, not just in its original calculation. If this is the case, disassemble the hex file, through TK3 for instance, or through other programmers which have a disassembly function, and examine the disassembled file's commands, looking for anything unusual.



Only the I.c.d. graphics display module should give rise for concern when shopping for parts for the *Moon and Tide Clock Calendar* project. The author used a Powertip PG12864-F monochrome Supertwist (STN) graphics display module, with an on-board Toshiba T6963-based con-

As far as we are aware, this display module only appears on the RS Components listing, code 329-0329. It can be ordered direct (credit card only) from RS on & 01536 444079 or web rswww.com. The last time we looked, the cost of the display module was around £37 plus a post and handling charge. It is most likely that some of our components advertisers will be able to offer a suitable display at a more reasonable

For those readers unable to program their own PICs, a preprogrammed PIC16F877 microcontroller can be purchased from **Magenta Electronics** (**1283** 565435 or **www.magenta2000.co.uk**) for the inclusive price of £10 each (overseas add £1 p&p). The software is available on a 3-5in. PC-compatible disk (Disk 7) from the *EPE Editorial* Office for the sum of £3 (UK), to cover admin costs (for overseas charges see page 744). It is also available for Free download from the click-link option on the EPE home page at www.epemag.wimborne.co.uk (take path PICs/MoonTide).

The printed circuit board is available from the EPE PCB Service, code 467 (see page 744).

Volts Checker

We do not expect any buying problems to be encountered when purchasing components for the Volts Checker project. The author makes the point that the Zetex ZTX458 transistor is best suited for this circuit and should not be substituted by other devices unless they can match or exceed its specification.

As mains voltages could be involved during voltage checks, it is essential that a plastic case is used to house the printed circuit board with nylon nuts and bolts used for the fixings. The small printed circuit board is available from the EPE PCB Service, code 468 (see page 744).

EPE Theremin

Most of the components needed to build the EPE Theremin project should be readily available from our components advertisers. The circuit board will need to be cut to size from a larger piece of stripboard. The most common large size appears to be 36 or 39 copper strips and it is probably not worth trimming off the redundant strips.

The 1mH miniature, general purpose, radial lead inductor should be easy to find. However, if you do have problems try **Squires** (② 01243 842424 or www.squirestools.com) or Maplin (② 0870 264 6000 or www.maplin.co.uk).

The telescopic aerials used for the "sensors" are usually sold as replacements for portable radios. They are certainly listed by Squires (see above). Finally, invest in as large as possible plastic case that your "pocket money" will allow to keep the Volume and Pitch sensors apart.

Smart Karts - 1

Apart from the "decks" material and choice of motors, the rest of the components required to construct the Smart Kart buggy should be available from readers' usual components suppliers.

We understand that the author obtained his p.v.c. material for the decks from a local signwriter as "off-cuts". You could also try your local craft shop or DIY store. Apart from some of the companies mentioned in this month's Robots Special Supplement, such as Jaycar, a good selection of low-voltage d.c. motors is listed by Squires (참 01243 842424 or www.squirestools.com)

grammed PIC16F84 microcontroller can be purchased from Magenta Electronics (01283 565435 or www.magenta2000.co.uk) for the inclusive price of £5.90 each (overseas add £1 p&p). The software is available on a 3-5in. PC-compatible disk (Disk 7) from the EPE Editorial Office for the sum of £3 (UK), to cover admin costs (for overseas charges see page 744). It is also available for *Free* download from the click-link on the *EPE* website at **www.epemag.co.uk**, follow path PICs/SmartKart).

For those readers unable to program their own PICs, a prepro-

PLEASE TAKE NOTE

Teach-In Part 10

(Aug '04)
Page 570, Fig.10.14. The top ends of VR2/R17 should be connected to the line above the one shown. Diode D5 should be labelled D6, and D6 should be labelled D7. The p.c.b. layout is correct.

Water Safety Interlock (Sept '04 Ingenuity Unlimited) Page 614, Fig.2. A link needs to be made between the transformer/relay RLB contact connection and the pump/switch connection.

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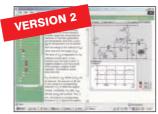


Logic Probe testing

Electronic Projects is split into two main sections: Building Electronic Projects contains comprehensive information about the components, tools and techniques used in developing projects from initial concept through to final circuit board production. Extensive use is made of video presentations showing soldering and construction techniques. The second section contains a set of ten projects for students to build, ranging from simple sensor circuits through to power amplifiers. A shareware version of Matrix's CADPACK schematic capture, circuit simulation and p.c.b. design software is included.

The projects on the CD-ROM are: Logic Probe; Light, Heat and Moisture Sensor; NE555 Timer; Egg Timer; Dice Machine; Bike Alarm; Stereo Mixer; Power Amplifier; Sound Activated Switch; Reaction Tester. Full parts lists, schematics and p.c.b. layouts are included on the CD-ROM.

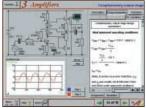
ELECTRONIC CIRCUITS & COMPONENTS V2.0



Circuit simulation screen

Provides an introduction to the principles and application of the most common types of electronic components and shows how they are used to form complete circuits. The virtual laboratories, worked examples and pre-designed circuits allow students to learn, experiment and check their understanding. Version 2 has been considerably expanded in almost every area following a review of major syllabuses (GCSE, GNVQ, A level and HNC). It also contains both European and American circuit symbols. Sections include: Fundamentals: units & multiples, electricity, electric circuits, alternating circuits. Passive Components: resistors, capacitors, inductors, transformers. Semiconductors: diodes, transistors, op.amps, logic gates. Passive Circuits. Active Circuits. The Parts Gallery will help students to recognise common electronic components and their corresponding symbols in circuit diagrams. Included in the Institutional Versions are multiple choice questions, exam style questions, fault finding virtual laboratories and investigations/worksheets

ANALOGUE ELECTRONICS



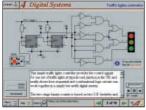
Complimentary output stage

Analogue Electronics is a complete learning resource for this most difficult branch of electronics. The CD-ROM includes a host of virtual laboratories, animations, diagrams, photographs and text as well as a SPICE electronic circuit simulator with over 50 pre-designed circuits. Sections on the CD-ROM include: **Fundamentals** – Analogue Signals (5

sections), Transistors (4 sections), Waveshaping Circuits (6 sections). Op.Amps - 17 sections covering everything from Symbols and Signal Connections to Differentiators. **Amplifiers** – Single Stage Amplifiers (8 sections), Multi-stage Amplifiers (3 sections). **Filters** – Passive Filters (10 sections), Phase Shifting Networks (4 sections), Active Filters (6 sections). **Oscillators** – 6 sections from Positive Feedback to Crystal Oscillators. **Systems** – 12 sections from Audio Pre-Amplifiers to 8-Bit ADC plus a gallery showing representative p.c.b. photos.

DIGITAL ELECTRONICS V2.0



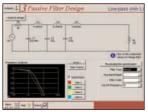


Virtual laboratory - Traffic Lights

Digital Electronics builds on the knowledge of logic gates covered in Electronic Circuits & Components (opposite), and takes users through the subject of digital electronics up to the operation and architecture of microprocessors. The virtual laboratories allow users to operate many circuits on screen. Covers binary and hexadecimal numbering systems, ASCII, basic logic gates,

monostable action and circuits, and bistables – including JK and D-type flip-flops. Multiple gate circuits, equivalent logic functions and specialised logic functions. Introduces sequential logic including clocks and clock circuitry, counters, binary coded decimal and shift registers. A/D and D/A converters, traffic light controllers memories and microprocessors – architecture, bus systems and their arithmetic logic units. Sections on Boolean Logic and Venn diagrams, displays and chip types have been expanded in Version 2 and new sections include shift registers, digital fault finding, programmable logic controllers, and microcontrollers and microprocessors. The Institutional versions now also include several types of assessment for supervisors, including worksheets, multiple choice tests, fault finding exercises and examination questions.

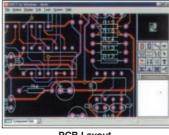
FILTERS



Filter synthesis

Filters is a complete course in designing active and passive filters that makes use of highly interactive virtual laboratories and simulations to explain how filters are designed. It is split into five chapters: Revision which provides underpinning knowledge required for those who need to design filters. **Filter Basics** which is a course in terminology and filter characterization, important classes of filter, filter order, filter impedance and impedance matching, and effects of different filter types. **Advanced Theory** which covers the use of filter tables, mathematics behind filter design, and an explanation of the design of active filters. **Passive** Filter Design which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, band-pass, and band-stop Bessel, Butterworth and Chebyshev ladder filters. Active Filter Design which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, band-pass, and band-stop Bessel, Butterworth and Chebyshev op.amp filters.

ELECTRONICS CAD PACK



PCB Layout

Electronics CADPACK allows users to design complex circuit schematics, to view design complex circuit animations using a unique SPICE-based simulation tool, and to design printed circuit boards. CADPACK is made up of three separate software modules. (These are restricted versions of the full Labcenter software.) ISIS Lite which provides full schematic drawing features including full control of drawing appearance, automatic wire routing, and over 6,000 parts. **PROSPICE Lite** (integrated into ISIS Lite) which uses unique animation to show the operation of any circuit with mouse-operated switches, pots. etc. The animation is compiled using a full mixed mode SPICE simulator. ARES Lite PCB layout software allows professional quality PCBs to be designed and includes advanced features such as 16-layer boards, SMT components, and an autorouter operating on user generated

ROBOTICS & MECHATRONICS



Case study of the Milford Instruments Spider

Robotics and Mechatronics is designed to enable hobbyists/students with little previous experience of electronics to design and build electromechanical systems. The CD-ROM deals with all aspects of robotics from the control systems used, the transducers available, motors/actuators and the circuits to drive them. Case study material (including the NASA Mars Rover, the Milford Spider and the Furby) is used to show how practical robotic systems are designed. The result is a highly stimulating resource that will make learning, and building robotics and mechatronic systems easier. The partititional versions have additional Institutional versions have additional worksheets and multiple choice questions.

- Interactive Virtual Laboratories
 Little previous knowledge required
- Mathematics is kept to a minimum and all calculations are explained
- Clear circuit simulations

PRICES

Prices for each of the CD-ROMs above are:

(Order form on third page)

Hobbyist/Student£45 inc VAT Institutional (Schools/HE/FE/Industry).....£99 plus VAT Institutional 10 user (Network Licence)£199 plus VAT Site Licence.....£499 plus VAT

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PICmicro TUTORIALS AND PROGRAMMING

HARDWARE

VERSION 2 PICmicro MCU DEVELOPMENT BOARD

Suitable for use with the three software packages listed below.

This flexible development board allows students to learn both how to program PICmicro microcontrollers as well as program a range of 8, 18, 28 and 40-pin devices. For experienced programmers all programming software is included in the PPP utility that comes with the development board. For those who want to learn, choose one or all of the packages below to use with the Development Board.

- Makes it easier to develop PICmicro projects
- Supports low cost Flash-programmable PICmicro devices
- Fully featured integrated displays 13 individual l.e.d.s, quad 7-segment display and alphanumeric l.c.d. display
- Supports PICmicro microcontrollers with A/D converters
- Fully protected expansion bus for project work
- All inputs and outputs available on screw terminal connectors for easy connection



£145 including VAT and postage 12V 500mA plug-top PSU (UK plug) £7 25-way 'D' type connecting cable £5

SOFTWARE

Suitable for use with the Development Board shown above.

ASSEMBLY FOR PICmicro V2 (Formerly PICtutor)

Assembly for PICmicro microcontrollers V2.0 (previously known as PICtutor) by John Becker contains a complete course in programming the PIC16F84 PICmicro microcontroller from Arizona Microchip. It starts with fundamental concepts and extends up to complex programs including watchdog timers, interrupts and sleep modes. The CD makes use of the latest simulation techniques which provide a superb tool for learning: the Virtual PICmicro microcontroller. This is a simulation tool that allows users to write and execute MPASM assembler code for the PIC16F84 microcontroller on-screen. Using this you can actually see what happens inside the PICmicro MCU as each instruction is executed which enhances understanding.

- Comprehensive instruction through 39 tutorial sections Includes Vlab, a Virtual PICmicro microcontroller: a fully functioning simulator Tests, exercises and projects covering a wide range of PICmicro MCU applications Includes MPLAB assembler
- Visual representation of a PICmicro showing architecture and functions Expert system for code entry helps first time users Shows data flow and fetch execute cycle and has challenges (washing machine, lift, crossroads etc.) Imports MPASM files.



Virtual PICmicro

'C' FOR PICmicro VERSION 2

The C for PICmicro microcontrollers CD-ROM is designed for students and professionals who need to learn how to program embedded microcontrollers in C. The CD contains a course as well as all the software tools needed to create Hex code for a wide range of PICmicro devices – including a full C compiler for a wide range of PICmicro devices.

Although the course focuses on the use of the PICmicro microcontrollers, this CD-ROM will provide a good grounding in C programming for any microcontroller.

● Complete course in C as well as C programming for PICmicro microcontrollers
● Highly interactive course ● Virtual C PICmicro improves understanding ● Includes a C compiler for a wide range of PICmicro devices ● Includes full Integrated Development Environment ● Includes MPLAB software ● Compatible with most PICmicro programmers ● Includes a compiler for all the PICmicro devices.



Minimum system requirements for these items: Pentium PC running Windows 98, NT, 2000, ME, XP; CD-ROM drive; 64MB RAM; 10MB hard disk space.

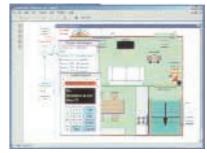
FLOWCODE FOR PICmicro

Flowcode is a very high level language programming system for PICmicro microcontrollers based on flowcharts. Flowcode allows you to design and simulate complex robotics and control systems in a matter of minutes.

Flowcode is a powerful language that uses macros to facilitate the control of complex devices like 7-segment displays, motor controllers and l.c.d. displays. The use of macros allows you to control these electronic devices without getting bogged down in understanding the programming involved.

Flowcode produces MPASM code which is compatible with virtually all PICmicro programmers. When used in conjunction with the Version 2 development board this provides a seamless solution that allows you to program chips in minutes.

● Requires no programming experience ● Allows complex PICmicro applications to be designed quickly ● Uses international standard flow chart symbols (ISO5807) ● Full on-screen simulation allows debugging and speeds up the development process ● Facilitates learning via a full suite of demonstration tutorials ● Produces ASM code for a range of 8, 18, 28 and 40-pin devices ● Institutional versions include virtual systems (burglar alarms, car parks etc.)



Burglar Alarm Simulation

PRICES

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(Order form on next page)

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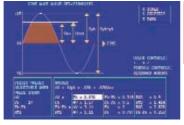
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£45 inc VAT

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Sine wave relationship values

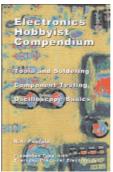
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A hands-on approach to electronics with numerous breadboard circuits to try out.

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FREE WITH EACH TEACH-IN CD-ROM - Electronics Hobbyist Compendium 80-page book by Robert Penfold. Covers Tools For The Job; Component Testing; Oscilloscope Basics

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ELECTRONICS IN CONTROLTwo colourful animated courses for students on one CD-ROM. These cover Key Stage 3 and GCSE syllabuses. **Key Stage 3:** A pictorial look at the Electronics section featuring animations and video clips. Provides an ideal introduction or revision guide, including multi-choice questions with feedback. **GCSE**: Aimed at the Electronics in many Design & Technology courses, it covers many sections of GCSE Electronics. Provides an ideal revision guide with Homework Questions on each chapter. Worked answers with an access code are provided on a special website.

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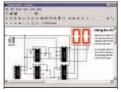
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VERSION 3

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pinouts, power supplies, decoupling etc. Single User £19.95 inc. VAT. Multiple User £34 plus VAT (UK and EU customers add VAT at 17.5% to "plus VAT" prices)

DIGITAL WORKS 3.0



Counter project

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Circuit Surgery

Alan Winstanley and Ian Bell

The identification of resistors and some essential safety advice are the topics of this month's column.

On Your marks

"I'm having difficulties with understanding resistors. Can you briefly explain the naming conventions to me? I've seen resistors that are called 100 ohm and 100R (which I assume are the same). I've also seen 1R2 and even 0R1.

I asked a supplier for some parts including a set of 5W 0RI resistors, and the ones I received are marked as follows:

ERG 58 ER R10 J 8450 Can you help me unravel these markings? Thanks from John Neal, Southampton."

On your first question, it is simply a printer's convention that decimal points in resistor values are denoted using the multiplier letter instead. This helps ensure that resistor values won't be misread due to, for example, a decimal point not printing very clearly on the paper.

The capital letter "R" is used for resistors under 1 kilohm. So 0·1 ohms is indeed the same as 0R1 and 100 ohms can be listed as 100R (which means 100·0 ohms). Next up the scale, a lower-case letter "k" is used for denoting kilohms, so that 2k2 is shorthand for 2·2 kilohms or 2,200 ohms, and of course 680k is 680 kilohms. Lastly, the upper-case letter "M" relates to megohms (millions of ohms), so a 2M2 resistor is 2,200,000 ohms and 10M is 10 megohms.

Similar customs are used to describe capacitor markings, with letters representing the decimal point location. You may see 2u2 to describe a 2·2µF (microFarads) capacitor, and the lower-case letter "n" for nanoFarads, so 100n is 100 nanoFarads. The letter "p" relates to picoFarads, so 4p7 is 4·7 picoFarads and 22p is shorthand for 22 picoFarads.

On the second question of resistor markings, it can take quite some practical experience to decipher some of the very cryptic markings seen on electronic components. The fun usually starts when you open a bag of freshly-delivered parts and try to compare them against what you actually ordered!

ERG is a resistor manufacturer (which I only know due to my constant flicking

through countless catalogues). The only other marking I recognise is the R10, which it is safe to assume is a 0R10 resistor or 0.1 ohms. The leading zero in the value has not been printed on the resistor.

More by elimination than anything, the 58 ER can't be a resistor value because it's not a preferred value (see Table 1). I guess the 8450 is a batch number, possible week 50 of the year it was made, an Orwellian 1984.

Table 1: E12 (grey values only) and E24 preferred values used in resistor, potentiometer and capacitor manufacture.

1.0	2.2	4.7
1.1	2.4	5⋅1
1.2	2.7	5.6
1.3	3.0	6.2
1.5	3.3	6.8
1.6	3.6	7.5
1.8	3.9	8.2
2.0	4.3	9.1

Preferred Values

Manufacturers produce resistors, potentiometers (variable resistors) and capacitors in ranges of "Preferred values", see Table 1. The most basic range is the so-called E12 set that divides the range 1-10 into 12 roughly equal steps. The popular E24 range consists of 24 preferred values, whilst the E48 and E96 ranges are seldom seen in hobby electronics, but would be of interest to commercial manufacturers. For instance, 47 is an E12 value, but would you really be fussy whether your circuit used a 47-5 or 48-7 E96 value?

Smaller capacitors (especially ceramic ones) can be printed with mad numerical codes that can defeat the finest of electronics minds, forcing users to retreat to the data library to decrypt the capacitor's hieroglyphics. Don't worry, it all comes together with practice. A.R.W.

More on SuperGlue

Following on from our safety item on Super Glue (July 2004 issue), an email was received as follows:

"I just purchased the July 2004 EPE and was reading the article Super Glued. I noticed you did not mention anything about the potential "fire hazard" of super glues when they come into contact with a porous and flammable material like cotton (e.g. cotton balls, swabs, clothing etc).

Due to the nature of how these super glues set when spread out very thin, which happens when they come into contact with a porous material that soaks up and distributes the glue so fast that it sets almost instantly, the "heat" produced can (and has been proven by me) cause the material to burst into flames if the conditions of adequate oxygen or oxidising agent(s) are present.

Just one or two standard drops on a "dry" cotton ball in low humidity (20% or less) can easily start a fire – but more often than not there will just be a bit of gaseous fumes and/or smoke. Still, I've learned this from experience when I accidentally spilled two grams of superglue on my cotton trousers one horrible day.

It's strange that all the "Data Sheets" on various super glues that I've read fail to mention this potential deadly problem – especially in the electronics industry where flammable solvents are often stored near such glues.

Lastly, you also didn't mention the dangers about super glue in the mouth – alas, as many of us tend to use our teeth to open a frozen/glued cover." (Anon.)

Although I worked in manufacturing industry for a long period I didn't hear of any incidents of Super Glue causing fires, and I am not aware that it has any particular exothermic properties (emitting heat during curing). Valuing the integrity of my workshop I have not attempted to run any experiments and I strongly recommend that Super Glue is not played around with.

The previous article gave some common sense advice (and some less well known

information) about handling tubes of cyanoacrylate adhesive. As for superglue in the mouth, well, I used to compile instructions (in eleven languages!) for consumer products, during which time I learned that some consumers really do leave their brains in neutral gear at operating time.

Needless to say (or is it?), putting tubes of glue in the mouth or near the eyes is a **MUST NOT**. In today's blame culture I can understand why manufacturers protect themselves by printing disclaimers on their products, as I have been there myself. (E.g. "This pie may be hot after heating.") *A.R.W.*

A Shocking State

We will round off this month's article with some more timely reminders on First Aid, this time relating to the risks related to electric shock. This has been prompted by the near tragedy that befell a neighbour of the writer, who this week suffered an entirely avoidable electric shock with nearfatal results. Although this column does not really cover "electrical" issues, some common sense reminders on first aid and safety matters are never wasted.

A friend started to rewire some electric lighting but failed to isolate the mains electrical supply (an example of an unthinking consumer, see above). This is even more startling because he was formerly an electrician in the armed forces.

He started to strip the electric cables and his wire strippers bit into the live (hot) copper conductor. The resulting shock caused convulsions and he was unable to release his grip on the wire strippers. It is solely because his wife was nearby, and she quickly pulled the plug out of the mains outlet, that he managed to escape with his life. There are some lessons to be learned by everybody, and so this article is dedicated to Stuart the ex-electrician who is very lucky to be alive.

It stands to reason that all mains supplies must be fully isolated before working on them. Skilled and qualified electricians will work happily on live mains fuseboxes, but as for the rest of us, complete electrical isolation is the name of the game.

This does not necessarily just mean switching off, because parts of the circuit (e.g. up to and including the fuse) could still be live. The first point is that working on your own means that there would be noone around to help you if anything went wrong, which is partly why in education it is forbidden to work with the mains supply by yourself if at all.

There are plenty of low cost voltage testers available that will warn you when supplies are live (such as Fluke's "Volt Alert" neon indicator from Maplin, code JJ18U, that doesn't even need to touch the mains wiring). These should be part of everyone's toolkit. Without fail, I use a mains-rated voltage probe to double check for live wires and I never, ever take anything for granted.

Deep Suspicion

If you want an example of why I view all mains wiring with deep suspicion, try this: after a major water leak and plumbing disaster in the author's kitchen, the whole lot had to be rebuilt from scratch. The "builders" also kindly reinstalled the

Table 2: Electric Shock Dangers

Current	Effect
1mA	Tingling sensation
9mA	Probably able to release the device
16mA	Borderline on ability to release the device
25mA	Probably unable to release the device
16-50mA	PAIN. Possible unconsciousness. Heart and respiratory functions probably continue.
> 100mA	Heart tremor (ventricular fibrillation). Respiratory paralysis. Asphyxia, suffocation. Severe shock and burns. Possible death .

dishwasher, which, one day, suddenly broke down. A piezo buzzer on the control panel warned of a fault condition. I turned off the wall switch but the buzzer kept on sounding; I removed the fuse to test it and, impossible to believe, the buzzer still kept bleeping.

Not relishing the idea of kneeling in a pool of water whilst tinkering with the electrical supply, it was then time to throw the master switch at the fuse board and isolate the whole building. It turned out that the "builders" had wired the dishwasher to the wrong terminals of the mains outlet, and had, in fact, wired it directly to the incoming ring mains terminals, thereby bypassing the fuse and switch altogether, so the whole appliance was still live even after I removed the fuse.

Nor do I trust the Live and Neutral wires to have been wired the correct way round either, ever since discovering a socket here in the Surgery that had a reverse polarity: it was permanently live. Simple neon testers are available that warn of mains socket wiring faults.

I too must admit to having had a nearmiss of my own making, while stood knee deep in a garden pool clearing some lily roots and weeds, I held a very tough piece of blackened root between the blades of my garden snippers — and several "ohnoseconds" later realised that I was about to slice through the mains cable feeding the electric pump *?!!

Out Of Control

Looking at the issues surrounding electric shock from the mains, the nature of alternating current means that you may lose control over the muscles. You may be unable to release your grip. The heart is also a muscle, and if this organ is in the path of the electric current then the heart-beat will be disrupted. Perversely, the heart is most susceptible to stimulation at a frequency of about 50Hz, which is the UK mains frequency. Furthermore, you may lose control over breathing functions and suffocate.

How much current flows through the body depends on a number of factors including skin surface moisture content, the route of the current through the body and what the victim was standing on (e.g. directly on earth or on a rubber mat); this latter point is why electrician's ladders are made of glass fibre. A smaller shock can still throw someone against a wall, causing an injury. Table 2 summarises the dangers of electric shock on human beings.

Additionally, burns may be caused that may run deeper in tissue than any visible outer burns might suggest, so this situation may be a medical emergency.

In the case of Stuart his wife had the presence of mind not to touch the victim or she

too would have received an electric shock. The correct action is to isolate the electrical supply, switch off or unplug, or use e.g. a dry wooden pole, wooden chair or other non-conductive material to reach over or push the victim clear of the live supply.

If the victim has stopped breathing, artificial respiration techniques should be used if you know how to apply the kiss of life. The latest advice on burns treatment is that pain and tissue damage can be reduced by cooling the area with plenty of clean water (apparently not using ice packs etc. which can damage nerves). Remove any item of a constrictive nature before swelling starts (e.g. watch straps, rings, boots etc.) and apply a sterile dressing.



An example of a typical plug-in RCD, with test button and warning neon on the front. The reset button is on the back.

Safety First

A major improvement in safety will be derived from using Residual Current Devices (RCDs) formerly known as Earth Leakage Circuit Breakers (ELCBs), or Ground Fault Circuit Interruptors (GFCIs) in the USA. These detect any imbalance between current flowing in and current returning. Any discrepancy is due to earth leakage, e.g. faulty insulation or an electrical fault, and beyond a certain value (typically 30mA), the RCD will trip and completely isolate the appliance from the mains. My first kitchen rewiring job involved adding an RCD mains outlet for the dishwasher.

These should not be confused with an ordinary miniature circuit breaker (MCB), which is nothing more than a resettable thermal fuse. Enjoy your hobby, use your special knowledge to encourage safe practice amongst your family and friends and most of all, stay safe. *A.R.W.*

Constructional Project

Smart Karts

Owen Bishop

Part 1 – Basic Construction Get Smart! – Build a PIC-based mobile buggy that's expandable to meet your needs

N this short series of articles we describe the construction of a mobile hobby robot, Smart Kart, and then proceed to variously equip it with a collection of sensors and actuators, each capable of a variety of tasks, some serious and some amusing. There are also discussions about the controlling software and how you can modify it to suit your own needs.

The basic Smart Kart has a pair of infrared sensors and a range of "bells and whistles" (actually, l.e.d.s and a bleeper) to give it character. They also indicate what it is going to do next.

Smart Kart comprises three decks (see photographs) joined vertically by spacer tubes. Construction is very simple, requiring few tools and does not involve high-precision engineering.

On the Move

The system of propulsion is a favourite one for small robots. The two drive wheels are separately driven by low-voltage d.c. electric motors. They each have a 1:288 gearbox to reduce the motor speed to a reasonable rate. The prototype has forward and reverse speeds of about 10cm/s, which is ideal for indoor operations. Balance is maintained by a castor at the rear of the vehicle.

In the basic Smart Kart, the motors are either switched off or run at full speed. Later in the series we introduce motor speed control when it suits the application. The simple on-off control used in the first application gives nine possible actions, that are combinations of *off*, *forward* and *reverse* for each motor.

The motors are switched by an H-bridge of transistors, controlled by a PIC16F84 microcontroller. The battery for the motors is a pack of four AA-type rechargeable lithium metal hydride cells. Each cell delivers a nominal 1·2V, giving a total of 4·8V.

The control circuit, sensors, bleeper and l.e.d.s are powered by an identical but separate battery. This is to avoid power-line glitches from the motors getting on to the sensor circuits and possibly interfering with the action of the PIC.

Getting Started

Before discussing further what the Kart can do, and how it does it, we first describe the physical construction of its mobile platform. Although we start off with the hardware, the electronic circuits and their construction also play a part in the overall assembly, and both aspects must be considered jointly.

It is best to build the robot one deck at a time, completing each deck with the mounted items, including the electronic circuit boards, before working on the next deck. We begin by describing the lower deck.

Smarty in line-follower mode. The robot pauses while deciding whether to turn left or right at a junction in the maze. This view shows the rear castor. The PIC is visible in the middle of the processor circuit board.





Building the Lower Deck

For the prototype, 3mm expanded PVC board was used for the decking, but the Smart Kart could instead be built from plywood, hardboard, or many of the other plastic-based boards that are readily available. Expanded PVC board is one of the best of these. It is light, rigid and easily worked. It can be cut with a craft knife, using a steel rule. It can be glued using PVA clear-drying adhesive.

It is slightly resilient so it is unnecessary to use locking washers when bolting parts together. Unlike expanded polystyrene, it does not crumble and has a smooth "solid" surface. Finally, it is self-coloured (red, blue, yellow, green, black, white) so does not need painting. It is fairly expensive by the sheet, as used by signwriters, but offcuts are usually available.

A cutting and drilling guide is given in Table 1 and Fig.1. Note that the exact number of holes and the positions for some of them depends on the sizes of items to be attached and what diameter bolts they require. Read each section's description thoroughly before marking out, cutting and drilling each board, as it may be necessary to modify the dimensions.

Avoid being tempted to make the overall size larger than shown – an over-large robot may be too heavy for the motors to drive and it may be difficult for the robot to manoeuvre in confined spaces (such as the average living-room or the average dining-room table).

The lower deck holds the motors with their gearboxes, the power switching board and the battery box that supplies the motors. The motors and/or the gearboxes will probably already have mounting holes drilled, usually for 2mm bolts. If not, springy clips are available for gripping small d.c. motors.

Mount the motors and gearboxes so that the axles are fairly close to the front edge of the deck. This helps to ensure that the sensors remain close to the path when the robot spins left or right.

Wheelies

A frequent problem is fixing the wheels securely to the axles, since very few wheels have grub-screws for this purpose. Plastic wheels were used in the prototype, 50mm

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HARDWARE ... YOU WILL NEED

PVC board cut and drilled as in Table 1 and Fig.1.

Spacers cut from plastic tubing (usually ABS), see text for lengths (6 off)

Bolts for spacers, with nuts, preferably not more than 5mm diameter, see text for lengths (6 off)

Bolts and nuts for sensors, M3, 30mm long (2 off)

PVC water-pipe coupling 27mm internal diameter, 32mm external diameter, length 42mm (2 off)

Gearboxes, high reduction ratio, often integral with motors (see text) (2 off)

Drive wheels, 5cm diameter, preferably with rubber tyres (2 off)

Castor, small furniture type, preferably with ball-bearing swivel

Assorted M2, M3, and M4 bolts and nuts Miniature stick-on cable clips, as required

Glue for upper deck panels



The Smart Kart, showing its three decks, large drive wheels and the pair of sensors.

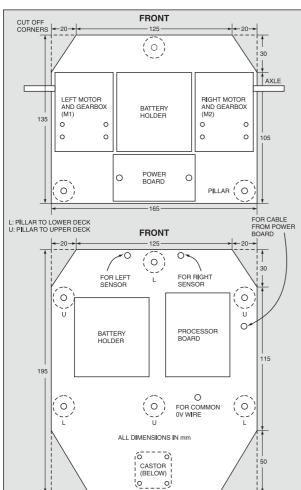


Fig.1. Cutting and drilling guide for the boards. Exact positioning of holes depends on the sizes of the items to be mounted.



Early stage in construction of the prototype shows the motors and gearboxes, with the power switching board.

Table 1: Cutting and Drilling Guide.

Piece	Dim. (mm)	Holes for	
Lower Deck	165 x 135 (see Fig.1)	separators (3), motors/gears, battery box, power board	
Middle Deck	165 x 195 (see Fig.1)	separators (6), battery box, processor board, castor, sensors (2), cable, com- mon 0V wire	
Sensors	60 x 40 (2 off)	bolt, wires, screwdriver	
Upper Deck – bottom	165 x 110	separators (3), slot for cable	
Upper Deck – top	165 x 110	beacon l.e.d., bleeper (mounting and connector)	
Upper Deck - front	165 x 25	headlight l.e.d.s (2)	
Upper Deck – rear	165 x 25	reversing and indicator l.e.d.s (2)	
Upper Deck - sides	104 x 25 (2 off)	indicator l.e.d.s (2)	
Upper Deck – tabs	31 x 20 (5 off)	_	

diameter, with 5mm diameter axle holes. Axles of small gearboxes are often 3mm in diameter. Cut a 10mm length of standard plastic aquarium aerator tubing to give a friction grip between axles and wheels.

First push the tubing on to the end of the axle; it should be a fairly tight fit. Then push the plastic-sheathed axle into the hole in the wheel. This too should be a tight fit. In many hours of testing, the wheels never came loose or dropped off. If you are not so lucky as to find compatible wheels, axles and tubing, you could try using lengths of PVC insulation stripped from mains cable.

One point to remember when mounting items on this deck is that bolts should generally be inserted in an upward direction, with their heads flush with the underside of the deck. This minimises the risk of bolts catching on the carpet or other soft surfaces as the robot moves along.

In the prototype, a battery box with a switch on its underside was used. Because of this, a slot had to be cut in the lower deck to allow access to the switch from below. However, if you are using a switchless box, you will need to drill a hole to mount a miniature low-voltage toggle switch. Probably this is best mounted toward the rear of the middle deck for easy access (see later).

Between Deck Spacers

A feature of this vehicle is that each deck is separated from adjacent decks by spacers, rather like those used for mounting circuit boards but considerably more robust. The boards are held together by long bolts which press the boards firmly against the ends of the spacers (see Fig.2).

The spacers used in the prototype are lengths of black ABS tubing 21mm in diameter with a 13mm bore. The tubing was cut from fittings used for garden/greenhouse reticulation (or irrigation) systems. These are cheap to buy and easy to cut with a junior hacksaw.

The length of the spacers between the lower and middle decks is determined by the dimensions of the castor (see photo below). It was felt preferable to use a really freely-turning castor that does not drag on the surface and divert the robot from its path. DIY stores stock a range of furniture castors with ball bearings and one of the smaller types was chosen. It functions perfectly.

First of all, work out the clearance between the underside of the lower deck and the running surface. This depends on the radius (r) of the drive wheels, the thickness of the board (t) and the height above the board of the axle centre (a):

Clearance = r - t - a

Allowing for protruding bolt-heads below the lower deck, it is advisable to have 10mm or more clearance for negotiating the edges of rugs and other hazards. Given the height (h) of the castor, the radius (r) of the drive wheel and the height of the axle (a), the required length (s) of each spacer is:

s = h - r + a

Before cutting the spacers, check that the distance between the decks is sufficient to allow you to insert and remove the cells of the motor power supply. If this is insufficient, consider mounting the castor on a block, or using a larger castor. It is important for the appearance and operation of the robot that the decks should be level.

Having determined the required length of the spacers, you need three long bolts with nuts (e.g. 50mm roofing bolts - DIY store again). Drill holes for the bolts at the centre front and the two rear corners. The distance from the edge and corners is a few millimetres greater than the external radius of the spacers. At the same time drill matching holes in the middle deck.



The upper deck at the stage when the l.e.d.s are being connected.

Fig.2. Connecting decks by spacers.

Building the Middle Deck

The middle deck carries the processor board, battery for the processor and its input and output circuits, the sensors (though these can be mounted elsewhere if you want to experiment), and the castor.

The castor is bolted at the rear end of the board. Although it normally trails behind as the vehicle moves forward, it must be free to turn to the opposite position when the vehicle is reversing. You may need to make the middle deck longer, to position the castor further back from the lower deck. The castor must turn freely through 360°.

the box. It is essential to drill the holes in the panels before gluing the panels together. The sides have pairs of 1mm holes through which the wires from the l.e.d.s pass. The bottom has three holes in it for the bolts of the separators. The bottom also has a slot cut in it to pass the 6-way socket from the Effects board out of the box, down to plug

circuit board, with the l.e.d.s and the bleep-

er mounted on the top and sides of the box. The box was made from rectangular panels

of PVC board glued together by their

edges. It consists of a shallow tray with a

drop-on lid, held in place by five rectangu-

lar tabs projecting 3mm above the top of

If the processor battery holder has a

built-in switch, you may need to cut a slot

in the deck, as with the motor battery hold-

er. Otherwise, mount a separate miniature

switch on the deck, alongside the switch for

the motor battery. Make sure that the bod-

ies of the switches do not obstruct the turn-

wires (4-cored cable and a common 0V

line) to come from the lower deck. The two

sensors are mounted at the front edge of the

middle deck, directed downward. Two

holes are needed for bolting the sensors to

lower deck by three separators. One is

close to the centre of the front edge and the

other two are at the rear corners of the

deck. Leave room for these when planning

the layout of the items on the deck, but you

There is scope for inventiveness in

designing and decorating the upper deck.

For the prototype, a shallow box (see below) was made to house the Effects

will not be able to fit them until later.

Building the Upper Deck

The middle deck is supported above the

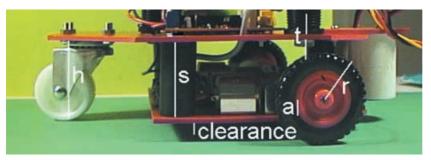
Two holes are needed in the deck for

ing of the castor.

the deck.

on to the processor board on the deck below. The upper deck is mounted on three spacers, placed at the centre of the rear edge and near the front two corners. Leave room for these when planning the middle deck. Drill three holes for these bolts in the middle deck, and three matching holes in the board that will eventually form the bottom of the upper deck.

The spacers can be any reasonable length, the main point to consider is to have enough room between the middle and upper decks to allow the PIC to be removed easily from its socket for



Showing how the dimensions of the castor determine the required length of the spacers between the lower and middle decks.

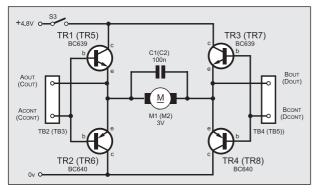


Fig.3. Schematic diagram of circuit for controlling one motor. A duplicate circuit is required for the other motor. Note capacitor C1 (C2) is connected directly across the motor M1 (M2).

programming, and then replaced. The battery cells must also be easily removable for charging.

However, the between-deck spacing may be limited by the length of bolts that you can buy. Long bolts are often quite hefty and add unwanted weight to the robot, so choose with care.

Power Switching Board

The first circuit to be built is a switching circuit to put the motors under the control of the PIC. The control circuit for each motor is a conventional H-bridge, in this case based on a four low power transistors, two npn, two pnp. The circuit diagram for this bridge is shown in Fig.3, and two copies of it are needed. This is reflected in its component numbering - those notations in brackets are for the second copy.

The circuit is operated by applying either a high voltage (4.8V) or a low voltage (0V) to terminals A and B. If A is made high and B is made low, TR1 and TR4 are switched on, while TR2 and TR3 are switched off. Current flows from the positive supply line, through TR1 (on), through the motor from left to right (in the figure) and through TR4 (on) to the 0V line.

If A is made low and B is made high, TR2 and TR3 are switched on, the other two transistors being turned off. Current flows through the motor from right to left (in Fig.3), so driving it in the opposite direction. If A and B are both high or both low, no current flows through the motor. The capacitor (C1) across the terminals of the motor filters out the worst spikes on the supply lines.

Construction

The stripboard component assembly layout for both copies of this circuit is shown in Fig.4. First cut out a rectangle of stripboard having the same number of strips and holes as indicated. Then cut the copper strips beneath the board where shown. Drill two 3mm mounting holes and drill matching holes in the lower deck.

Solder the wire links first, then the transistors and finally the 2-way terminal blocks. The +VE and 0V terminals are wired to the battery box (through a switch, either built-in or separate, as discussed earlier).

The terminals labelled A_{OUT} and B_{OUT} are connected to the left-hand motor, with A_{OUT} going to the brush on the left of the motor. Those labelled COUT and DOUT are connected to the right-hand motor, with

COUT going to the brush on the left of the motor. Ensure that these connections are the right way round so that the motors will turn in the intended directions. (The connections can be changed later necessary).

The other terminals, A_{CONT}, B_{CONT}, C_{CONT} and D_{CONT} are control inputs to the transistors. Eventually these will be connected to the PIC microcontroller. For the present, test the assembly by switching the on power, and using temporary leads to connect these terminals to the positive supply or to 0V for test purposes.

If the motors are wound with the same polarity as those used in the prototype and have the same gearbox, they will turn the drive wheels in the forward direction when A is made low and B high (or C low and D high). If the wheels turn in the reverse direction, swap the leads going from the terminal blocks to the motors.

When the power board has been tested, connect a 4-core light-weight cable to the control terminals. This should be about 20cm long to reach to the processor board, which will eventually be mounted on the middle deck. Note the colours of the wires to assist you when soldering their other ends to sockets later.

Connect the battery to the power terminals (+VE, 0V), carefully observing the correct polarity, inserting a switch in the +VE line if the battery box does not have one. The leads to the switch should be long enough to run to it when it is mounted on the middle deck. Finally, connect a wire to the 0V terminal to act as a common 0V line for the two power supplies. Another 0V connection will also run to the 0V terminal on the Processor board.

Make certain that the component wires are clipped short on the underside of the board. Secure the board to the lower deck, using 3mm nylon nuts and bolts. Use short spacers if you prefer, but remember that there is not much headroom between the lower and middle decks.

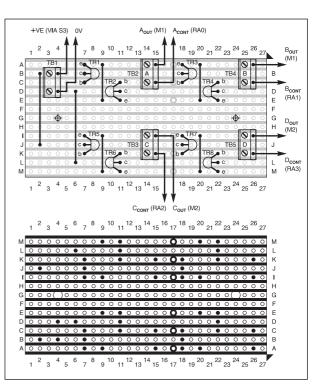
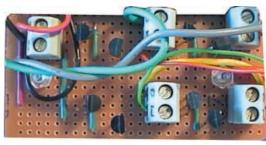


Fig.4. Stripboard layout of the Power Switching board.



Component layout on the completed Power Switching board.

COMPONENTS

POWER SWITCHING BOARD

Capacitors

C1, C2

100n polyester (2 off)

page

Semiconductors

TR1, TR3

TR5, TR7 BC639 npn transistor (4 off)

TR2, TR4,

TR6, TR8 BC640 pnp transistor

(4 off)

Miscellaneous

M1, M2 3V d.c. motor, with reduction gearboxes

(2 off)

S3 s.p.s.t. toggle switch

(option - see text)

Stripboard, size 13 strips x 27 holes; AA battery (4 off), plus holder; 2-way p.c.b. screw terminals (5 off); M3 nylon nut and bolt (2 off); wire for links and connections, solder, etc.

Approx. Cost Guidance Only

excl. batts & motors

Processor Circuit

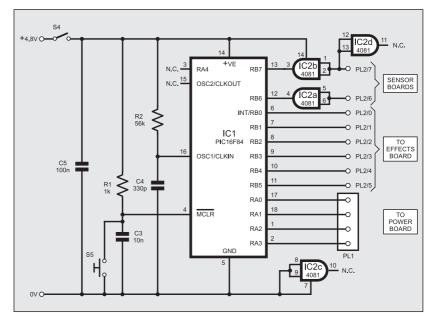
The Processor circuit is the heart of the system, and its circuit diagram is shown in Fig.5.

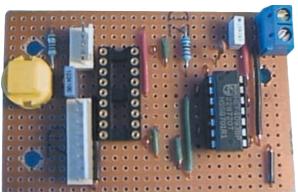
The circuit is based around a PIC16F84 microcontroller, IC1. Its operational speed is set by the values of resistor R2 and capacitor C4. It is held in normal running mode by resistor R1, with switch S5 providing a Reset control if needed. Capacitor C3 provides a slight delay between the Reset pulse and the PIC entering normal running mode.

PIC pins RB0 to RB5 and RA0 to RA3 supply output control signals to other aspects of the whole system. Control signals from those aspects are input to the PIC via AND gates IC2a and IC2b, respectively into PIC pins RB6 and RB7.

The 4.8V power supply is switched to the circuit by S4, with capacitor C5 providing power line decoupling.

The stripboard layout for this circuit is shown in Fig.6. Again cut out a rectangle of stripboard to the size indicated, and cut the





The processor board, without the PIC but showing its turnedpin socket.

Fig.5. Circuit diagram for the PIC microcontroller Processor stage of Smart Kart.

copper strips beneath the board where shown. Drill the three 3mm mounting holes and drill matching holes in the middle deck.

Assemble the board in order of link wires, i.c. sockets, and then in ascending order of size. The 18-pin i.c. socket should be a turned-pin type for easy removal and replacement of the PIC. Do not insert the PIC into its socket at this stage. Wait until after all the connections have been tested. A completed assembly, but without the PIC, is shown in the photograph on the left.

The +VE and 0V terminals are wired to the battery box through a switch, either built-in or separate, as previously discussed.



PROCESSOR BOARD

Resistors

R1 1k R2 56k

Both 0.25W 5% tolerance or better.

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Capacitors

C3 10n polyester, 5mm pitch C4 330p ceramic, 5mm pitch C5 100n polyester, 5mm pitch

IC1 PIC16F84 microcontroller, pre-programmed

IC2 4081 quad 2-input AND gate

Semiconductors

Miscellaneous

S4 s.p.s.t. min. toggle switch (option – see text) S5 s.p.s.t. snap-action pushswitch, p.c.b. mounting PL1 4-way locking and polarising header connector, with matching socket PL2 8-way locking and polarising header connector, with 2-way and 6-way sockets

Stripboard 47mm x 73mm (18 strips x 28 holes); 2-way p.c.b. screw terminal; 14-pin d.i.l. socket; 18-pin d.i.l. socket; AA battery (4 off), plus holder; wire for links and connections; solder, etc.

Approx. Cost **Guidance Only**

excl. batts

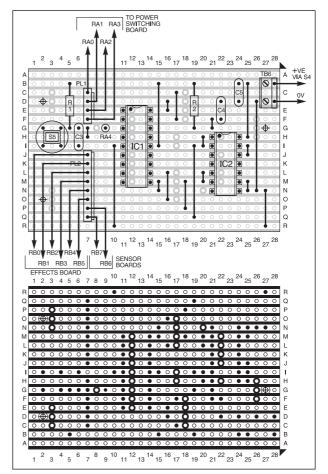


Fig.6. Stripboard layout of the processor board.

Table 2: Processor board connections, Plug PL1 (Port A)

Port name	Pin no	1/0	Connected to
RA0	17	Output	Power board, terminal A, left motor
RA1	18	Output	Power board, terminal B, left motor
RA2	1	Output	Power board, terminal C, right motor
RA3	2	Output	Power board, terminal D, right motor

Pin RA4 is not used, and is unconnected, so it is available for experimental use but can be left unused.

Table 3: Processor board connections, Plug PL2 (Port B)

Port name	Pin no.	1/0	Connected to	
RB0	6	Output	Headlamp I.e.d.s	
RB1	7	Output	Reversing I.e.d. and bleeper	
RB2	8	Output	Left indicator l.e.d.	
RB3	9	Output	Right indicator l.e.d.	
RB4	10	Input	Behaviour mode select S1	
RB5	11	Input	Behaviour mode select S2	
RB6	12	Input	Infra-red sensor, right	
RB7	13	Input	Infra-red sensor, left	

Connections to the board are through 0·1in. (2·5mm) pitch pin-plugs and sockets, with a 2-way terminal block for the power supply. The board can be completely disconnected should you want to take it out and change its wiring.

Plug PL1 carries the wires from the power switching board to PIC Port A. Solder a 4-way socket to the 4-way cable and plug this on to PL1. Plug PL2 is an 8-way connector but there are two sockets mating with it. A 2-way and a 6-way socket will usually fit neatly onto PL2, but first you may need to pare away one of the polarity guides on each socket. Ignore PL2 until the sensors and effects boards have been built.

The plugs are connected as indicated in Table 2 and Table 3.

Sensors

There are two sensors, each comprising an infra-red (IR) l.e.d. and an infra-red photodiode. The l.e.d. is directed down onto the surface over which the vehicle is travelling. The photodiode receives the reflected radiation. The sensor gives a low logic output when the surface is black or dark-coloured, and a high output when the surface is white or light- coloured. The reason for using infra-red is to avoid interference from ambient light. No problems were found when operating in bright indoor daylight, or in low-level incandescent illumination.

The sensors are used to detect lines painted in black on a white background. Theoretically, it is possible to operate the sensors in the reverse direction, by changing the program so that they respond to white lines on a black background. This was the original intention, but an unexpected problem was encountered – some black cardboard is not black, or at least, it is not black in infra-red light.

Two different batches of black card were tried, purchased from the same stationers. They both looked equally black to the eye; one did not reflect IR, but the other reflected it very strongly. The robot was unable to detect a white strip on the reflecting black background. So it was decided to use white (or light-coloured) fluorescent or metallic card as the background and paint the paths

on it, using acrylic paint (black or Payne's Grey).

Sensor Circuit

The circuit diagram for one IR Sensor is shown in Fig.7 (the bracketed numbers refer to the second channel). IR l.e.d. D1 has series resistor R3 limiting the current to 40mA. This is near the maximum for the l.e.d., producing a high level of illumination. The radiation reflected back from the surface (Fig.8) is detected by IR photodiode D2. This is reverse-biased so only a weak leakage current flows.

The current passes through a high resistance (R4 plus preset potentiometer

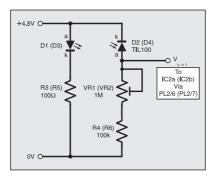


Fig.7. The circuit of one sensor.

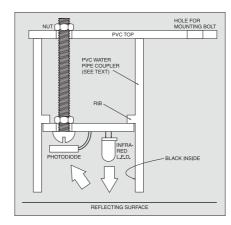


Fig.8. A sensor in vertical section.

VR1) generating a potential difference (p.d.) of a few volts. VR1 is adjusted so that the voltage at V_{out} is well above half-supply voltage (2·4V) when D2 is receiving IR reflected back from a light-coloured surface, but is well below half-supply voltage when reflection is from a dark surface.

To give a response which differentiates more strongly between light and dark, the voltage at V_{out} is fed to the PIC via one of the two CMOS AND gates (IC2a, IC2b) in Fig.5. The gate's output swings strongly between +VE and 0V, so presenting a more rapidly changing, clear-cut signal to the PIC.

Sensor Housing

Each sensor of the prototype is housed in a standard PVC waterpipe coupler, having an external diameter of 32mm (27mm internal). This has an internal rib half-way along it (Fig.8). The circuit board is made circular to fit the pipe, and is held (strip-side uppermost) pressed against the rib by a bolt that runs through the board and up through the plastic top of the sensor.

The top is a small rectangle of expanded PVC (as used for the decks). This has holes bored in it for the bolt, screwdriver access, the cable, and for bolting the sensor to the middle deck. If the pipe you are using has no rib, glue three small rectangles of PVC board inside the pipe at the right level.

COMPONENTS

SENSORS

(Two required)

 $\begin{array}{ccc} \textbf{Resistors} & & \\ \text{R3, R5} & & 100\Omega \ (2 \ \text{off}) \\ \text{R4, R6} & & 100k \ (2 \ \text{off}) \\ \text{All 0.25W 5\% tolerance} & & \\ \end{array}$

TALK page

See

Potentiometers

or better

VR1, VR2 1M, min. preset, horiz (2 off)

Semiconductors

D1, D3 5mm infra-red l.e.d. (2 off)
D2, D4 TIL100 or similar
photodiode (2 off)

Miscellaneous

Stripboard, circular 26.5 diameter (see text) (2 off); Wire for links and connections; solder etc.



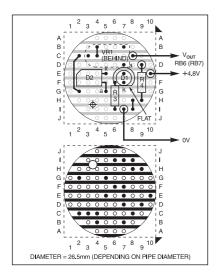


Fig.9. Stripboard layout of one of the sensors.

Sensor - Construction

The stripboard layout for the Sensor board is shown in Fig.9. You need to build two copies of it. For each copy, first cut a square of stripboard to the right size, and then very carefully, using a small hacksaw plus a file, trim it to a circular shape that will just fit inside the waterpipe coupler.

Drill a hole for the bolt, then assemble the board in order of component size. Note that preset VR1 should be mounted on the trackside of the board. Ensure that the diodes are correctly orientated. The leads on IR sensor D2 must be long enough to bend back, to make its sensitive side face away from the bolt (towards the floor when in use), see photograph.

Push the bolt up through the hole in the circuit board and secure it with a nut. Prepare the top PVC board (refer back to Table 1) with holes drilled to align with VR1 and the bolt, and for the three leads.

Thread the leads through the pipe from below and then through the hole in the top board. Press the board up against the internal rib, then push the end of the bolt through the hole in the top board and secure it with a nut. Before you tighten the nut, make sure that the screwdriver access hole is directly above VR1.

A slip of black card that shields D2 against direct light from D1 can be seen in the above photo. However, this has minimal effect and can be omitted, as the beam from D1 is fairly directional. The inside of the lower half of the pipe can be painted matt black, or lined with a collar of black paper to cut out reflection from the walls of the pipe. This is important to minimise interference from ambient light.

Testing

Test the sensors at this stage. Connect the power leads to a 4-8V supply and the output lead to a voltmeter. Hold the sensor with the tube vertical, pointing down on to a white surface about 15mm below the lower end of the pipe. Switch off any bench-lamps and nearby bright filament lamps. Adjust VR1 until the voltage swings high (nearly to 4-8V). Point the sensor at a black surface; the voltage should drop to a few hundred millivolts. Repeat until a clear response is obtained.



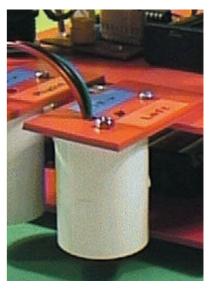
A sensor board, as seen from the side.

If there are any particular surfaces on which you intend to run the Smart Kart, now is a good chance to test them for IR reflectivity. You will need a white or light background surface, producing a sensor output of 3V or more. You also need a dark surface (such as a wide, black strip painted on white card) for the guide-lines, producing an output of 1V or less.

The sensors are each connected to the processor board by three wires. Their +VE and 0V wires go to the screw terminals on the board. The output wires are fitted with a 2-way socket for plugging onto PL2 pins 6 and 7 (right and left respectively). In the prototype, these wires are about 30cm long to allow for the sensors to be mounted on other parts of the vehicle. You could make them shorter if you intend to mount them only at the front.

Effects Board

The Effects board holds the logic and switching circuits for the l.e.d.s and bleeper, whose circuit diagram is shown Fig.10. Switching is controlled by the PIC via a mixture of NAND gates and transistors.



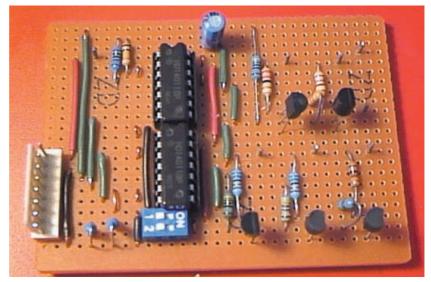
Plastic "shroud" around the sensor board.

The headlamps (l.e.d.s D5 and D6) are controlled by PIC pin RB0 via *npn* transistor TR9, a high level from RB0 turning them on. Resistor R7 limits the current flow through the l.e.d.s.

The yellow beacon l.e.d. (D12) on the lid is flashed on and off via transistor TR13, which is controlled by an astable based on NAND gates IC3a and IC3b. This l.e.d. flashes all the time, indicating that the power to the middle and upper decks is switched on.

PIC pins RB1 to RB3 are fed to NAND gates IC3c, IC4a and IC4b. These control transistors TR10 to TR12, which in turn control l.e.d.s D7 to D11 and bleeper WD1. The gates are also controlled by the astable, only allowing the PIC's control signal to pass through when the astable's output from IC3a pin 3 is high, with a resulting intermittent flashing of the selected l.e.d., or the sounding of the bleeper.

Switches S1 and S2 select the robot's behaviour mode, via PIC pins RB4 and RB5. These are used in input mode and are held normally-low by pull-down resistors R17 and R18, going high when the respective switch is on.



Completed Effects board showing component layout and the two d.i.l. switches S1 and S2

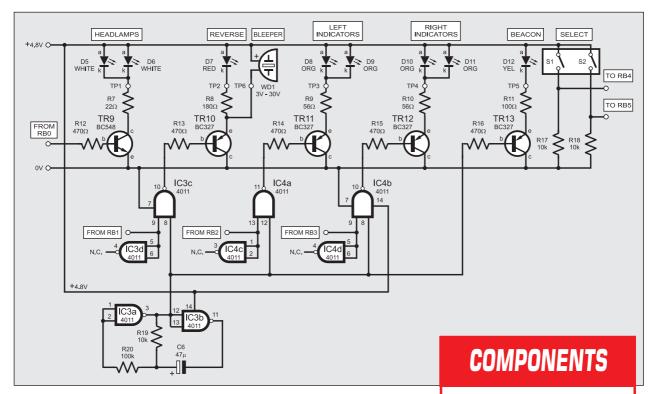


Fig.10. Circuit diagram for the Effects board.

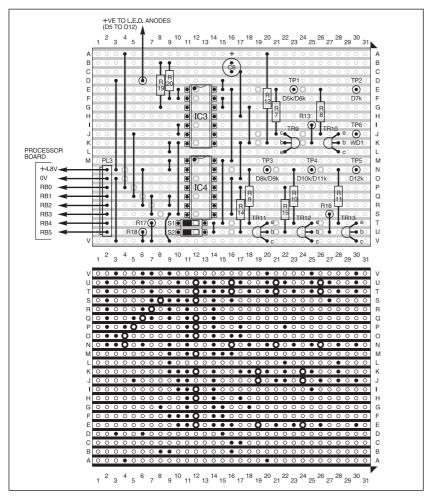


Fig.11. Stripboard layout of the Effects board.

EFFECTS BOARD

Resistors		See
R7	22Ω	SHOP
R8	180Ω	TALK
R9, R10	56Ω (2 off)	TALK
R11	100Ω ´	page
R12 to R16	470Ω (5 off)	
R17 to R19	10k (3 off)	
R20	100k	
All 0.25 W 5%	tolerance or	hetter

Capacitor

 47μ radial elect. 10V C₆

Semiconductors D5. D6

white l.e.d., high brightness, 10mm (2 off) red I.e.d., standard brightness, 5mm orange I.e.d., standard brightness, 5mm (4 off) D8 to D11 D12 yellow I.e.d., standard brightness, 10mm IC3, IC4 4011 quad 2-input NAND gate (2 off) TR9 BC548 npn transistor TR10 to TR13 BC327 pnp transistor

(4 off)

Miscellaneous S1, S2 dual d.i.l. switch (1 off) PL3 8-way locking and polarising header connector, with matching socket, 0.1in (2.5mm) pitch WD1 piezo-buzzer, miniature

Stripboard 56mm x 80mm (22 strips x 31 holes); 14-pin d.i.l. socket (2 off); 1mm terminal pins (7 off); wire for links and connections; solder etc.

Approx. Cost Guidance Only

Effects Board - Construction

The stripboard layout for the Effects board is shown in Fig.11. Cut the board to size and cut the strips where indicated. Assemble in the usual order, taking care to orientate the transistors correctly (note that the flat side of TR9 faces left, but the others face right). Connections from the board to PIC pins RB0 to RB5, and to the power supply, are made via connector PL3.

A 2-pole d.i.l. (dual-in-line) switch was used for S1 and S2 in the prototype. However, if you prefer, you could insert terminal pins at stripboard holes T10, U10, T13 and U13, and run wires from these to a pair of low-voltage toggle switches mounted on the side or top of the deck.

As shown in Fig.10, the anodes of the l.e.d.s and the positive terminal of the bleeper are all connected to the +VE supply line. In practice, you can run a bare wire around the inside of the deck, connecting all the anodes to it via flying leads, as can be seen in the accompanying photograph.



Running a common l.e.d. anode wire around the upper deck side panel.

The anode wire of an l.e.d. is cut short and bent around a bare wire before the joint is soldered. The bare wire is then connected to the board at *D3*. The cathode wires are connected individually to the terminal pins (TP1 to TP5) marked on Fig.11.

The two power lines to the board are soldered to PL3 as indicated in Fig.11, and pass down through the slot in the bottom of the box, to connect to the terminal block on the processor board. The OV line completes the common connection between the OV lines on all five circuit boards.

A 6-way cable about 20cm long, and with suitable sockets at each end, connects between plug PL3 and PL2 on the processor board, passing down through the allocated slot.

Once the board and its wiring have been completed and thoroughly checked, it can be fixed on the bottom of the box, using Blu-Tack

Wiring Check

You should now check the Processor board, Effects boards and the sensors, but without the PIC or the logic gates in their sockets

Interconnect the boards and sensors by their cables. Connect the negative probe of a testmeter to the 0V line at any convenient point. Switch the meter to its continuity checking function. With the power disconnected, touch the positive probe to as many as possible of the points that should normally be at 0V (refer to the appropriate figures), such as the 0V screw terminals on all boards, exposed wire links, pin 7 of each logic i.c. socket and pin 5 of the PIC's socket. There should be continuity throughout the circuits.

Connect the negative probe to the +VE line and check all points that are normally at a positive voltage, including pin 14 of each logic i.c. socket and pin 14 of the PIC's socket. There should be continuity throughout.

Reconnect the negative probe to the 0V line, switch on the processor board battery and use the positive probe to confirm that a voltage of 4.8V appears at all expected points.

Connect flying leads to the power lines and check the action of applying one or the other of these to the pins of the PIC's socket:

Pins 1, 2, 17, 18: Control the motors, as previously described (with motor power on)

Pin 6: Headlamp l.e.d.s come on when made high

Pin 7: Reverse l.e.d. flashes and bleeper sounds intermittently when made high

Pin 8: Left indicator l.e.d.s flash when made high

Pin 9: Right indicator l.e.d.s flash when made high

The beacon l.e.d. flashes all the time

With the meter probes to 0V and the positive probe to the following PIC socket pins, measure these voltages:

Pin 4: Normally high; goes low for as long as the Reset button is pressed

Pin 10: Low when S1 is off; high when it is on

Pin 11: Low when S2 is off; high when it is on

Pin 12: High when the right sensor points at white; low when it points at black

Pin 13: High when the left sensor points at white; low when it points at black

If you have a suitable meter or an oscilloscope, measure the signal frequency at pin 16 of the PIC socket. It should be in the region of 14kHz.

If the circuit passes all of the above checks, you are ready to insert a pre-programmed PIC and begin operating it in its four different modes. We shall look at this in Part 2 next month.

RESOURCES

Software, including source code files, for the Smart Kart Part 1 is available on 3·5-inch disk from the Editorial office (a small handling charge applies – see the *EPE PCB Service* page). It can also be downloaded *free* from the *EPE* Downloads page, accessible via the home page at **www.epemag.wimborne.co.uk**. It is held in the PICs folder, under SmartKart. Download all the files within that folder.



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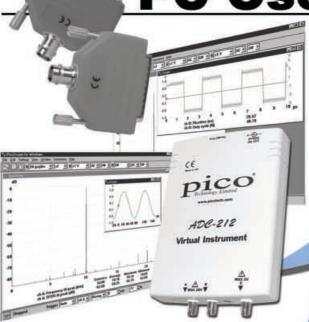
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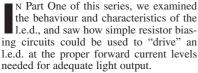
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Light Emitting Diodes – Operation and Applications

Anthony H. Smith, BSc. (Hons)

Part 2 – Biasing and Interfacing



This month, we demonstrate the operation of other, more specialised, biasing circuits, looking at the best ways to interface l.e.d.s with different logic families, and showing how logic circuits can be developed both for flashing an l.e.d. and as an efficient way of varying the brightness.

Constant Current Biasing

We showed last month how a series resistor may be used as a simple means to set the forward current, I_F , through an l.e.d. Although effective, the technique depends on a stable voltage source; any changes in voltage will produce corresponding changes in I_F which can result in significant variations in light intensity.

Applications that suffer from wide changes in voltage, such as unregulated d.c. supplies and battery packs, usually require some kind of *constant current biasing* if the l.e.d.'s brightness is to remain fixed. A simple and yet remarkably effective technique is shown in Fig.1, where transistor TR1, an *n*-channel JFET (junction field effect transistor), maintains a constant current through the l.e.d.

To understand how the JFET regulates the current, consider the drain current equation for a JFET:

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P} \right)^2 \tag{A}$$

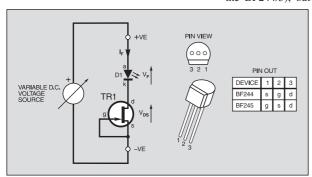


Fig.1. JFET constant current biasing.

where I_D is the drain current (= I_F in this case), I_{DSS} is the *drain source saturation current*, V_{GS} is the *gate-source voltage*, and V_P is the *pinch-off voltage*, also known as the *gate source cut-off voltage*. In this circuit, the gate is connected directly to the source such that $V_{GS} = 0$, and so the equation reduces to $I_D = I_{DSS}$. Since I_{DSS} is a constant for a given JFET, the drain current (the l.e.d.'s forward current) is also held constant.

A good choice for TR1 is a device from the BF244 or BF245 range of JFETs. These parts have essentially the same electrical characteristics but different pinouts, as also shown in Fig.1. They are grouped into different classes according to the spread in I_{DSS} : the BF244/5A has $I_{DSS}=2$ to $6\cdot5mA;$ the BF244/5B has $I_{DSS}=6$ to 15mA; and the BF244/5C has $I_{DSS}=12$ to 25mA.

Despite its obvious simplicity, the circuit can regulate the l.e.d. current to within about $\pm 10\%$ with supply variations of more than 20V. However, it is not without drawbacks. For example, the minimum working voltage is given by $V_F + V_{DS(min)}$, where V_F is the l.e.d.'s forward voltage and $V_{DS(min)}$ is the lowest value of drain-source voltage at which the JFET remains in its "active" region.

If V_{DS} falls too low, the device enters its "saturation" or "linear" region in which it can no longer maintain I_D equal to I_{DSS} . Typically, this means the supply voltage must be at least 6V, or so, in order to regulate the forward current in a red l.e.d.

The *maximum* working voltage depends not only on the JFET's maximum drain-source voltage, $V_{\rm DS(max)}$ (which is 30V for the BF244/5), but also on the maximum

allowable power dissipation, $P_{D(max)}$. The BF244 and BF245 have $P_{D(max)} = 360 \text{mW}$ at 25°C, and so if I_D is, say, 15mA, V_{DS} must not be allowed to exceed 24V.

Bipolar Alternative

A slightly more complex constant current circuit is shown in Fig.2, in which an



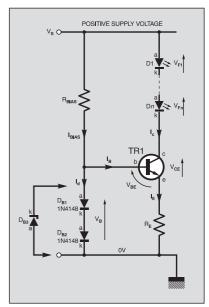


Fig.2. Bipolar transistor current sink capable of driving several l.e.d.s.

npn bipolar transistor, TR1, is used to regulate the l.e.d.s' forward current. Forward-biased diodes $D_{\rm B1}$ and $D_{\rm B2}$ furnish a stable voltage, $V_{\rm B}$, at the base of TR1, such that the voltage, $V_{\rm E}$, across emitter resistor $R_{\rm E}$ is given by $V_{\rm E} = V_{\rm B} - V_{\rm BE}$, where $V_{\rm BE}$ is TR1's base-emitter voltage.

Provided TR1 has large current gain, we may ignore base current I_B and assume that collector current I_C equals emitter current I_E , which itself is given by $(V_B - V_{BE})/R_E$.

When examining a circuit, it's often useful to make *reasonable* assumptions in order to simplify the analysis. In this case, since the 1N4148 signal diodes, D_{B1} and D_{B2} , are biased by the same current, I_D , we may assume that the voltage drop across each of them is the same, and denote the voltage V_D . Furthermore, since $V_B = 2V_D$, it follows that $I_C = (2V_D - V_{BE})/R_E$.

Now, provided the diodes are at the same temperature as TR1, it is also fair to assume that $V_{\rm D}$ is approximately (but not exactly) equal to $V_{\rm BE}$. Therefore, the expression for the collector current simplifies to:

 $I_C = 1.e.d.s$ ' forward current = $V_D / R_E(A)$

For example, let's say we wish to drive the l.e.d.s at 15mA. At normal room temperature, V_D will be roughly 600mV, and so $R_E (= V_D / I_C) = 600 \text{mV} / 15 \text{mA} = 40 \Omega$. We would make $R_E = 39 \Omega$, this being the nearest preferred value. Provided V_D and V_{BE} remain constant, the current flowing through the l.e.d.s will also remain reasonably stable despite wide variations in supply voltage, V_S . If the supply voltage itself is large enough, the circuit can support a large number of series-connected l.e.d.s sharing the same forward current. The main requirement is that:

$$V_S > V_{F1} + V_{F2} + ... + V_{Fn} + V_{CE} + V_E$$

where V_{F1} is the forward voltage of l.e.d. D1, V_{F2} is the forward voltage of l.e.d. D2, and so on. The transistor's collector-emitter voltage, V_{CE} , should be at least 1V to ensure the device does not saturate. If all the l.e.d.s are of the same type, we may assume they have the same forward voltage, V_F , such that $V_S > nV_F + V_{CE} + V_E$ where n is the number of l.e.d.s. Resistor R_{BIAS} establishes a current, I_{BIAS} , which supplies the diode current, I_D , and base current, I_B . The required resistor value is given by $R_{BIAS} = (V_{S(min)} - 2V_D) / I_{BIAS}$, where $V_{S(min)}$ is the lowest voltage to which V_S can fall.

Normally, a value of around 1mA will be suitable for I_{BIAS} , although lower values, say a few hundred microamperes, may be acceptable provided TR1 has high current gain (i.e., large β or h_{FE}). The circuit provides reasonable regulation of the l.e.d.

Test Example

For example, in a test circuit built with $R_{BIAS}=3.6k\Omega$, $R_E=39\Omega$, TR1=BC550B, and with a single red l.e.d., the collector current was 12.9mA at $V_S=5.0V$, rising to 17.0mA at $V_S=15.0V$, equivalent to a 32% change in l.e.d. current with a 200% increase in supply voltage.

Current regulation is not as good as the JFET circuit in Fig.1, but the bipolar circuit has the advantage of allowing the nominal l.e.d. current to be set to any desired value by appropriate choice of $R_{\rm E}$. Since $V_{\rm E}$ is of the same magnitude as $V_{\rm BE}$, any changes in the latter (for example, caused by temperature variations or part to part differences) can have a marked effect on l.e.d. current. This drawback can be corrected to some extent by replacing the two diodes ($D_{\rm B1}$ and $D_{\rm B2}$), with a single Zener diode, $D_{\rm B3}$, such that:

$$I_{C} = (V_{Z} - V_{BE}) / R_{E} (A)$$

Provided the Zener voltage, V_z , is at least, say, 3.6V, any changes in V_{BE} will have negligible effect on the l.e.d. current. However, there is a price to be paid for this improvement. Since V_E , the voltage across R_E , is now much larger, $V_{S(min)}$ must also be larger to support this voltage, and there is now more power wasted in R_E . Also, I_{BIAS} should be at least 5mA to ensure proper biasing of Zener diode D_{B3} , again wasting power.

We'll show in a later article how the addition of an extra l.e.d. can make a novel improvement to this circuit.

Two-Transistor Bias

By replacing the diodes with an extra *npn* transistor as shown in Fig.3, the

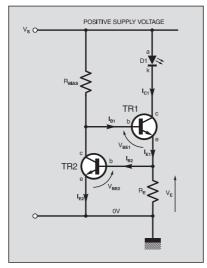


Fig.3. Two-transistor current-sink.

circuit's regulation in response to supply voltage variations can be improved significantly.

The circuit is essentially a classical, two-transistor current sink, in which feedback around TR1 and TR2 ensures that the l.e.d. current remains fixed despite changes in V_s . Transistor TR1 and resistors R_{BIAS} and R_{E} play the same part as before. However, TR2's base-emitter voltage, V_{BE2} , now provides the reference voltage necessary to regulate the l.e.d. current.

A thorough analysis of the circuit is beyond the scope of this article, but to understand how it works, assume that an increase in supply voltage causes a momentary increase in TR1's collector current, $I_{\rm Cl}$. The corresponding increase in emitter current, $I_{\rm El}$, produces an increase in TR2's base current, $I_{\rm B2}$. This, in turn, causes TR2's collector to sink more current through $R_{\rm BIAS}$ and also *away from* TR1's base.

The reduction in $I_{\rm B1}$ produces a proportionate decrease in $I_{\rm C1}$, thereby mitigating the initial rise in collector current. This overall *negative feedback* around the two transistors acts to oppose any external effects which would otherwise tend to increase or decrease the l.e.d. current.

Provided TR1 has plenty of current gain (large h_{FE}), we may assume that $I_{C1} = I_{E1}$. Furthermore, if TR2 also has large h_{FE} , we may ignore the effects of I_{B2} and assume that $I_{E1} = V_E / R_E$. However, $V_E = V_{BE2}$ and

 $I_{C1} = 1.e.d.$'s forward current = $V_{BE2} / R_E (A)$

Therefore, provided we can make a good estimate of $V_{\rm BE2}$, we may set the l.e.d. current to any value we choose simply by selecting an appropriate value for $R_{\rm E}$.

The value of bias resistor $R_{\rm BIAS}$ should be small enough to ensure adequate base drive for TR1 at minimum supply voltage. As a rule of thumb, the current through $R_{\rm BIAS}$ should be at least twice the maximum value of $I_{\rm BI}$.

Worked Example

Let's say we require an l.e.d. current of 20mA at a minimum supply voltage of 5V, that is, $I_{C1} = 20$ mA and $V_{S(min)} = 5$ V. We'll use two BC108 transistors for TR1 and TR2; these are inexpensive and have a

minimum current gain of around 100, such that the *maximum* value of I_{B1} is: $20mA/100 = 200\mu A$. Thus, the current through R_{BIAS} should be at least $2 \times 200\mu A = 400\mu A$

The voltage across resistor R_{BIAS} is simply $V_s - V_{BE1} - V_{BE2}$, and since both transistors are the same type it is fair to assume that $V_{BE1} = V_{BE2}$; therefore, the voltage across R_{BIAS} is $V_s - 2V_{BE}$. At room temperature, the typical value of V_{BE} for the BC108 is 600mV, so when $V_s = 5V$, the minimum voltage across R_{BIAS} is $5V - (2 \times 600 \text{mV}) = 3.8 \text{V}$. Therefore, the required value of R_{BIAS} is $3.8 \text{V}/400 \mu A = 9.5 \text{k}\Omega$. The nearest lower preferred value is $9.1 \text{k}\Omega$.

Rearranging the aforementioned equation for I_{C1} , we see that $R_E = V_{BE2} / I_{C1}$, and so in this example we require $R_E = 600 mV / 20 mA = 30 \Omega$.

A breadboard circuit built using these component values produced impressive results. At the minimum supply voltage of 5V, the measured l.e.d. current was exactly 20mA. With V_s increased five-fold to 25V, the rise in collector current was just 1·7mA, that is a current increase of just 8·5% against a voltage increase of 200%!

Despite its splendid current regulation, the circuit provides only moderate thermal stability due to the dependence on $V_{\rm BE}$ as a reference voltage. Typically, the temperature coefficient of $V_{\rm BE}$ in a small-signal transistor is around $-2mV/^{\circ}C$. Therefore, if $V_{\rm BE}$ is 600mV at 25°C, a temperature change of, say, $\pm 20^{\circ}C$ will cause a change in $V_{\rm BE}$, and hence in l.e.d. current, of around $\pm 7\%$.

Fortunately, this is unlikely to cause noticeable changes in l.e.d. intensity, so the circuit is usually a good choice for applications which must accommodate wide supply voltage changes and yet are not subject to extreme temperature variations.

One such application is the accompanying *EPE Voltage Checker*. This simple constructional project makes use of the circuit in Fig.3 to produce an inexpensive tester for "GO/NO GO" voltage checks.

From Logic to Light

In our examination of l.e.d. driver circuits thus far, we have dealt with purely analogue techniques for setting the current level and intensity. Many applications, however, require a digital interface between a logic circuit and one or more l.e.d.s. Depending on the type of logic device, it may be possible to drive the l.e.d. directly from one or more outputs using simple resistive current limiting. In other cases, a suitable driver circuit must be interposed between the digital output and the l.e.d.

As we shall see in a moment, different logic families have different drive capabilities, and their output characteristics – which can differ considerably from one family to another – will have a significant bearing on the kind of circuit required. The type and number of l.e.d.s that must be driven, along with the magnitude of the available supply voltage, will also influence the kind of circuit required.

CMOS 4000-Series

Due largely to its low quiescent supply current, high input impedance and relatively wide supply voltage range, the CMOS 4000-series logic family has been popular

Table 1: CD4049UB and CD4050B Output Current Vs Supply Voltage

Supply Voltage, V _{DD} (V)	Low-level (sink)	High-level (source)
	Output Current,	Output Current,
	I _{OL} (mA)	I _{OH} (mA)
5	5	1.6
10	12	3.6
15	40	12

for over quarter of a century. However, although a particular gate's output may be capable of driving the inputs of many other gates, it may be incapable of driving a single l.e.d.

In common with many other logic families, 4000-series outputs can sink and source current, that is, current can flow into a low-level output, or flow out of a high-level output. The actual output current available varies slightly with different devices and from one manufacturer to another. However, for a device such as the 4001B (quad 2-input NOR gate) or the 4011B (quad 2-input NAND gate), the output current is typically less than ± 1 mA at a supply voltage, V_{DD} , of 5V, rising to around ± 8 mA with V_{DD} increased to 15V.

For "light" loads, such as the inputs of other 4000-series gates, this is more than adequate. However, if a relatively "heavy" load is connected to the output, the output voltage tends to shift away from the nominal high or low logic level. In other words, the output does not behave as an ideal switch to $V_{\rm DD}$ or 0V, but instead appears resistive. For this reason, "standard" 4000-series gates often make poor l.e.d. drivers, except when *low current* l.e.d.s are used (more of this later).

Much better drive is available from "buffer" devices such as the 4049UB (hex, inverting) and 4050B (hex, non-inverting). Typical output current values at +25°C for the National Semiconductor CD4049UB and CD4050B are given in Table 1.

The buffer devices provide much greater output current than standard 4000-series gates. They are also clearly better at *sinking* current than sourcing it. For this reason, when driving an l.e.d. directly from a 4049UB or 4050B buffer output, it is best to refer the l.e.d. to $V_{\rm DD}$ as shown in Fig.4.

It would appear from Table 1 that the CD4049UB and CD4050B can happily sink up to 40mA when operating at 15V. However, careful examination of the data sheet's small print reveals otherwise. A footnote to the data states that:

"These are peak output current capabilities. Continuous output current is rated at 12mA maximum. The output current should not be allowed to exceed this value

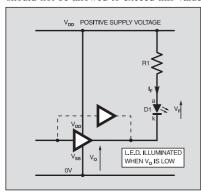


Fig.4. Logic buffer I.e.d. drive.

for extended periods of time."

When designing any l.e.d. drive circuit using logic gates, it is important to pay careful attention to the specifications for a particular device. We'll return to the subject of maximum ratings a lit-

tle later in this article.

Boosted Output Drive

The output drive current can be increased by connecting two or more buffers of the same type in parallel, as shown by the dotted connections in Fig.4. The number of buffers and the resistor value required can be determined using the method demonstrated in the following worked example:

Let's say we wish to drive a forward current, I_F , of 15mA through a red l.e.d. with maximum forward voltage of 1·8V from a minimum supply voltage of $V_{DD} = 10V$. From Table 1, we see that I_{OL} for a single buffer is typically only 12mA at this voltage – not enough. However, connecting two buffers in parallel would provide a total sink current of 24mA – more than adequate. The value of current limiting resistor R1 is given by:

$$R1 = (V_{\text{DD(min)}} - V_{\text{F(max)}} - V_{\text{OL(max)}}) \ / \ I_{\text{F}} \ (\Omega)$$

From the CD4049UB/CD4050B data sheet, the low level output voltage, $V_{\rm OL}$, is found to be a maximum of 0.5V when the buffer is sinking 12mA. Although two buffers would each sink only 7.5mA, we can assume that $V_{\rm OL(max)}=0.5V$, such that $R1=(10V-1.8V-0.5V)/15mA=513\Omega.$ The nearest preferred value is 510Ω .

74LS-Series

Like the original 74-series logic devices, the 74LS-series logic gates can sink much more current "into" their outputs than they can source out. A typical device like the 74LS02 (quad, 2-input NOR gate) operating on a 5V supply can sink up to 8mA, but can source only around 0-4mA. Therefore, with the l.e.d. referred to the positive supply, a single output may provide just enough current to illuminate an l.e.d. adequately, although two or more devices in parallel may be required to increase the brightness.

Like the 4000-series, the 74LS-series includes buffer devices which provide enhanced drive capability. For instance, the 74LS240 (octal buffer/line driver) can sink as much as 24mA into a single output.

74HC/HCT-series

Although the 74HC/HCT-series logic family uses CMOS technology like the 4000-series, its output drive capability is much better than 4000-series devices. Unlike the 74LS-series, its output drive is fairly symmetrical, in that the magnitude of the output sink and source currents are roughly equal. The 74HC parts have CMOS logic level compatibility, whereas the 74HCT parts are compatible with TTL voltage levels.

"Standard" devices such as the 74HC04 (hex inverter) and 74HC32 (quad, 2-input OR gate) operating on a 5V supply can sink

and source a *maximum* of 5mA with little shift in output voltage. "Buffer" devices like the 74HC365 (hex tri-state buffer) provide slightly more drive at around ±7mA on a 5V supply.

Paralleling two standard outputs, for example, would provide around 10mA of drive current to an l.e.d. that could be referred either to the positive supply rail (l.e.d. illuminates when the outputs go low) or to the GND (0V) rail (l.e.d. illuminates when the outputs go high).

74AC/ACT-Series

Like the 4000-series and 74HC/HCT parts, the 74AC/ACT family uses CMOS technology and, likewise, benefits from low quiescent power consumption. However, this family also boasts remarkably good output current specifications.

Consider, for example, the 74AC/ACT04 (hex inverter). When powered by a 3V supply rail, each of the six inverters can source and sink up to 12mA. At 5V, the output drive doubles to ±24mA. These specifications mean that a single inverter could drive a low-V_F l.e.d. from a supply voltage as low as 3V. Most other devices in this family (gates, Schmitt triggers, flip-flops, and so on) provide the same degree of output drive.

Like most other 74-type logic families, the 74AC/ACT devices have a maximum operating voltage of around 6-0V. This is much lower than that of 4000-series parts which can operate at supply voltages as high as 15V. However, the 74AC devices are specified to operate down to just 2-0V, making them ideal for battery-powered applications.

For example, two series-connected Nickel Cadmium cells when fully charged will provide around 2.6V. Therefore, using a circuit similar to that in Fig.4, two paralleled 74AC gates could easily drive around 15mA, or more, into a red l.e.d., and would continue to provide adequate brightness as the cells became discharged.

A Word of Caution

On several occasions, the author has seen circuits in which a logic gate output has been connected directly to an l.e.d., that is, without a series-connected, current limiting resistor. Except in very special circumstances, this practice is inviting disaster!

For example, let's consider the Philips HEF4000-series CMOS logic family. The absolute maximum ratings for these parts specify a maximum output current, $I_{O(max)}$, of ± 10 mA per output. Also, the maximum power dissipation, $P_{O(max)}$, for each output is 100mW.

Tests on a single gate from an HEF4001B (quad 2-input NOR gate) produced interesting results. With the supply voltage, $V_{\rm DD}$, set to 5V and the gate output set high, a red l.e.d. connected directly between the output and the $V_{\rm SS}$ (negative) supply rail drew just under 4mA from the output. With the output set low and the l.e.d. connected directly between the output and $V_{\rm DD}$, the output current was 3·2mA.

In each case, the output current is below the maximum rating (± 10 mA) and would do no damage to the l.e.d. itself. However, at a higher supply voltage, the results were drastically different. At $V_{\rm DD}=10$ V, and with the gate output set high, the directly-connected l.e.d. drew 18-4mA from the

output and pulled down the high level output voltage (nominally 10V) to 2·3V. The corresponding power dissipation in the *sin-gle gate* was 142mW.

With the gate output set low, the l.e.d. current increased to 19·8mA and the low level output voltage (nominally 0V) was pulled up to 7·73V, corresponding to a gate power dissipation of 153mW!

In each case, the output current clearly exceeds the gate's maximum rating by a huge amount, and the gate's power dissipation is exceeded by around 50%! Furthermore, a forward current of 20mA, or so, could damage certain types of l.e.d., particularly the "low current" kind discussed later.

Obviously, in this example, increasing the supply voltage from 5V to 10V made an enormous difference to the current and power ratings. However, this does not necessarily imply that operation at low supply voltages is always safe.

As a second example, let's consider the National Semiconductor MM74HC02N, a quad 2-input NOR gate. The maximum operating voltage for this family of devices is 6V (although it's rare to see them operating at more than 5V) and the maximum output current per pin is ±25mA. Tests on a single gate from the 74HC02 produced sobering results. With the supply voltage set to 5V and the gate output set high, the red l.e.d. connected directly to the output drew almost 40mA from the output, some 15mA more than the maximum rated value!

With the output set low, the l.e.d. current was 37mA – again, dangerously high. Clearly, a series resistor should always be used when driving l.e.d.s from logic outputs. Not only does the resistor allow the forward current to be set to precisely the required value, but it can also be essential in protecting both the gate and the l.e.d.

Logic to L.E.D. Interfacing

Although we have seen that logic devices can drive l.e.d.s directly (via a suitable resistor), one often encounters applications where circumstances prevent a simple "direct drive" approach. For instance, although a 4000-series gate with a "weak" output could be boosted by several 4050B buffers, it may be inappropriate in terms of cost or board space to do this.

Similarly, it is often impossible for a logic output to drive a string of series-connected l.e.d.s, especially when the logic supply voltage is relatively low.

In these cases, some kind of interface circuit becomes necessary. One such approach is shown in Fig.5, where TR1, a *pnp* bipolar transistor, effectively provides current gain for a weak logic gate.

When the logic output, V_0 , is high, TR1 and the l.e.d. are both "off". However, when V_0 goes low (ideally to 0V), TR1 turns on and sinks current through R1 and the l.e.d. The transistor behaves as an emitter-follower in that it provides plenty of emitter current in return for very little base current supplied by the logic gate.

When V_0 is low, the l.e.d.'s forward current is given by:

$$I_F = (V_{CC} - V_F - V_{BE} - V_O) / R1 (A)$$

where V_{CC} is the logic supply voltage, and V_{BE} is TR1's base-emitter voltage drop. For most CMOS-type logic devices, the low-

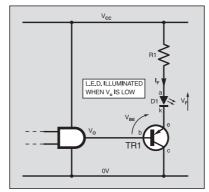


Fig.5. Active-low logic-l.e.d. interface circuit.

level output voltage is roughly zero when sinking little current, so it is reasonable to assume that $V_{\rm O}=0$. We can thus simplify and rearrange the equation to give an expression for the required value of series resistor:

$$R1 = (V_{CC} - V_F - V_{BE}) / I_F (\Omega)$$

This circuit has the advantage of requiring very little current from the logic gate to drive the l.e.d. A forward current of, say, $I_F = 20 \text{mA}$ would typically demand less than $100 \mu \text{A}$ sink current from the gate output. A disadvantage, however, is the presence of TR1's V_{BE} drop which can limit the circuit's applicability when V_{CC} is low, or when two or more l.e.d.s must be driven in series

Alternative Version

In Fig.6 is shown a variation on the theme in which either an npn bipolar transistor (Fig.6a) or an n-channel MOSFET (Fig.6b) is used as the on-off "switch". In both circuits, the l.e.d. is illuminated when the logic output, $V_{\rm O}$, goes high.

In Fig.6a, the high logic level sources base current to TR1, via current-limiting resistor R1, turning on both the transistor and the l.e.d. Provided R1 is chosen properly, TR1 will receive adequate base current from the logic output and will saturate, such that the l.e.d.'s forward current is:

$$I_F = (V_S - V_F - V_{CE(sat)}) / R2 (A)$$

where V_{S} is the l.e.d. supply voltage and $V_{\text{CE(sat)}}$ is TR1's collector-emitter saturation voltage. In most cases, the magnitude of $V_{\text{CE(sat)}}$ will be negligibly small compared to

 V_S and V_F , and so the equation simplifies to:

$$I_F = (V_S - V_F) / R2 (A)$$

or:
$$R2 = (V_S - V_F) / I_F (\Omega)$$

making it a simple matter to select a suitable value for R2.

Base resistor R1 should be chosen to ensure TR1 turns on fully when V_0 is high. The actual value required depends on the base current needed by TR1, and will therefore be influenced by the transistor type. A device like the BC546, for example, requires a base current of around $100\mu A$ to ensure $V_{\text{CE(sat)}}$ is less than 200mV when conducting a collector current of 20mA.

A higher collector current naturally requires a greater base current. Generally, for $V_{\rm CC}$ = 5V, a value of $2{\cdot}2k\Omega$ to $10k\Omega$ should suffice for R1.

The circuit can be simplified by replacing the npn transistor with an n-channel MOSFET as shown in Fig.6b. Again, the l.e.d. is turned on when V_0 goes high. Provided the chosen MOSFET has low drain-source "on" resistance, $R_{\mathrm{DS(on)}}$, the corresponding drain-source voltage, denoted $V_{\mathrm{DS(on)}}$, will be small and negligible, such that the equation given above may be used to calculate R2.

Note, however, that achieving low $R_{DS(on)}$ isn't just a matter of choosing the right MOSFET – adequate gate voltage is also important. A device like the popular 2N7000, for example, will turn on with a typical gate-source voltage, V_{GS} , of just 2-3V, but at this level, $R_{DS(on)}$ could be more than $1k\Omega!$

With V_{GS} increased to 4.5V, however, $R_{DS(on)}$ is typically just 4.8 Ω ; at this value, the corresponding value of $V_{DS(on)}$ would be just 96mV with a forward l.e.d. current of 20mA.

With a logic supply voltage, $V_{\rm CC}$, of 5V, the output of most CMOS gates will swing up to 5V, thus providing more than enough gate drive for TR1. However, a TTL device such as a 74LS gate might have an output high voltage of just 3·0V, or so, when operating at $V_{\rm CC}=5$ V. In this case, pull-up resistor R1 can be employed to ensure the gate is pulled up to $V_{\rm CC}$ when $V_{\rm O}$ goes high. The pull-up resistor value can be fairly large (typically around $100{\rm k}\Omega$), so little power is wasted in it when $V_{\rm O}$ is low.

In the circuits of Fig.6, the l.e.d. supply voltage, V_s , can be higher than V_{CC} . This is a distinct advantage when it is necessary to drive multiple series-connected l.e.d.s where the total forward voltage could

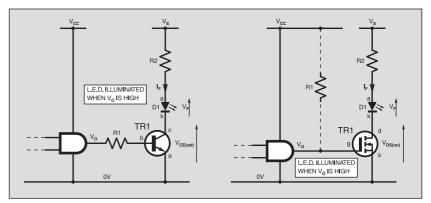


Fig.6. Active-high logic-l.e.d. interface circuits (a) bipolar, (b) MOSFET.

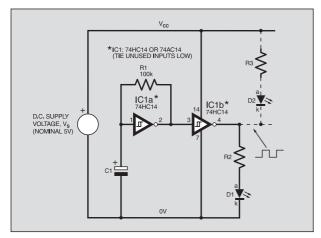


Fig.7. Astable I.e.d. flasher circuit.

Fig.8. Gated I.e.d. flasher circuit.

easily exceed the available logic supply voltage. For example, driving five red l.e.d.s each having $V_F = 1.6V$ would require a supply voltage of at least 8V for proper operation.

Low Current L.E.D.s

Several manufacturers now supply a range of low current l.e.d.s which provide good light intensity at forward current levels that are a fraction of those needed by "standard" l.e.d.s. Take the HLMP-D155, for example, manufactured by Agilent (formerly Hewlett-Packard). This red l.e.d. has a typical luminous intensity of 10mcd (millicandela) when operated at a forward current of just 1mA. Compare this with a standard red l.e.d., such as the HLMP-3301, which has a typical luminous intensity of only 7mcd at 10mA.

Clearly, the standard l.e.d. has only 70% of the intensity of the low current lamp, even though it is operating at ten times the forward current!

The low current requirements of devices like the HLMP-D155 mean that relatively "weak" logic gates, such as standard 4000-series devices operating at low supply voltage, can drive the l.e.d. directly, i.e., without needing an interface circuit (but still needing a ballast resistor of course).

In addition to simplifying the drive circuit, low current l.e.d.s also provide significant power savings. The standard HLMP-3301, for example, operating at 10mA on a 5V supply consumes 50mW, whereas the low current HLMP-D155 operating at 1mA on the same supply voltage would require just 5mW – a power saving of 45mW. Such power savings are an obvious benefit for battery powered equipment where reduced power consumption equates to longer battery life.

The *EPE Volts Checker* project makes good use of a low current l.e.d. where a diminutive forward current of around 2mA is essential for high voltage operation.

On the Blink

A flashing l.e.d. is a highly effective visible signal, and can be used to warn of a fault condition, to provide a decorative display, or as a means of attracting attention such as an emergency beacon.

The circuit in Fig.7 is a simple and inexpensive l.e.d. "blinker", where IC1a forms an astable multivibrator and IC1b acts as a buffer. The second inverter is not essential,

but helps to minimise loading of IC1a which could otherwise affect the timing.

The l.e.d. may be connected to the negative supply rail (D1 with R2) or to the positive rail (D2 with R3). Connecting both l.e.d.s provides an alternating display. When experimenting with the circuit, choose l.e.d.s which give good brightness at a forward current of 10mA or less. The value of current limiting resistor R2 (and/or R3) should be around 270Ω to $1k\Omega$ depending on the type and colour of l.e.d.(s) used.

Resistor R1 and capacitor C1 set the astable time constant, and hence the flash rate. A Schmitt trigger inverter such as the 74HC14 or 74AC14 is necessary for IC1a – a standard inverter will not work. Other Schmitt devices such as the 4093B (quad 2-input NAND gate) or 40106B (hex Schmitt inverter) could be substituted and would permit operation at supply voltages up to 15V, but suitable buffering may be required unless low current l.e.d.s are used.

The circuit is not a "precision" flasher because the flash rate depends heavily on the Schmitt trigger thresholds which vary on a part-to-part basis and with changes in supply voltage. Nevertheless, the circuit's simplicity makes it ideal for experimenting.

The value of resistor R1 should not be too small, otherwise a large value for capacitor C1 would be required to produce a low flash rate. On the other hand, R1 should not be too large, or leakage currents could affect the timing. A maximum value of $680 \text{k}\Omega$ should be acceptable.

With R1 = $100k\Omega$, a capacitor value of 100μ F will produce a typical flash frequency of around 0.15Hz, or roughly one flash every seven seconds. Reducing the value of C1 increases the flash rate. With C1 = 1.0μ F, the l.e.d. blinks rapidly, at approximately 15 flashes per second.

Gated Flasher

The circuit in Fig.8 implements a "gated" flasher, where the flashing may be turned on and off by means of a logic level

Enable signal. The circuit is similar to that in Fig.7, but with the Schmitt inverter replaced by a single Schmitt NAND gate (one quarter of a 74HC132 quad Schmitt NAND gate).

When the Enable signal is low, the NAND output is continually high such that the l.e.d. is held off. Taking the Enable signal high "releases" the output, allowing the l.e.d. to flash. As before, the values of resistor R1 and capacitor C1 determine the flash rate; with C1 = 10μ F and R1 = $100\kappa\Omega$, the l.e.d. blinks roughly twice every second.

An interesting variation on this circuit is shown in Fig.9, where capacitor C1 and resistor R1 again determine the astable oscillation frequency. The addition of C2, R2, R3 and diode D2 forms another timing network which effectively causes the circuit to "gate itself". The result is a sequence of l.e.d. flashes, followed by a pause during which the l.e.d. is off, followed by another sequence of flashes, and so on.

On power up, both capacitors are initially discharged, such that pin 1 of IC1a is high and pin 2 is low causing the NAND output to be high. C1 now begins to charge via R1; during this time D2 is reverse biased and the l.e.d. is off. Eventually, the voltage on C1 crosses the Schmitt trigger's upper threshold, the gate output goes low, and the astable starts to oscillate and flashes the l.e.d. exactly as per the circuit in Fig.8.

However, every time the output pulses low and flashes the l.e.d., it also delivers a packet of charge into C2 via D2 and R2. As

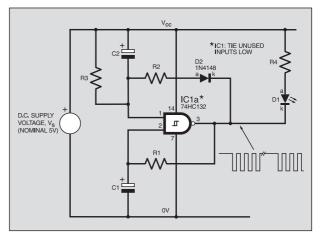


Fig.9. Self-gated I.e.d. flasher circuit.

a result, C2 slowly charges up causing the voltage at pin 1 to fall in a stepped fashion. Eventually, when this voltage crosses the Schmitt's lower threshold, the NAND output goes high and stays high irrespective of the voltage at pin 2.

The l.e.d. is now off and remains off while C2 begins to discharge via R3, causing the voltage at pin 1 to rise exponentially with a time constant determined by C2 and R3. When the voltage crosses the upper threshold, the gate is again Enabled, the astable starts to oscillate, and the process repeats.

Eye Catching

Although the circuit in Fig.9 is more complex than the simple flashers of Fig.7 and Fig.8, the visual effect is much more eye catching than an l.e.d. which simply flashes repeatedly.

The duration of each flash depends on the C1/R1 time constant. The number of flashes depends on the time taken to charge C2, and so depends on C1/R1, C2/R3, and R2. The "off" period depends on the time taken for C2 to discharge, which is determined only by the C2/R3 time constant.

For proper operation, the C1/R1 time constant should be much smaller than that of C2/R3. Also, R2 should be much smaller than R3, ideally by a factor ten or more, to ensure that the voltage at pin 1 can cross the Schmitt's lower threshold. Diode D2's voltage drop must also be taken into account here, especially if operating at supply voltages below 5V.

A test circuit built with R1 = $470k\Omega$, C1 = 1μ F, R2 = $47k\Omega$, C2 = 33μ F and R3 = $1M\Omega$ produced a series of three flashes, followed by an "off" pause of around ten seconds. All timing parameters are affected by the Schmitt thresholds and by the supply voltage, so be prepared to experiment with component values to get the best effect.

From Flashers to Dimmers

Returning briefly to the circuit in Fig.7, and reducing the value of C1 to 100nF produces an interesting effect. The astable oscillates at around 150Hz, and although the l.e.d. actually flashes at this rate it appears to be continually on, but at reduced brightness. What is happening?

Put simply, the human eye cannot respond quickly enough to distinguish each individual flash, and so it effectively "integrates" the rapid pulsing to give the

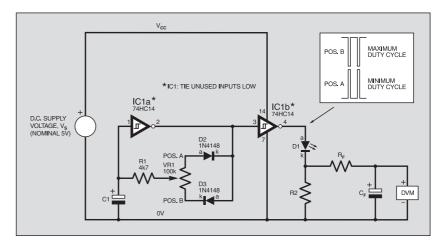


Fig. 10. PWM brightness control.

appearance of a dimmed l.e.d. This effect can be explored further using the circuit shown in Fig.10, where resistor R1 in Fig.7 has been replaced with the combination of R1, potentiometer VR1, and diodes D2, D3.

The presence of the potentiometer and two diodes allows the *duty cycle* of the flash pulses to be varied, where:

Duty Cycle =
$$\frac{\text{On Time}}{\text{On Time} + \text{Off Time}}$$
 (%)

For example, with C1 = 10µF, the flash rate is typically around 2Hz and varies very little with changes in VR1's wiper position. However, the flash duration varies from a very brief flash with VR1's wiper at position A (corresponding to minimum duty cycle) to being on almost continually in position B (maximum duty cycle).

If C1 is now reduced to 10nF, the oscillation frequency is increased a thousand fold to around 2kHz, and although the l.e.d. is still being pulsed on and off, it appears to be on all the time. Adjusting the pot's wiper position changes the duty cycle and hence varies the average current through the l.e.d. Thus, with the wiper in position B, the l.e.d. is almost at full brightness, whereas in position A, it is very dim.

This circuit demonstrates an important technique known as *pulse width modulation*, or PWM, wherein the width of the pulse driving the l.e.d. is modulated to vary the brightness. Although the current

through the l.e.d. still consists of a series of variable width, constant amplitude pulses, the brightness is proportional to the average current. To measure the average current, the current limiting resistor R2 may be used as a convenient current sense resistor.

Additional components C_F and R_F (required only for measuring the average current) form a low-pass filter, such that the voltage across C_F is a d.c. level corresponding to the average voltage across R2. The voltage on C_F should be measured using a voltmeter with high input impedance, such as a $10M\Omega$ digital voltmeter (DVM).

Dividing the measured voltage by the value of R2 gives the average current through the l.e.d. The values of C_F and R_F are not critical, but C_F should be large enough to minimise the ripple on the measured voltage. Values of $C_F = 100\mu F$ and $R_F = 22k\Omega$ should give good results.

Digitally-Controlled Brightness

By replacing the manual potentiometer VR1 with a "digipot" (digitally controlled potentiometer), the circuit of Fig.10 can be adapted to provide fully digital control of the l.e.d.'s brightness. In the circuit of Fig.11, the digipot, IC2, takes the place of VR1 in Fig.10. The digital pot behaves in a similar manner to its manual counterpart, except that the wiper position and direction are controlled by digital signals at the CLK (clock) and U/D (up/down) inputs.

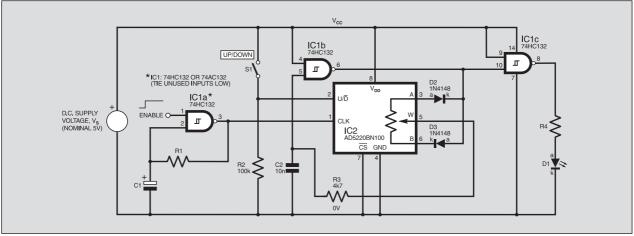


Fig.11. Digitally controlled I.e.d. brightness.

Schmitt NAND gate IC1b, together with C2, R3 and the digipot, forms the astable multivibrator whose pulse width is modulated to vary the l.e.d. intensity. IC1c is not essential and simply acts as a buffer. IC1a, C1 and R1 form a gated astable similar to that in Fig.8; in this circuit, however, it provides a means of clocking the digital pot. With C1 = 1μ F and R1 = $100k\Omega$, the clock frequency is around 15Hz.

Note that an astable clock source is not mandatory; a suitably "debounced" pushbutton switch could be used instead. The logic level at pin 2 of IC2 controls the direction of the pot wiper. Closing switch S1 puts a high level at this input, such that each clock pulse arriving at pin 1 causes the pot to increment, thereby moving the wiper "W" closer to terminal "A" at pin 3. This maximises the duty cycle at the output of IC1b, but the inversion by buffer IC1c minimises the duty cycle drive to the l.e.d., causing it to get dimmer

With pin 2 low (switch S1 open), clocking IC2 causes the digipot to decrement,

such that the wiper moves in the opposite direction toward terminal "B" at pin 6. This maximises the duty cycle at the output of IC1c, thereby increasing the l.e.d.'s brightness.

When the Enable signal at pin 1 of IC1a is high, the astable "free runs", thus supplying continuous clock pulses for the digipot. By opening and closing switch S1, the l.e.d. ramps up to maximum brightness, or ramps down to minimum brightness, respectively. Taking the Enable signal low cuts off the clock pulses and holds the l.e.d. at its current level of intensity.

The circuit provides a completely solidstate way of varying the l.e.d.'s intensity no moving parts are required. The PWM method is an efficient technique, often employed to vary the brightness of l.e.d.s used as backlights in items such as mobile phones and laptop computers. However, it is not restricted to l.e.d.s, and is widely used to control other loads, such as d.c. motors, heating elements, and so on.

When experimenting with the circuit, choose a fairly large value for resistor R4 so as to produce maximum contrast between the extremes of l.e.d. intensity. Also, note that IC2, a 128-position, $100k\Omega$ digipot, is just one of many digital potentiometers on the market. Other types, manufactured by companies such as Maxim, Xicor and Catalyst Semiconductor, may be better suited to your application.

Next Month

In the next part of this series we look at other techniques for flashing an l.e.d., and we examine "booster" circuits capable of driving l.e.d.s from very low voltages. We'll also look at some of the convenient features of bicolour and tricolour

For now, we conclude this month's article with a conundrum: considering that a red l.e.d. can have a forward voltage of 1.6V or more, how is it possible to flash such an l.e.d. from a voltage of just

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Teach-In 2000 covers all the basic principles of electronics from Ohm's Law to Displays, including Op.Amps, Logic Gates etc. Each part has its own section on the interactive software where you can also change component values in the various on-screen demonstration circuits.

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be used to control the robots.

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Detailed building instructions are provided for the featured robots, including numerous step-by-step photographs. The designs include rover vehicles, a virtual pet, a robot arm, an 'intelligent' sweet dispenser and a colour conscious robot that will try to grab objects of a specific colour.

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fully animated robot or android can also be built and programmed to perform a wide variety of functions. The contents include an Overview of State-of-the-Art Robots; Robotic Locomotion; Motors and Power Controllers; All Types of Sensors; Tilt; Bump; Road and Wall Detection; Light; Speech and Sound Recognition; Robotic Intelligence (Expert Type) Using a Single-Board Computer Programmed in BASIC; Robotic Intelligence (Neutral Type) Using Simple Neural Networks (Insect Intelligence); Making a Lifelike Android Hand; A Computer-Controlled Robotic Insect Programmed in BASIC; Telepresence Robots With Actual Arcade and Virtual Reality Applications; A Computer-Controlled Robotic Arm; Animated Robots and Androids; Real-World Robotic Arm; Animated Robots and Androids; Real-World Robotic Arm; Animated Robots and Androids; Real-World Robotic Applications. Robotic Applications

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For a further selection of books see the next two issues of EPE.

Radio

BASIC RADIO PRINCIPLES AND TECHNOLOGY

Radio technology is becoming increasingly important in today's high technology society. There are the traditional uses of radio which include broadcasting and point to point radio as well as the new technologies of satellites and cellular phones. All of these developments mean there is a growing need for radio engineers at all levels. Assuming a basic knowledge of electronics, this book

provides an easy to understand grounding in the topic.
Chapters in the book: Radio Today, Yesterday, and
Tomorrow; Radio Waves and Propagation; Capacitors, Inductors, and Filters; Modulation; Receiver Transmitters; Antenna Systems; Broadcasting; Satellite Personal Communications; Appendix – Bas Receivers Calculations.

263 pages

Order code NE30

£18.99

PROJECTS FOR RADIO AMATEURS AND S.W.L.S. R. A. Penfold

This book describes a number of electronic circuits, most of which are quite simple, which can be used to enhance the performance of most short wave radio systems. The circuits covered include: An aerial tuning unit; A

simple active aerial; An add-on b.f.o. for portable sets; A wavetrap to combat signals on spurious responses; An audio notch filter; A parametric equaliser; C.W. and S.S.B. audio filters; Simple noise limiters; A speech processor; A

audio filters; Simple noise limiters; A speech processor; A volume expander.

Other useful circuits include a crystal oscillator, and RTTY/C.W. tone decoder, and a RTTY serial to parallel converter. A full range of interesting and useful circuits for short wave enthusiasts.

Order code BP304

AN INTRODUCTION TO AMATEUR RADIO

Amateur radio is a unique and fascinating hobby which has attracted thousands of people since it began at the turn of the century. This book gives the newcomer a comprehensive and easy to understand guide through the subject so that the reader can gain the most from the hobby. It then remains an essential reference volume to be used time and again. Topics covered include the basic aspects of the hobby, such as operating procedures, jar-gon and setting up a station. Technical topics covered include propagation, receivers, transmitters and aerials

150 pages

Order code BP257

VALVE RADIO AND AUDIO REPAIR HANDBOOK

VALVE RADIO AND AUDIO REPAIR HANDBOOK (Second Edition) Chas Miller
This book is not only an essential read for every professional working with antique radio and gramophone equipment, but also dealers, collectors and valve technology enthusiasts the world over. The emphasis is firmly on the practicalities of repairing and restoring, so technical content is kept to a minimum, and always explained in a way that can be followed by readers with no background in electronics. Those who have a good grounding in electronics but wish to learn more about grounding in electronics, but wish to learn more about the practical aspects, will benefit from the emphasis given to hands-on repair work, covering mechanical as well as electrical aspects of servicing. Repair techniques

are also illustrated throughout.

A large reference section provides a range of information compiled from many contemporary sources, and includes specialist dealers for valves, components and complete receivers.

270 pages

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Computers and Computing

THE INTERNET FOR THE OLDER GENERATION Jim Gatenby

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228 pages

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HOW TO BUILD YOUR OWN PC -

Morris Rosenthal

More and more people are building their own PCs. They get more value for their money, they create exactly the machine they want, and the work is highly satisfying and actually fun. That is, if they have a unique beginner's guide like this one, which visually demonstrates how to construct a state-of-the-art computer from start

to finish.

Through 150 crisp photographs and clear but minimal text, readers will confidently absorb the concepts of computer building. The extra-big format makes it easy to see what's going on in the pictures. For non-specialists, there's even a graphical glossary that clearly illustrates technical terms. The author goes "under the hood" and shows step-by-step how to create a socket 7 (Pentium and non-intel chipsets) and a Slot 1 (Pentium Purpless of the property of the p II) computer, covering: What first-time builders need to know; How to select and purchase parts; How to assemble the PC; How to install Windows 98. The few existing books on this subject, although badly outdated, are in steady demand. This one delivers the expertise and new technology that fledgling computer builders are eagerly looking for.

224 pages – large format Order code MGH2 £20.99

PIC YOUR PERSONAL INTRODUCTORY COURSE SECOND EDITION John Morton
Discover the potential of the PIC microcontroller through graded projects – this book could
revolutionise your electronics construction work!
A uniquely concise and practical guide to getting up
and running with the PIC Microcontroller. The PIC is
one of the most popular of the microcontroller that are
transforming electronic project work and product
design

Assuming no prior knowledge of microcontrollers Assuming no prior knowledge of infercoontineing and introducing the PIC's capabilities through simple projects, this book is ideal for use in schools and colleges. It is the ideal introduction for students, teachers, technicians and electronics enthusiasts. The step-bystep explanations make it ideal for self-study too: this is not a reference book – you start work with the PIC straight away.

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The revised second edition covers the popular reprogrammable EEPROM PICs: P16C84/16F84 as well as the P54 and P71 families.

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UNDERSTANDING PC SPECIFICATIONS R. A. Penfold (Revised Edition)

PCs range from simple systems of limited capabilities up to complex systems that can happily run applications that would have been considered beyond the abilities of a microcomputer not so long ago. It would be very easy to choose a PC system that is inadequate to run your applications efficiently, or one which goes beyond your needs and consequently represents poor value for money.

This book explains PC specifications in detail, and

the subjects covered include the following: Differences the subjects covered include the following: Differences between types of PC (XT, AT, 80386, etc); Maths co-processors; Input devices (keyboards, mice, and digitisers); Memory, including both expanded (EMS) and extended RAM; RAM disks and disk caches; Floppy disk drive formats and compatibility; Hard disk drives (including interleave factors and access times); Display adaptors, including all standard PC types (CGA, Hercules, Super VGA, etc); Contains everything you need to know if you can't tell your EMS from your EGA!

128 pages

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Theory and Reference

BEBOP TO THE BOOLEAN BOOGIE Second Edition Clive (Max) Maxfield

BOOK PLUS CD-ROM

This book gives the "big picture" of digital electronics. This indepth, highly readable, up-to-the-minute guide shows you how electronic devices work and how they're made. You'll discover how transistors operate, how printed circuit boards are fabricated, and what the innards of memory ICs look like. You'll also gain a working knowledge of Boolean Algebra and Karnaugh Maps, and understand what Reed-Muller logic is and how it's used. And there's much, MUCH more. The author's tongue-in-cheek humour makes it a delight to read, but this is a REAL technical book, extremely detailed and accurate. Comes with a free CD-ROM which contains an eBook version with full text search plus bonus chapter—

an eBook version with full text search plus bonus chapter — An Illustrated History of Electronics and Computing. Contents: Fundamental concepts; Analog versus digital; Conductors and insulators; Voltage, current, resistance, capacitance and inductance; Semiconductors; Primitive logic functions; Binary arithmetic; Boolean algebra; Karnaugh maps; State diagrams, tables and machines; Analog-to-digital and digital-to-analog; Integrated circuits (ICs); Memory ICs; Programmable ICs; Application-specific integrated circuits (ASICs); Circuit boards (PWBs and DWBs); Hybrids; Multichip modules (MCMs); Alternative and future technologies. and future technologies.

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BEBOP BYTES BACK (and the Beboputer Computer Simulator)

Clive (Max) Maxfield and Alvin Brown

This follow-on to Bebop to

This follow-on to Bebop to the Boolean Boogie is a multimedia extravaganza of information about how computers work. It picks up where "Bebop I" left off, guiding you through the fascinating world of computer design ... and you'll have a few chuckles, if not belly laughs, along the way. In addition to over 200 megabytes of mega-cool multimedia, the CD-ROM contains a virtual microcomputer, simulating the motherboard and standard computer peripherals in an extremely realistic manner. In addition to a wealth of technical information, myriad nuggets of trivia, and hundreds of carefully drawn illustrations, the CD-ROM contains a set of lab experiments for the virtual microcomputers. tains a set of lab experiments for the virtual microcomputer that let you recreate the experiences of early com-puter pioneers. If you're the slightest bit interested in the

inner workings of computers, then don't dare to miss Over 800 pages in Adobe Acrobat format

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ELECTRONICS MADE SIMPLE Ian Sinclair

Assuming no prior knowledge, Electronics Made Simple presents an outline of modern electronics with an emphasis on understanding how systems work rather than on details of circuit diagrams and calculations. It is ideal for students on a range of courses in electronics, including GCSE, C&G and GNVQ, and for students of other subjects who will be using electronic instruments and methods

Contents: waves and pulses, passive components, active components and ICs, linear circuits, block and circuit diagrams, how radio works, disc and tape recording, elements of TV and radar, digital signals, gating and logic circuits, counting and correcting, microprocessors calculators and computers, miscellaneous systems.

199 pages

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SCROGGIE'S FOUNDATIONS OF WIRELESS ELECTRONICS - ELEVENTH EDITION S. W. Amos and Roger Amos

Scroggie's Foundations is a classic text for anyone working with electronics, who needs to know the art and craft of the subject. It covers both the theory and practical aspects of a huge range of topics from valve and tube technology, and the application of cathode ray tubes to radar, to digital tape systems and optical recording techniques

Since Foundations of Wireless was first published over 60 years ago, it has helped many thousands of readers to become familiar with the principles of radio and electronics. The original author Sowerby was succeeded by Scroggie in the 1940s, whose name became synonymous with this classic primer for practitioners and students alike. Stan Amos, one of the fathers of modern electronics and the author of many well-known books in the area, took over the revision of this book in the 1980s and it is he, with his son, who have produced this latest version.

400 pages

124 pages

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GETTING THE MOST FROM YOUR MULTIMETER R. A. Penfold

This book is primarily aimed at beginners and those of limited experience of electronics. Chapter 1 covers the basics of analogue and digital multimeters, discussing the relative merits and the limitations of the two types. In Chapter 2 various methods of component checking are described, including tests for transistors, thyristors, resistors, capacitors and diodes. Circuit testing is covered in Chapter 3, with subjects such as voltage, current and continuity

checks being discussed.

In the main little or no previous knowledge or experience is assumed. Using these simple component and circuit testing techniques the reader should be able to confidently tackle servicing of most elec-

96 pages

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Music, Audio and Video

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THE INVENTOR OF STEREO - THE LIFE AND WORKS OF ALAN DOWER BLUMLEIN
Robert Charles Alexander
This book is the definitive study of the life and works of
one of Britain's most important inventors who, due to a

cruel set of circumstances, has all but been overlooked by

Inistory.

Alan Dower Blumlein led an extraordinary life in which his inventive output rate easily surpassed that of Edison, but whose early death during the darkest days of World War Two led to a shroud of secrecy which has covered his life and experiencements ourse incered.

and achievements ever since.

His 1931 Patent for a Binaural Recording System was so revolutionary that most of his contemporaries regarded it as more than 20 years ahead of its time. Even years after his death, the full magnitude of its detail had not been fully utilized. Among his 128 patents are the principal electronic circuits critical to the development of the world's first elecronic television system. During his short working life, Blumlein produced patent after patent breaking entirely new ground in electronic and audio engineering.

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lan Waugh
Even though music production has moved into the digital domain, modern synthesisers invariably use analogue synthesis techniques. The reason is simple—analogue synthesis is flexible and versatile, and it's rela attively easy for us to understand. The basics are the same for all analogue synths, and you'll quickly be able to adapt the principles to any instrument, to edit existing sounds and create exciting new ones. This book describes: How analogue synthesis works; The essential modules every synthesiser has. The three steps to synthesis; How to create phat bass sounds; How to generate filter sweeps; Advanced synth modules; How to create simple and complex synth patches; Where to find soft synths on the Web.

If you want to take your synthesiser – of the hardware

or software variety – past the presets, and program your own sounds and effects, this practical and well-illustrated book tells you what you need to know.

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Whether you want to stay bang up to date with the latest music or create your own MP3s and join the on-line digital music revolution, this book will show you how.

60 pages

R. A. Penfold

Order code PC119

Whether you wish to save money, boldly go where no musi-cian has gone before, rekindle the pioneering spirit, or sim-ply have fun building some electronic music gadgets, the designs featured in this book should suit your needs. The

projects are all easy to build, and some are so simple that even complete beginners at electronic project construction can tackle them with ease. Stripboard layouts are provided for every project, together with a wiring diagram. The

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and achievements ever since.

breaking entirely new ground in electronic and data-engineering.

During the Second World War, Alan Blumlein was deeply engaged in the very secret work of radar development and contributed enormously to the system eventually to become 'H25' – blind-bombing radar. Tragically, during an experimental H25 flight in June 1942, the Halfat womber in which Blumlein and several colleagues were flying, crashed and all aboard were killed. He was just days short of his thirty-labb killedau. Order code NE32

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VIDEO PROJECTS FOR THE ELECTRONICS CONSTRUCTOR R. A. Penfold

Written by highly respected author R. A. Penfold, this book contains a collection of electronic projects specially

designed for video enthusiasts. All the projects can be simply constructed, and most are suitable for the new-comer to project construction, as they are assembled on extrabologic

comer to project construction, as they are assembled on stripboard.

There are faders, wipers and effects units which will add sparkle and originality to your video recordings, an audio mixer and noise reducer to enhance your sound-tracks and a basic computer control interface. Also, there's a useful selection on basic video production techniques to get you started.

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124 pages

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PC MUSIC - THE EASY GUIDE

Robin Vincent
How do I make music on my PC? Can I record music onto
my PC? What's a sequencer? How can I get my PC to print
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board to my PC?:

Just a few of the questions you've probably asked. Well, you'll find the answers to all these questions, and many more, in this book. It will show you what can be done, what it all means, and what you will need to start creating your own music on your PC. It's an easy read, it's fully illustrated and it will help you understand how a computer can be used as a creative music tool.

It covers soundcards, sequencers, hard disk digital audio recording and editing, plug-ins, printing scores with notation software, using your PC as a synthesiser, getting music onto and off the Internet, using Windows, sample PC music setups, FAQs, a glossary, advice on hardware and software, and a list of industry contacts.

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HIGH POWER AUDIO AMPLIFIER CONSTRUCTION

R. A. Penfold
Practical construction details of how to build a number of audio power amplifiers ranging from about 50 to 300/400 watts r.m.s. includes MOSFET and bipolar transistor designs

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Circuits. Data and Design

PRACTICAL ELECTRONIC FILTERS

Owen Bishop
This book deals with the subject in a non-mathematical way. It reviews the main types of filter, explaining in simple terms how each type works and how it is used.

The book also presents a dozen filter-based projects with applications in and around the home or in the constructor's workshop. These include a number of audio projects such as a rythm sequencer and a multi-voiced electronic organ.

Concluding the book is a practical step-by-step guide to designing simple filters for a wide range of purposes, with circuit diagrams and worked examples

88 pages

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With FREE Software: Number One Systems - EASY-PC

Professional XM and Pulsar (Limited Functionality) Richard Monk

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There is a 'blow-by-blow' guide to the use of EASY-PC Professional XM (a schematic drawing and printed circuit board design computer package). The guide also conducts the reader through logic circuit simulation using Pulsar software. Chapters on p.c.b. physics and p.c.b. production techniques make the book unique, and with its host of project ideas make it an ideal companion for the integrative assignment and common skills components required by BTEC and the key skills demanded by GNVQ. The principal aim of the book is to provide a straightforward approach to the understanding of digital

Those who prefer the 'Teach-In' approach or would rather experiment with some simple circuits should find the book's final chapters on printed circuit board production and project ideas especially useful.

250 pages (large format) Order code NE28 £21.99

A BEGINNER'S GUIDE TO TTL DIGITAL ICS R. A. Penfold

This book first covers the basics of simple logic circuits in general, and then progresses to specific TTL logic integrated circuits. The devices covered include gates, oscillators, timers, flip/flops, dividers, and decoder circuits. Some practical circuits are used to illustrate the use of TTL devices in the "real world".

142 pages

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DOMESTIC SECURITY SYSTEMS

This book shows you how, with common sense and basic do-it-yourself skills, you can protect your home. It also gives tips and ideas which will help you to maintain and improve your home security, even if you already have an alarm. Every circuit in this book is clearly described and illustrated, and contains components that are easy to source. Advice and guidance are based

on the real experience of the author who is an alarm installer, and the designs themselves have been rigor-ously put to use on some of the most crime-ridden streets in the world. The designs include all elements, including sensors, detectors, alarms, controls, lights, video and door entry systems. Chapters cover installation, testing, maintenance and upgrading.

192 pages

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MICROCONTROLLER COOKBOOK

The practical solutions to real problems shown in this cook-book provide the basis to make PIC and 8051 devices really work. Capabilities of the variants are examined, and ways to enhance these are shown. A survey of common interface devices, and a description of programming models, lead on to a section on development techniques. The cookbook offers an introduction that will allow any user, novice or experienced, to make the most of microcontrollers.

240 pages

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Project Building & Testing

ELECTRONIC PROJECTS FOR EXPERIMENTERS

R. A. Penfold

Many electronic hobbyists who have been pursuing their hobby for a number of years seem to suffer from the dreaded "seen it all before" syndrome. This book is fairly and squarely aimed at sufferers of this complaint, plus any other electronics enthusiasts who yearn to try something a bit different. No doubt many of the projects featured here have practical applications, but they are all worth a try for their interest value alone.

The subjects covered include:- Magnetic field detector. Basic Hall effect compass, Hall effect audio isolator, Voice scrambler/descrambler. Bat detector. Bat style echo location, Noise cancelling, LED stroboscope, Infra-red "torch" Electronic breeze detector, Class D power amplifier, Strain gauge amplifier, Super hearing aid.

138 pages

Order code BP371

FAULT-FINDING ELECTRONIC PROJECTS

R. A. Penfold
Starting with mechanical faults such as dry joints, short-circuits etc, coverage includes linear circuits, using a meter to make voltage checks, signal tracing techniques and fault finding on logic circuits. The final chapter covers ways of testing a wide range of electronic components, such as resistors, capacitors, operational amplifiers, diodes, transistors, SCRs and triacs, with the aid of only a limited amount of test equipment.

Signal Tracer, a Logic Probe and a CMOS Tester are also included. The construction and use of a Tristate Continuity Tester, a

136 pages

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PRACTICAL FIBRE-OPTIC PROJECTS R. A. Penfold

While fibre-optic cables may have potential advantages over ordinary electric cables, for the electronics enthusiast it is probably their novelty value that makes them worthy of exploration. Fibre-optic cables provide an innovative interesting alternative to electric cables, but in most cases they also represent a practical approach to the problem. This book provides a number of tried and tested circuits for projects that utilize fibre-optic cables.

The projects include:- Simple audio links, F.M. audio link, P.W.M. audio links, Simple d.c. links, P.W.M. d.c. link, P.W.M. motor speed control, RS232C data links, MIDI link, Loop alarms, R.P.M. meter.

All the components used in these designs are readily available, none of them require the constructor to take out a second mortgage

132 pages

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RADIO BYGONES

We also carry a selection of books aimed at readers of EPE's sister magazine on vintage radio Radio Bygones. These books include the Comprehensive Radio Valve Guides (five books with a Free copy of the Master Index) for just £15. Also Jonathan Hill's excellent Radio Radio, a comprehensive book with hundreds of photos depicting the development of the British wireless set up to the late 1960s.

The three volumes of our own Wireless For the Warrior by Louis Meulstee are also available. These are a technical history of radio communication equipment in the British Army from pre-war through to the 1960s.

For details see the shop on our UK web site at www.epemag.wimborne.co.uk or contact us for a list of Radio Bygones

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