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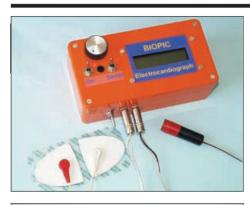
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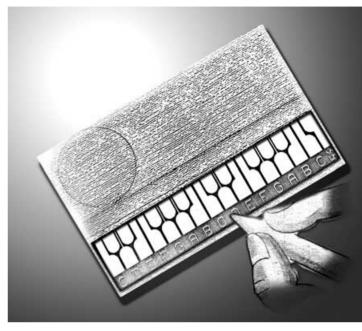
# **NEXT MONTH**

# EPE STYLOPIC

Rolf Harris is well known these days for his concern for sick animals. Back in the 1970's though, he was popular as an entertainer who would delight his audience by (amongst many other activities) playing the didgeridoo and the Stylophone. The latter was a compact hand-held electronic musical instrument having a built-in keyboard which was activated by an electrically conductive stylus.

The EPE StyloPIC pays homage to this classic design, which is now relegated to the depths of history and enthusiast's web sites (although we know some readers still have the real thing!). Whereas the original had 20 keypad zones, the StyloPIC has been extended to cover two full octaves – 25 notes including sharps and flats. It too is, of course, activated by a stylus, which makes contact with the integral keyboard-style p.c.b. that also holds the electronic components. Its tuning accuracy is superb, the software making use of a technique hitherto unpublicised through EPE.

Apart from the PIC microcontroller, there are only three active integrated circuits – a waveform converter, an envelope shaper and a power op.amp. It is really simple in its concept, yet remarkably fun to play!



# INFRA-RED AUTOSWITCH

The Infra-Red Autoswitch will find many applications, but it was designed initially to switch on the concealed lighting around a set of kitchen units. Simply waving your hand near the unit will cause the lights to switch on for a timed period. If you stay in the area the unit will remain triggered. The sensor employed is similar to the autoswitches used in up-market public wash hand basins and hand driers, where the water or air is switched on when your hand is in place. The system described here is triggered when your hand is about 30cm from the unit. The project was designed for maximum ease of assembly and fitting.

# USING THE PIC'S PCLATH COMMAND

Readers are probably very familiar with the PIC16x84, which has 1K of program memory in a single page. With this device, many readers will believe that for successful table calling, any tables need to be placed within the first 256 bytes of program memory, a restriction imposed for a reason that may not immediately be apparent. In fact, tables may be put anywhere in program memory space, once the operation of the program counter and the PCLATH function are understood. The use of PCLATH is also essential with those PICs which have more than 2K of program memory, the PIC16F87x family for example, since these higher program addresses cannot be directly accessed without it being set appropriately.

There are constraints, though, on setting the program counter through PCLATH, and which must be complied with for correct operation of the program, as this feature article explains.

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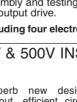


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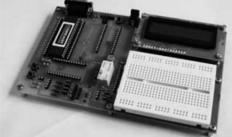
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#### VOL. 31 No. 6 **JUNE 2002**

### HELP PLEASE

It is very rare for us to request articles for EPE, in fact we actually receive or commission plenty of material to fill our pages. (Although we are always on the lookout for unusual or ingenious projects, so do let us know if you have such an item.) However, there is one project that has basically eluded us for many years and I would like to ask if anyone could assist us in our search for suitable information.

As a reader recently pointed out, many of our projects are repeated over a period of time but he had only once seen an ultrasonic cleaning bath project in the magazine, and that was more than 20 years ago. He is, of course, quite right, the problem being the lack of a suitable transducer at a reasonable price for hobbyist use.

So, if you work for a company that could supply such an item or if you know of a supplier please get in touch. What we are looking for is a fairly high powered piezoelectric ultrasonic transducer that can be fixed to a small stainless steel cleaning bath for cleaning small items like jewellry, watches, p.c.b.s etc.

### **ORIGINAL PROJECT**

The original design, back in the January 1980 issue of Practical Electronics relied heavily on some specially made transformers and, of course, an ultrasonic transducer, unfortunately soon after publication the company supplying the specialist parts had a fire and their stock was destroyed, so even that design was relatively short-lived.

This is the type of project that will attract interest over a number of years so, if possible, we need to find a reliable supplier and not just a few "surplus" transducers.

Commercial ultrasonic cleaning baths do seem to be quite expensive and we believe it should be possible to make a small bath reasonably cheaply provided, of course, we can find that elusive transducer.

Any ideas, anyone?

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# **Constructional Project**

BIOPIC HEARTBEAT MONITOR

# JOHN BECKER

Take a peek at the rhythmic waveforms that keep you ticking!

**B**EFORE joining *EPE*, the author was on a short-term contract with a lifesupport equipment manufacturer. Of particular interest were the heartbeat waveforms displayed on the heart defibrillator screens, which had to match certain software criteria before electric shock treatment could be given to patients.

The various categories of waveform could be simulated electronically, but in active service the system would monitor a patient's heart via chest electrodes.

### **BEATING HISTORY**

Inspired by the simplicity of the heart monitoring circuits, the author designed one for *EPE*. This was published in Feb/Mar '93 as the *Biomet* (which loosely translates as *Life Meter*).

PIC microcontrollers were unheard of to hobbyists at that time and the three-board design required 13 i.c.s. Heartbeats were monitored across the chest using simple electrodes. Pulse rates could be monitored separately by a handheld sensor that detected the opacity of the thumb, which changes as blood pulses through it. A 3<sup>1</sup>/<sub>2</sub> digit liquid crystal display (l.c.d.) showed the pulse rate. Data could also be output to a computer for waveform display.

### MODERNISED MONITORING

Although heart monitoring techniques may not have changed fundamentally since that design, the methods for processing the data have moved on dramatically. The Biopic design presented here takes advantage of a PIC16F876 microcontroller's capabilities and uses an alphanumeric l.c.d. screen, plus an electrically-isolated serial data link to a PC-compatible computer. There are only five i.c.s.

The probes and contact pads used are those sold inexpensively by major chemists for use with proprietary TENS (pain relief) machines.

The design can be used as a handheld unit without using a PC. In this role it outputs heartbeat waveforms to the l.c.d., which is used in the same graphical fashion as with the author's *Micro-PICscope* (simple oscilloscope) of April '00. The display also shows the heartrate in beats per minute



(BPM) and a real-time pulse beat via a flashing asterisk and l.e.d.

When used with a PC, the design switches off the l.c.d. waveform and outputs data as a 9600 baud serial stream to a program written for QB (QBasic or QuickBASIC). The Biopic software is selfcontained and does not require QB itself to be installed.

The QB program can be run in DOS mode or under Windows 3.1, 95, 98 and ME (other Windows versions have not been tested with it and no advice can be offered for them).



Example of a thumb-monitored waveform on the Biopic.

### PC WAVEFORM DISPLAY

A composite photo of several typical heart waveforms displayed on a PC screen is shown on the next page. Whilst the subject is believed to be in good health, a medical opinion of the waveforms has not been sought.

Normally a real-time waveform starts at top left (zone 1), continues to the right, recommences in zone 2, continues right, etc. At the end of zone 10, zone 1 is cleared and the display continues from the top again, each zone then clearing in sequence as the waveform starts at its beginning.

The BPM rate is calculated across each zone, and the value displayed at the screen's top right. This value is updated each time a zone line is completed. The BPM and a real-time calender/clock display are updated simultaneously.

At the bottom of the screen, details of the COM port in use are shown. The COM port address (shown here as COM 2) can be changed by pressing keyboard key <C> (more later). The baud rate and its format cannot be changed as this is set within the PIC software as well as in the QB program.

The span between screen data plotting points can be changed to vary the display detail, using the <+> and <-> keys. The

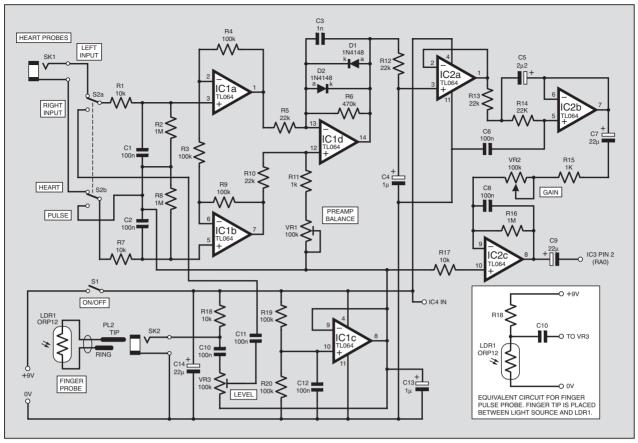


Fig.1. Circuit diagram for the analogue stages of the Biopic Heartbeat Monitor.

range is 1/1 to 1/50, the latter showing waveforms closer together. The sampling rate itself is not affected by these changes, the result is purely cosmetic on the PC's screen.

Pressing <H> turns on the "hold" function. The display then pauses when it reaches the far right of its current zone. Pressing any key then lets the display run again.

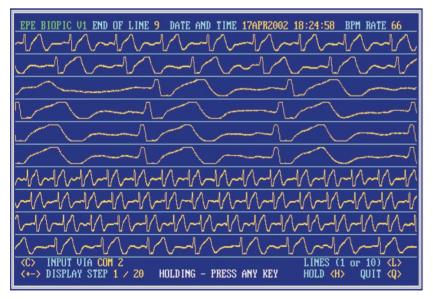
Pressing <Q> causes an exit from the program and a return to the screen from which it was first called.

Pressing <L> enlarges the display to cover just one nearly full-screen line. Pressing <L> again returns you to 10-line mode.

When run under Windows, waveforms can be "captured" for saving to disk and printing to paper (via Paint).

### ANALOGUE CIRCUIT

The Biopic effectively has two circuits, the sensor input and signal pre-conditioning circuit, plus the PIC-controlled display and data output circuit.



Composite of several heart waveform traces as shown on the PC screen. In normal running the waveforms run continuously.

The analogue circuit diagram is shown in Fig.1. Probes from the subject whose heart is to be monitored are connected into socket SK1. The circuit is not "earthed" for safety reasons and a differential amplification technique is used.

The probes are each connected separately to the non-inverting inputs of two op.amps, IC1a and IC1b, via resistors R1 and R7. Both inputs are biassed at about 4-5V (half the nominal 9V supplied by a PP3 battery) via resistors R2 and R3, which have a high value to minimise the voltage drop across R1 and R7.

A pumping heart is accompanied by a small electrical field across the chest. This can be detected by using two electrical contact pads and probes. One pad is placed below the heart, near the bottom left of the subject's rib cage. The other is placed on the subject's right towards the top of the chest. It is worth experimenting to find the best position.

The self-adhesive pads have terminals to which the commercially purchased probe leads are clipped. As the heart beats, the electrical field is detected as a voltage difference between the probes, and thus by their amplifiers. The difference in potential between the probes is extremely small and needs a considerable amount of amplification.

Initially, the difference is d.c. amplified by a factor of two, as set by resistors R4, R3 and R9, although this is complicated by the intercoupling between IC1a and IC1b via R3 (see *Teach-In 2002* Part 5 (Mar '02) for an explanation).

Further amplification of about  $\times 21$  is given when combining both paths via differential op.amp IC1d. The balance between the two signals can be set by

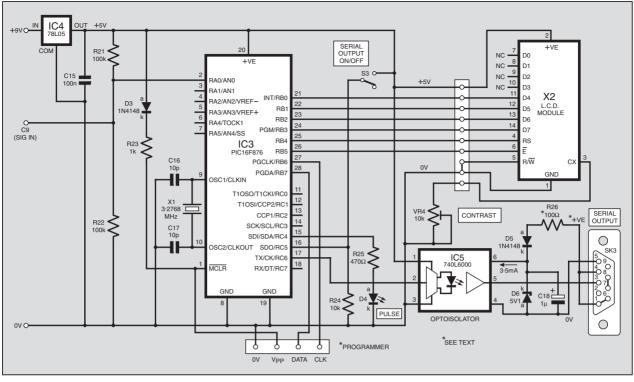


Fig.2. The Biopic's control circuit details.

preset VR1. High-frequency cut-off is provided by capacitor C3.

Still as a d.c. voltage, the signal is fed to the first stage of a low-pass filter via resistor R12, with capacitor C4 providing some preliminary upper-frequency cut. Heartbeats normally take place at around one per second, and seldom above two per second except under extreme conditions.

Whereas the previous *Biomet* used a switched-mode filter (MF10), Biopic uses a purely analogue filter. This is a variant of the Third Order Chebyshev low-pass filter from the *Filter Handbook* by Stefan Niewiadomski. It is formed around op.amps IC2a and IC2b and removes practically all frequencies above those that are to be expected from a heartbeat waveform.

Its output is a.c. coupled by capacitor C7 to the final gain stage, formed around IC2c. Here the gain can be varied between about  $\times 10$  and  $\times 1000$  by potentiometer VR2. Capacitor C9 gives stability to IC2c at high gain levels.

The final output is a.c. coupled by capacitor C9 to one of the ADC inputs (RA0/AN0) of the PIC microcontroller (Fig.2).

### THUMB PULSE

Still referring to Fig.1. The circuit can be switched to monitor a pulse beat detected via a thumb probe, detecting blood flow as mentioned earlier.

The familiar ORP12 light dependent resistor (l.d.r.) is used. This is biassed across the 9V/0V power rails via resistor R18. An equivalent circuit of the sensor configuration is shown in the inset of Fig.1.

As the blood pulses, the amount of light passing to the l.d.r. through the thumb varies. This cause the resistance across the l.d.r. to change minutely. The resulting fluctuating voltage at the l.d.r./R18 junction is a.c. coupled via C10 to potentiometer VR3. This sets the amplitude that is fed via C11 to op.amp IC1a and switch S2a. Amplification takes place as before, but with IC1b not used.

### CONTROL CIRCUIT

In the control circuit of Fig.2, the principal component is the PIC16F876 microcontroller, IC3. This is clocked at 3.2768MHz as set by crystal X1.

The amplified and a.c. coupled signal from IC2c is fed to the PIC's RA0/AN0 analogue-to-digital converter (ADC) input. The input is biassed to about 2.5V (half the PIC's nominal 5V supply), by resistors R21 and R22.

When no signal is present, the ADC conversion value read by the PIC is about decimal 128, out of a possible 8-bit range of 0 to 255 (the PIC's full 10-bit range is not needed in this design).

From the incoming values, the PIC calculates the BPM rate according to an internal clock. It does not attempt to determine which pulses are truly heart-generated and which have been caused, for example, by movement of the thumb on the l.d.r. The twin heart-probe path is less prone to extraneous conditions.

The pulse beats are displayed by l.e.d. D4, which is buffered by resistor R25 and controlled by PIC output RC4. The calculated BPM rate is output to the l.c.d. via lines RB0 to RB5, the display being operated in conventional 4-bit mode. Preset VR4 adjusts the l.c.d. screen contrast.

The software also outputs graphics data to the l.c.d. for display on line 1 in cells 1 to 8 as a simple waveform (as shown earlier and on the final page). The technique was first used by the author in the l.c.d. display for his *Micro-PICscope*.

Readers familiar with alphanumeric l.c.d.s will be aware that the display has several internal character generator registers that can be programmed by the user. Data is written from the PIC into these registers such that the pixels show a moving display of the heartbeat waveforms.

It has to be emphasised, though, that the l.c.d. shows less detail than can be displayed on the PC screen. Nonetheless, for simple handheld use of the unit, the l.c.d. can produce informative results.

### SERIAL OUTPUT

When the PIC detects that its RC5 pin has been set high via switch S3, it switches from l.c.d. output mode to serial output mode via pin RC6, feeding to opto-isolator IC5. This has an integral l.e.d. that is controlled by RC5 and buffered internally. The l.e.d.'s behaviour controls an opto-sensor coupled to IC5's internal buffer. The resulting logic level changes are output from pin 5 to the serial connector SK3, and thence to the PC.

The buffer within IC5 draws its power directly from the PC. The positive supply is passed via ballast resistor R26 and diode D5 to IC5 pin 6. Zener diode D6 ensures that the voltage across the buffer cannot exceed 5.1V and capacitor C18 smooths this supply. As a result, the data logic levels output to the PC stays within its acceptable range of nominally 0V to 5V.

The PIC16F87x series all have serial communications routines built into them, and the protocol (e.g. Baud rate etc.) can be changed as detailed in this PIC family's data sheets. The rate has been set at 9600 baud, 8-bit, 0 parity, 1 stop bit. This configuration data is also set into the Biopic's QB software that controls the PC's serial reception mode.

Each PIC-generated ADC conversion is sent to the PIC's serial data output register and then to the PC, which receives each data byte and processes it as discussed later. The l.e.d., D4, is not active in this mode.

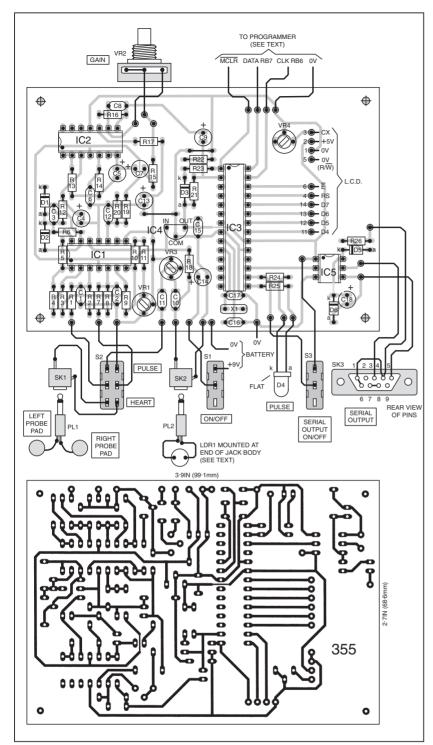


Fig.3. Printed circuit board component layout and full-size master track pattern, plus off-board connection details.

### **OPTO-ISOLATION**

The importance of using an opto-isolator cannot be over-emphasised. The most dangerous route that an electrical current can take across the human body is via the chest. The voltages and currents that can be lethal can vary between subjects, but can be as low as 50V and 10mA.

It is therefore imperative that any unit which requires a pair of electrodes to be secured across the chest when that unit is also connected to a mains powered piece of equipment, such as a PC, must provide *total electrical isolation* between them. Any designer must ensure that his units and their users cannot be harmed by mains power faults. The opto-isolator specified for the Biopic is stated by the manufacturer to provide isolation up to 2500V, well in excess of the 230V a.c. present on a normal UK a.c. mains supply.

The reason for using a serial connection between the PC and the Biopic was to simplify the isolation circuit. To have used the parallel port as the connection path (easier in some respects) would have required using an isolator for each data line.

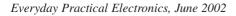
Also for electrical safety reasons, the Biopic MUST NOT be powered from a

	PONLINIO			
Resistors R1, R7, R17 R18, R24 R2, R8, R16 R3, R4, R9, R19 to R22 R5, R10,	10k (5 off) 옷님() ()			
R12 to R14 R6 R11, R15, R23 R25 R26 LDR1	22k (5 off) 470k 1k (3 off) 470Ω 100Ω (see text) ORP12 light dependent resistor			
Potentiomet VR1 VR2, VR3 VR4	ers 100k min. preset, round 100k min. preset, round, or panel mounting, lin. (see text) (2 off) 10k min. preset, round			
Capacitors C1, C2, C6, C8, C10, C11 C12, C15 C3 C4, C13, C18	100n ceramic, 5mm pitch (8 off) 1n ceramic, 5mm pitch 1µ radial elect. 16V			
C5 C7, C9, C14 C16, C17	(3 off) $3\mu 2$ radial elect. 16V $22\mu$ radial elect. 16V (3 off) 10p ceramic, 5mm pitch (2 off)			
Semiconductors D1 to D3,				
D5 D4 D6 IC1, IC2 IC3 IC4	1N4148 signal diode (4 off) red l.e.d. 5V1 400mW Zener diode TL064 quad op.amp (2 off) PIC16F876-4P microcontroller, pre-programmed (see text) 78L05 +5V 100mA voltage regulator			
IC5	740L6000 logic-to-logic opto-isolator			
Miscellaneo S1, S3 S2 SK1, SK2 SK3 PL1, PL2 X1 X2	us min. s.p.d.t. toggle switch (2 off) min. d.p.d.t. toggle switch 3.5mm jack socket (2 off) 9-pin serial connector 3.5mm jack plug (2 off) 3.2768MHz crystal 2-line x 16-character (per line) l.c.d. module			
Printed circuit	board, available from the			

COMPONENTS

Printed circuit board, available from the *EPE PCB Service*, code 355; TENS replacement electrode pads (2 off, minimum), plus twin connecting lead; 6-pin d.i.l. socket; 14-pin d.i.l. socket (2 off); 28-pin d.i.l. socket, narrow; 6.35mm plastic jack plug cap (see text); plastic case 150mm x 80mm x 50mm; PP3 9V battery plus clip; knobs (see text) (2 off); connecting wire; solder, etc.

Approx. Cost Guidance Only excluding chest electrodes



mains operated power supply of any type. Only battery power must be used. The safety of the person to whom the probes are connected must be the prime consideration. DO NOT IGNORE THIS SAFETY ADVICE.

### CONSTRUCTION

The component and track layout details for the Biopic, plus the off-board wiring, are shown in Fig.3. This board is available from the *EPE PCB Service*, code 355.

Sockets must be used for all d.i.l. (dualin-line) i.c.s, but do not insert the i.c.s until the correctness of the power supply regulator has been proved. Assemble the other components in any order you prefer, but it is recommended that they should be inserted and soldered in order of ascending size.

It is up to you whether you use preset potentiometers for VR2 and VR3, mounting them on the p.c.b. in the holes provided. The alternative is to use panel mounted rotary potentiometers with knobs. The former is neater, but the latter provides easy external control of signal amplitudes. On consideration, the author opted for VR2 as a rotary pot.

If you do not intend to use a PC with the Biopic, connect the lead that would otherwise go to the pole of switch S2 to the 0V connection.

A plastic case measuring 150mm × 80mm × 50mm was used for the prototype. A viewing slot for the l.c.d. was cut in its lid, and holes drilled for the controls and sockets.

### PROBES

The ORP12 l.d.r. for thumb pulse monitoring conveniently has a diameter that allows it to be inserted in the plastic "cap" of a 6.35mm (¼in) jack plug. Solder a suitable length (say half a metre) of twin lowvoltage cable to the l.d.r.'s pins, pass the cable through the hole at the end of the cap and gently push the device into the cap until it is flush with the rim.

Secure the l.d.r. with a suitable glue or adhesive-backed tape, such as insulating tape (as was done with the prototype). Connect a 3.5mm plug to the other end of the cable. Polarity does not matter.

The heart monitoring pads are likely to be sold in multi-packs – two pairs in the pack bought by the author. They are intended as replacements for use with proprietary TENS machines and their



Interior details of the Biopic enclosure.

existing cables. Spare cables can be purchased, however. Two were in the pack supplied for the prototype, having pressstuds that clip onto the pads at one end, and a 2-pin female plug at the other.

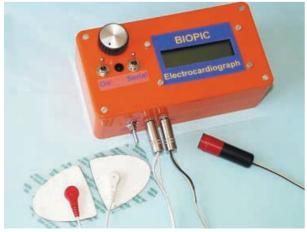
The author made no attempt to buy a suitable male connector for use with the Biopic. Instead, the plug was cut off and the twin wires soldered to a 3.5mm jack plug. Again polarity does not matter.

### PRELIMINARY CHECKS

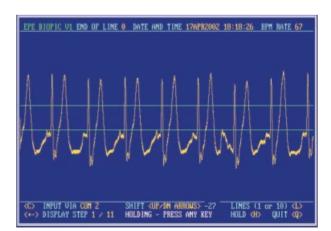
Having completed the board assembly, thoroughly check that all components are in the right place and that their polarity has been correctly observed as appropriate. Pay great attention to the soldering, especially for those components in the physical region of opto-isolator IC5. It is imperative to ensure that no solder bridges or "hairs" occur in that area. The isolation value quoted earlier only holds true if the manufacturer's pin separation distance is maintained. The board area in that region (above and below) must be scrupulously clean to maintain its own electrical isolation qualities.

With d.i.l. i.c.s and the l.c.d. omitted, apply battery power to the board. Check that +5V exists at the output of regulator IC4 and at strategic points around the board. If the voltage is not correct, within about five per cent or so, switch off and investigate the reason.

When satisfied, and with the battery disconnected, insert the remaining i.c.s., ensuring their correct orientation in the sockets. Connect the l.c.d. Apply power



Completed Biopic, plus heart monitoring probe pads, and thumb pulse detector.



Typical example of a heart waveform shown on the PC when the program is in "enlarge" mode. The two horizontal lines indicate the "trigger" zone used for assessing beat rate.

again and recheck that the regulator still outputs +5V.

If you have *Toolkit Mk2* or *Mk3 (TK3)* you can now program your PIC while *in situ*, making the required connections via the connections provided. The PIC configuration is for crystal XS, POR on, WDT off.

For those who do not have programming facilities, pre-programmed PICs can be obtained as stated on this month's *Shoptalk* page, which also gives details of obtaining the software itself.

### ACTIVE TESTS

Once the programmed PIC is running, adjust preset VR4 until a message is visible on the l.c.d. With mode switch S2 in the Pulse setting, line 1 should show the word BIOPIC on the right, to its left nothing recognisable is likely to be displayed. Line 2 should read PULSE RATE followed by a



Example of a heart waveform monitored across a subject's chest.



Another example of a pulse being monitored via the thumb probe (the lack of uniformity is due to the subject's slight agitation!).



The Biopic's screen while it outputs pulse waveform data to the PC.

numerical value and perhaps an asterisk.

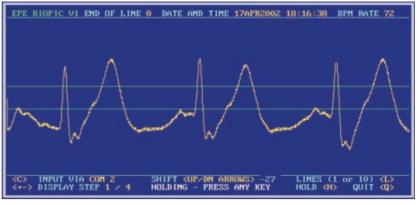
It is best to check the unit while using the "thumb-pulse" detector, with the first message on display. Plug the l.d.r. assembly into socket SK2 and switch S2 to "Pulse" mode.

Set preset VR1 midway and potentiometers VR2 and VR3 for maximum signal gain. Move your hand back and forth across the l.d.r.'s face in a reasonably welllit room, so putting the l.d.r. in and out of shadow. Observe l.e.d. D4 while you do so. It should flash on and off in response. If it does not, check that you have set VR2 and VR3 in the correct direction.

Looking at the l.c.d. screen, the asterisk at the right of line 2 should also go on and off in response to light changes. On line 1 a pulsed waveform should now be seen, corresponding to the high and low light levels as the shadow crosses the l.d.r.

Gently place your thumb across the l.d.r. face and point the top of your thumb towards the light. As your blood pulses, so the l.e.d. should flash, accompanied by meaningful detail on the l.c.d. screen.

You must keep your hold of the l.d.r. gentle and steady to avoid false triggering. It may either be necessary to come closer to the light or to reduce the setting of VR3 until an even l.c.d. waveform is shown,



Another "enlarged" waveform, displayed horizontally on a wider scale.

together with a reasonable BPM value on line 2. A certain amount of practice is needed to get the technique right.

### CHEST PROBES TESTING

To check the chest probes mode, switch S2 to Heart. Plug the heart sensor cable into its socket.

Attach the TENS monitoring pads to your chest, as described earlier, and clip the cable to them. Next, stop moving, relax and observe the l.c.d. screen. If you have positioned the pads on your chest correctly, you should see your heart waveform begin to appear on the l.c.d. It may take a few seconds to stabilise.

Adjust potentiometer VR2 until distinct pulses are seen. Your pulse rate should again be indicated numerically, together with a flashing asterisk, and the l.e.d. flashing in sympathy.

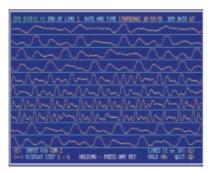
Again, it takes a bit of practice to remain in a sufficiently passive state for the display and BPM rate to remain stable.

Having set VR2 for what appears to be a reasonable gain setting, return to using the thumb monitor and if necessary readjust VR3 to compensate for the change in VR2's setting.

If you have an oscilloscope, preset VR1 can be adjusted to balance the gain between the two input signal paths. If not, leave VR1 set midway.

### PC MONITORING

Instructions for loading the PC with its program for monitoring the Biopic are to be found in the text file BIOPIC.TXT that accompanies the software. This file may be read via a text editor such as Notepad or MS-DOS Edit. Follow the instructions,



Composite of several typical pulse displays when the thumb pulse monitor is used. Slight involuntary movement of the patient causes unevenness.

connect a serial cable between the PC and Biopic, using the type required by standard modems.

Set the Biopic for thumb monitoring and switch S2 for serial output. Then run the program.

On first running, it may be that the software and PC have different ideas on which COM port to use. If the PC screen does not show a series of waveforms such as appear in the screen photos, press key <C> to change the COM address used by the Biopic's PC software.

Pressing <C> again reverts to the previous COM port address, of which there are two, COM1 and COM2. It is not known if PCs can have more than two COM ports – if yours has, consult its Help file for more information.

Do not change any of the settings within the Biopic's PC program unless you know what you are doing.

### SCREEN "DUMPING"

When the program is run from within Windows, the display can at any time be "captured" and pasted into the Windows Paint program, from where it can be saved to disk or sent to a printer (i.e. a "screen dump" – such as shown in on this page). Screen dumps cannot be created from within DOS, nor can other forms of screen output to disk or printer (there are methods to do so, but discussing these is beyond the scope of this article).

To capture a screen, press the Print Screen function key. Now press the Windows-symbol key, and select the route Programs > Accessories > Paint > Edit > Paste > Save As. If you are not familiar with Paint, explore its range of options.

### MEDICAL GUIDANCE

Biopic should not be used as a substitute for seeing your medical practitioner. It is not claimed that the waveforms it displays accurately mimic those that would be displayed by fully professional heart monitors. The displays are purely for interest. They should not be taken as an indication of anyone's true state of health.

If you have any reason to believe that anyone's heart is not behaving as it should, seek prompt medical advice.

### REFERENCE

Filter Handbook – a practical design guide. Stefan Niewiadomski. Heinemann Professional Publishing. ISBN 0 434 91378 2.

# New Technology Update Combining glass and silver allows a new type of nonvolatile high speed and very low power semiconductor memory device to be created. Ian Poole reports

**M**EMORIES are a key element in today's digital electronics scene. With the developments that are being undertaken there is a considerable pressure on all i.c. technologies to keep up with the pack. However, it is quite possible that the standard DRAM which is widely used may be overtaken by some emerging technologies such as polymer ferro-electric memory, magnetic RAM (MRAM) and Ovonic Unified Memory (OUM).

# Axon's PMCm

A new entry into this field by a company named Axon shows great promise. Called Programmable Metallisation Cell memory (PMCm) it uses a completely different approach to any memory technologies available now or to others that are also in development.

It uses solid state electrochemistry and it provides large non-volatile resistance changes in the substance. It has the ability to be scaled down to very small dimensions, allowing it to remain a viable technology for many years to come. It is thought that it will be possible to scale the technology down to below 10nm and this will bring with it the possibility of being able to have truly enormous levels of data density. Furthermore, it operates at a low voltage and this reduces the problems of heat dissipation that are encountered on some chip technologies.

# **Principles**

It is found that when certain glasses are combined with silver they enable silver ions to move very quickly over short distances under the influence of a high electric field. In effect this generates what could be looked at as a high ion mobility solid electrolyte.

Electrodes across the electrolyte are formed by using an anode of oxidisable silver and an inert cathode. Together with the solid electrolyte these form a device that normally has a high resistance but can be quickly switched to have a low resistance.

When a bias is applied, the silver ions are reduced at the cathode and the silver anode becomes oxidised. This results in the fast formation of stable conducting regions in the electrolyte, providing a conducting path between the anode and cathode. The cell can be returned to its non-conducting state by applying a reverse bias. This process is non-volatile and enables the cell to be in either a non-conducting or a conducting state. These states have a large resistance difference between them enabling them to be easily identified electrically, and providing an excellent basis for a highly reliable memory cell.

# Operation

Part of the key to the elegance of the new technology is its simplicity. The typical PMCm structure consists simply of a sandwich of a minimum geometry conductor, solid electrolyte, oxidisable electrode. Typically silver concentrations of more than 30% are used, and this provides high switching speeds and maximum stability in the "on" state. Despite the high silver concentration the off state resistivity is still very high and is typically around 100 ohm centimetres.

To switch the device a voltage in excess of 200mV is applied. For an "off" device this is applied so that the negative potential is applied to the inert contact and the positive to the oxidisable electrode. This is called forward bias and silver oxidation/reduction takes place as a result and a conducting pathway starts to form. The application of a voltage of as little as 200mV causes the local fields in the cell to be very high and electro-deposition will proceed very quickly, fed by the ion current in the device.

With this, the growing zone where the reduced silver forms and the conducting point close to the anode becomes the favoured area for electro-deposition because of the increased local field in this area. This has the effect of reducing the gap between the anode and the cathode which still further increases the electric field. This positive feedback mechanism speeds up the growth, and makes the switching very fast. Eventually a conducting bridge is formed between the two electrodes, putting the device into its low resistance or "on" state.

The characteristics of the device in the "on" state are simply those of the deposited link. The greater the amount of silver that has been deposited the lower the resistance of the link. Typically it might be of the order of 20 kilohms in view of the very small diameter of the link. Although this may seem high it is many orders of magnitude lower than that of the device in its "off" state.

To reverse the state of the device a reverse bias in excess of 200mV is applied. The electro-deposited silver is dispersed and the cathode becomes an oxidisable electrode again. The process is self limiting and terminates when all the excess silver in the electro-deposit has been oxidised.

### in use

It is obvious that programming the device is effected by applying either a forward bias to "write", or reverse bias to "erase" the cell. As already mentioned this occurs at less than a quarter of a volt. Reading the device must be achieved nondestructively if the data in the cell is to be preserved. One method is to apply a subswitching threshold voltage. Alternatively an extremely fast forward pulse can be applied. This would charge the capacitance of the cell in the "off" state or see the low resistance of the "on" state. It is then relatively easy to differentiate between the very small capacitance of the "off" state and the low resistance of the "on" state.

Programming is very fast and currently matches the speeds of today's DRAMs. Typical switching speeds of around 10 nano seconds are achieved. However the density of the new PMCm devices can be made very much higher. In addition to this the very low programming voltage or around 200 millivolts, switching current of around 200 millivolts, switching current of around 10 microamps and switching time of 10 nano seconds means that the energy required to switch the device is astonishingly low (around 20 femto Joules). This is a distinct advantage for high density devices where heat dissipation becomes a problem.

The reliability of the data is another issue. The data in the new memory does not require constant refresh and is therefore considered to be non-volatile. However the retention time depends upon how hard the data is written into the cell. Data written with the minimum amount of energy will not be retained indefinitely. If a higher energy level is used, closer to 1 pico Joule then data will be retained for many months.

### **Applications**

Work on the new memory device is progressing and no products are available at the moment although it is anticipated that the first products will be seen around 2004. The technology has been licensed to Micron with whom Axon has been working since 2000. When the devices are launched it is hoped that they will be comparable to DRAMs in terms of cost. This results from the fact that they have relatively low material costs, the device structures are simple and they use many existing fabrication techniques.

A further exciting possibility is that PMCm could be used alongside existing CMOS technology as the processes are compatible and the outputs from the cells and the inputs to them could be supplied by CMOS circuitry. In this way many more possibilities are opened up, but for the moment the main focus is on launching the basic memory cell onto the market.

For more information browse **www.axontc.com**.

# News . . . A roundup of the latest Everyday

# News from the world of electronics

# **IT'S MORE THAN THE MONKEY AT STAKE**

### If digital TV does become common-place reality, important existing analogue radio links could be lost, to our detriment. Barry Fox reports.

HIGH profile publicity for the ITV Digital's financial problems has completely overshadowed an important side-issue. Switching Europe onto digital television will switch off the radio links which theatres, concerts, business conferences, television studios and sports broadcasts now rely on.

The warning comes from the Joint Frequency Management Group, the government-approved body which licences the link frequencies. If anyone is in charge of digital TV in the UK, it is Culture Minister Tessa Jowell. Despite ITV Digital's predicament she still talks hopefully about the UK "being on track for analogue switch-off between 2006 and 2010". Even the government's own Radiocommunications Agency is worried about what this will do to the existing microphone links which it licences.

"No-one is reading the value of radio links to UK PLC", Dave Toman of the RA told engineers recently. "We need to shout louder to the DTI and Department of Culture Media and Sport about the contribution to GDP and jobs".

Radio microphones, in-ear sound monitors, talkback links and video links for portable cameras use analogue FM frequencies at the top of the UHF TV band.

One TV channel, 8MHz wide, can support eight microphones. Granada uses 40 links to record This is Your Life. West End musical shows use 50. Film-maker Robert Altman straps a radio mic to every actor on the set.

The DTI and DCMS recently suggested that the 10 TV channels at the top of the UHF band could be sold off for mobile radio (www.digitaltelevision.gov.uk). The new Independent Review of Spectrum Management (www.spectrumreview. radio.gov.uk) recommends auctioning to the highest bidder, trading surplus spectrum and raising costs "to the full opportunity cost level".

Paul Gill of the JFMG thinks the Review board did not understand what the Group tried to tell them, "Surplus spectrum is exactly what we depend on".

Says John Hesketh, Technical Co-ordinator for Granada Television, "If switching to digital television means we lose our radio links, there won't be any pro-grammes to put on digital television."

Ron Hope, Sound Supervisor for Carlton, thinks there is a very simple way to remind the government how much they rely on radio mics. "Let's just turn them all

off for a day. The government would then find it can't talk to anyone."

RA engineers have told JFMG its members should be developing digital links that work in microwave frequency bands. "First you tell me what work you have done to show it is healthy and safe to strap a microwave transmitter to your body for eight hours a day", challenges Aldo Hakligil Managing Director of microphone manufacturer Audio Engineering. "Unlike a cellphone these things are pumping out continuous power".

# **Triangular Heads**

PATENTS show that Intel has been working with the Indian Institute of Technology in Kharagpur on novel ways to reduce the amount of information needed for video conferencing (W0 02/03709).

Existing video compression systems divide each image into a mosaic of rectangles, compare how they change as people talk and transmit only the difference information. Because all the conference images are of heads, they can be divided into triangles which fit naturally over the curved surface. Some of these patches are then sub-divided into smaller triangles to capture more detail for key areas such as the eves and mouth. Larger triangles suffice for the cheeks and head top where there is less motion. So, overall, the system needs less digital code for realistic pictures.

**Barry Fox** 

# STRIPPING WITH SHESTO



SHESTO, renowned for their tools, supplies and equipment for technicians and craftsmen, have introduced a new precision wire cutter and stripper. It is said to be invaluable for practical electronics, computer and hi-fi applications, and equally well-suited for railway modelling, building radio controlled models and for other similar crafts. In fact, say Shesto, it is useful wherever fine control and accurate repeatability are the essential objectives.

The new combination wire cutter and stripper features a simple thumb adjustable cam for easy setting when working on desired wire sizes from 0.4mm to 2.6mm. Manufactured in the USA by the famous Xuron Tool Corporation, this quality tool is made from alloyed steel for durability and is ergonomically designed with cushioned hand grip and a light touch return spring for ease of use. There is optimum leverage for high performance.

Shesto's part number for this tool is PL0501 and the UK price is £12.95 including VAT.

For more information contact Shesto Ltd., Dept. EPE, Unit 2 Sapcote Trading Centre, 374 High Road, Willesden, London NW10 2DH. Tel: 020 8451 6188. Fax: 020 8451 5450. Email: sales@shesto.co.uk. Web: www.shesto.com.

# **SCREEN ESPIONAGE**

# Optical techniques can hack PC screen data even through closed curtains. Barry Fox reports.

**C**OMPANIES that have just spent a fortune on electrical screening to stop hackers eavesdropping on stray radiation from computers, now have something completely new to worry about – optical hacking of the plain text on screen. As we go to press, Markus Kuhn of the University of Cambridge Computer Laboratory was scheduled to tell delegates at the May 2002 IEEE Symposium on Security and Privacy, in Oakland, California, how it is done, and why optical eavesdropping can be as effective as radio-frequency snooping.

Even though the data stored by a PC can be securely encrypted, it must be deencrypted before display on screen. In 1985 Wim van Eck proved that a sensitive radio receiver could pick up stray electromagnetic signals from the screen and display it on a modified TV set. So screens, casings and cables are now screened to stop leakage, and office windows are treated with transparent shielding.

Security experts also know that a simple amateur astronomy telescope can read even small font text from a screen at 60 metres. So security-conscious offices keep screens away from windows.

Now Markus Kuhn, working with a European Commission grant, has found that it is possible to reconstruct the image on screen from the light emitted and diffusely reflected off walls, out through a window and even through curtains.

Most PC displays work on the raster scan principle, like a TV set. An electron beam scans the screen in lines, with varying intensity to change the brightness of light emitted by phosphors on the surface. The intensity of light tracks the video signal voltage; the scanning parameters for screen display rasters are set by the Video Electronics Standards Association, and freely available.

### **Afterglow Sensing**

Although screen phosphors have an afterglow of milliseconds, there is an immediate and detectable fall in glow intensity as the beam leaves a phosphor spot. These changes can be detected by off-the-shelf photodiode light sensors, developed for gigabit optical fibre communica-tion links and laser rangefinders.

The Cambridge lab ran tests with a PC monitor screen using a standard  $640 \times 480$  raster, with images displayed at normal 85Hz refresh rate, and the PC's screen brightness and contrast at the manufacturer's settings. Flickering light from the screen, bounced off walls and picked up by a detector, is electronically filtered to reduce random noise from ambient light. Light from mains lamps can be "tuned out" because it flickers at a fixed frequency, e.g. 100Hz.

When the detected signal was fed to another monitor screen, text was clearly legible. By using a telescope to home in on a patch of light reflected from the screen, eavesdroppers could work from 100m or more.

Exposing the risk, says Kuhn, helps people take counter-measures. Simple solutions are, not to work in a dark room, and choose fluorescent lights with similar phosphors to the monitor, so the screen light is swamped. L.C.D. panels refresh more slowly so they may be harder to track.

The paper which the IEEE will hear makes passing mention of an even more

intriguing security risk. Although screen phosphors show an initial and very rapid dimming, they retain some glow energy for minutes or even hours. It may thus be possible to read confidential information from a screen even after it has been switched off and the staff have gone home.

Do commercial companies and government departments recognise the significance of this?

I asked D. K. Matai who runs the high profile security consultancy called mi2g, which says it advises Lloyd's and the London insurance markets and "liaises with UK Government Departments on Digital Risk Management matters".

I was told I must put my question in writing, so I did.

Would you like to comment pithily on this? Are you shocked? Does nothing surprise you any more? Do you think companies are aware of the risks?

I am still puzzling over the statement I got back:

"The UK Government's Terrorism Act 2000 in which the disruption of key computer systems or interference with them was classified as terrorism has played a critical part in heightening awareness within the hacking community about cyber crime and its consequences."

Graham Cluley, senior technology consultant with security software company Sophos has a pragmatic view: "James Bond technology like this sounds cool, but it's usually a lot easier to rely on humans – like cleaners. Even if someone can't take home a floppy disk, no-one can wipe their memory of what they read on screen."

# NANOTECHNOLOGY

READERS interested in nanotechnology will be interested to know of a new service provided by Derwent Information, said to be the world's leading patent information provider. The service provides comprehensive information about global patents specific to the nanotechnology field.

Each patent is summarised in a clearlywritten abstract, highlighting the novelty, advantages and uses of the new invention. The volume of nanotechnology patents files worldwide has grown dramatically over the past few years and by browsing Derwent's site at **www.derwent.com** you should get a pretty good insight into how this amazing technology is progressing.

It is a technology that is used in industries such as aerospace, communications, semiconductors, and civil engineering and is based upon structures whose sizes are measured in nanometres – a billionth of a metre, that's 1/80,000th the diameter of an average human hair, or 10 times the diameter of a hydrogen atom.

# MICROCHIP CHARGES-UP



MICROCHIP, the manufacturers of PIC microcontrollers, have entered the standalone battery charging market with three high-performance charger i.c.s.

The Li-Ion battery charger devices, in the MCP7382x family, each have unique feature sets dedicated to single-cell battery-powered portable applications. The features include the ability to monitor charge current and temperature, to indicate a constant-current to constant-voltage transition, to maximise battery life and minimise system cost, and they additionally feature low shutdown ( $1\mu$ A) and operating current (250 $\mu$ A). The devices are available in 4-1V and 4-2V options, to accommodate various Li-Ion battery types. Packaging is surface mount, in SOT-23 and MSOP styles.

For more information browse www.microchip.com.

# **PIC C COMPILER**

R.F. SOLUTIONS has announce its ANSI C compiler designed for use with Microchip's PIC18xxx series of microcontrollers.

The compiler enables PIC designs to be developed and managed from within an ICEPIC environment or MPLAB. It is officially recommended by Microchip as a "preferred professional development tool", has an unlimited number of source files, a comprehensive library with source code and the ability to carryout mixed C and assembler programming.

For further information contact R.F. Solutions Ltd., Dept. EPE, Unit 21 Cliffe Industrial Estate, South Street, Lewes, E.Sussex BN8 6JL. Tel: 01273 488880. Fax: 01273 480661. Email: sales@rfsolutions.co.uk. Web: www.rfsolutions.co.uk.

# **PENNY & GILES**

LONG RENOWNED for the very high quality of their potentiometers, Penny & Giles tell us that they have changed their name to PG Drives Technology Ltd. They go on to say that their parent company, Spirent plc, has recently sold some of the aerospace component companies that also traded under the Penny & Giles group name, which is a well-known brand in aerospace markets.

This means that the Drives Technology aspect of the company can no longer use the original name. They feel that the new name is close enough to the old so that any potential confusion is kept to a minimum.

For more information contact PG Drives Technology Ltd., Dept. EPE, 1 Airspeed Road, Christchurch BH23 4HD. Tel: 01425 271444. Fax: 01425 272655. Web: www.pgdt.com.



# INGENUITY UNLIMITED

Our regular round-up of readers' own circuits. We pay between £10 and £50 for all material published, depending on length and technical merit. We're looking for novel applications and circuit designs, not simply mechanical, electrical or software ideas. Ideas must be the reader's own work and must not have been submitted for publication elsewhere. The circuits shown have NOT been proven by us. Ingenuity Unlimited is open to ALL abilities, but items for consideration in this column should be typed or word-processed, with a brief circuit description (between 100 and 500 words maximum) and full circuit diagram showing all relevant component values. Please draw all circuit schematics as clearly as possible. Send your circuit ideas to: Alan Winstanley, Ingenuity Unlimited, Wimborne Publishing Ltd., 408 Wimborne Road East, Ferndown Dorset BH22 9ND. (We do not accept submissions for IU via E-mail.) Your ideas could earn you some cash and a prize!



### WIN A PICO PC BASED OSCILLOSCOPE WORTH £586

- 100MS/s Dual Channel Storage Oscilloscope
- 50MHz Spectrum Analyser
- Multimeter 
   Frequency Meter
- Signal Generator

If you have a novel circuit idea which would be of use to other readers then a Pico Technology PC based oscilloscope could be yours. Every 12 months, Pico Technology will be awarding an ADC200-100 digital storage oscilloscope for the best IU submission. In addition, a DrDAQ Data Logger/Scope worth £69 will be presented to the runner up.

# L.E.D. Sequencer - Light Rider

WHEN asked by a friend whether it was possible to make a simple circuit to replicate the "bouncing light" effect used by the talking car "Kit" in the TV series *Knight Rider*, after some experimentation the result was the circuit diagram shown in Fig.1.

A timer is formed by IC1, a standard NE555 timer running in astable mode, with resistors R1, R2 and capacitor C1 determining its frequency. The output of IC1, at pin 3, is used to control a pair of 4017 divide-by-ten counters, with pulses fed into the clock input (pin 14) of IC2 and the Enable input (pin 13) of IC3.

The Q9 output (pin 11) of IC2 is used to clock IC3. Because Q9 is low when the circuit is initially powered up, IC3 does nothing while IC2 begins to count as normal which illuminates the l.e.d.s D1 to D9 one at a time from left to right.

When Q9 goes high, it disables any further counting on IC2 and it enables

counting on IC3 instead. As the outputs of IC3 are connected to the l.e.d.s in the reverse order the light seems to move from right to left.

Once a further clock pulse is received after Q9 output goes high, IC3 resets and the Carry Out pin (12) goes high, this is fed via the differentiating circuit of capacitor C2 and resistor R4 to the Reset pin (15). This disables counting on IC3 and the whole cycle starts again.

### **Clocking On**

This circuit could have many interesting uses other than in a "talking car", for example it could provide an electronic "pendulum" effect to a digital clock, or even as an eye-catching warning indicator. Consider using high-efficiency l.e.d.s for the display, if available. *Ian Hill, Plymouth*,

Devon

CIRCUIT IDEA? Earn some extra cash and possibly a prize!



WHY NOT

**SEND US YOUR** 

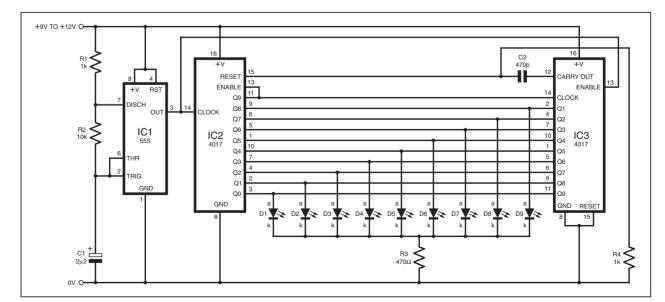


Fig.1. Circuit diagram for the L.E.D. Sequencer.

# Quiz Master - A Team Effort

THE circuit diagram shown in Fig.2 resulted from a practice exercise which the pupils of my electronics set and I designed at the start of school term. The brief given to my pupils was to design the old favourite of a Quiz Master to give a "who pressed first" I.e.d. display. Although the circuit does not provide a buzzer, one could be added if desired.

We used a CD4013 dual D-type flip-flop as it was realised that we could utilise their Set and Reset inputs as follows. In the "Ready" state both latches IC2a and IC2b have an output of zero and they are reset, which means that the inputs to IC1c are both *high*; therefore the output (pin 10) of this gate (labelled EN for Enable) is also high. When a contestant presses a button, switch S1 or S2, the output of IC1a or IC1b as appropriate goes high, which will set the appropriate latch. The output of that latch will go high and illuminate the corresponding l.e.d. D1 or D2.

At the same time, one of the inputs to IC1c will be taken low, and so Enable will go low, this prevents the other contestant setting their latch as further pulses are blocked by the AND gates IC1a and IC1b. Capacitor C1 in parallel with the Reset switch S3 provides a power-on reset pulse to ready the circuit for action. The values of the pull-down resistors R1 to R3 are not critical and the l.e.d. series resistors (R4/R5) should reflect the supply voltage used.

#### On the Buzzer

If an audio signal is required, the "Enable" signal may be used to switch on a buzzer

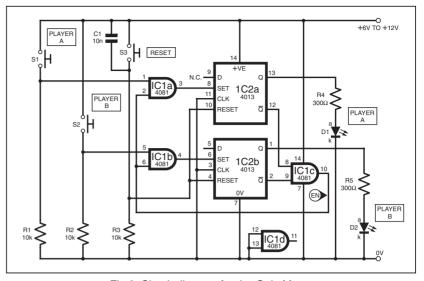


Fig.2. Circuit diagram for the Quiz Master.

(switch on when Enable is low), or for the more adventurous, each output,  $Q_A$ ,  $Q_B$  could activate its own, unique, buzzer. Alternatively, a single buzzer whose frequency could be controlled could be used.

To extend the circuit for n players, repeat the circuit segment IC1a and IC2a, and replace IC1c with an n input AND gate to provide the Enable signal. Some pupils used the Enable signal to trigger a monostable which would apply a reset pulse a few seconds after a button was pressed.

In addition to myself, the boys involved in the design were the members of form L3e, namely Oliver Russell, David Steynor, Patrick Overy. Patrick Almeida, Jon Dawes and Russell Garrett.

M. A. Burbridge, The Royal Grammar School, Guildford, Surrey.



Everyday Practical Electronics, June 2002

# **Constructional Project**

WORLD LAMP

# THOMAS SCARBOROUGH and SURYA RAO MATURU

Low cost lighting when there's no mains supply available

HE quest for a battery-powered lamp for lighting in poorer areas has presented an interesting and important design challenge for many years. Such a lamp should ideally be cheap, simple, efficient, and flexible – all at the same time.

With this in mind, the authors have designed a lamp which is made from inexpensive stock parts throughout, runs off a nominal 12V d.c. supply, and will power any ordinary fluorescent lamp between 100mW and 15W. It will power the equivalent of a 60W incandescent lamp for about 80 hours off a standard 12V car battery.

### CORE CONCEPT

Most d.c. powered fluorescent lamps use specialised components. While this may not pose a problem in major urban centres, it could pose serious supply problems in more remote areas of the world. The authors therefore searched for a means to unhook the World Lamp from the need for any uncommon or custommade parts.

This is accomplished in the present design through a.c. pulse-width modulation (p.w.m.), which is the core concept of

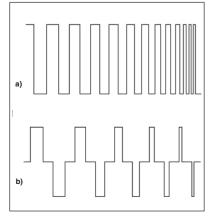


Fig.1. A.C. pulse width modulation.

the design. In short, the faster a pulse passes through an inductor, the more the inductor resists it (called reactance).

The simple equation which applies to a pure sinewave is defined by  $X_L$  (reactance) =  $2\pi fL$ , where f is frequency, and L is inductance. If, for instance, an inductor of 1H is used on a 50Hz mains supply, its reactance is  $314\Omega$ . At 100Hz, its reactance rises to  $628\Omega$ . This fact is obviously useful in controlling a.c. power.

However, instead of simply changing the frequency which passes through the inductor (see Fig.1a), the present design modulates the pulse-width (Fig.1b), thus making the circuit very versatile, and also minimising problems associated with power dissipation.

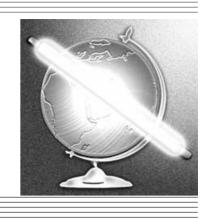
The practical implications of using a.c. p.w.m. are that a single circuit will power a very wide range of fluorescent lamps. Also, component tolerances may be fairly wide, since it is the pulse-width which is critical, not the components themselves. Supply voltage also becomes less critical, as p.w.m. compensates for voltage variations.

### WORLD LAMP CIRCUIT

The complete circuit diagram for the World Lamp is shown in Fig.2.

The circuit around Schmitt NAND gate IC1a is configured as an oscillator whose frequency is determined by the value of capacitor C3 and the total resistance across resistor R1 and preset VR1. The latter is used to set the square wave output frequency to between 60Hz and 70Hz.

From IC1a, the signal is routed in two directions, to IC1d and IC1b, the latter inverts the signal and sends it to IC1c. The circuits around IC1c and IC1d are identical and their input signals are the two complementary (opposite phase) signals from IC1a and IC1b. The two sub-circuits are configured as oscillators whose outputs can have their mark-space ratios simultaneously and identically changed. The



oscillators can only run when their control inputs are held high by the preceding gates.

To take the circuit around IC1c as the example, assume that the control input at pin 8 held high by IC1b. Capacitor C5 charges slowly via resistor R4 when the output of IC1c is high, until the voltage on input pin 9 crosses the Schmitt threshold, which causes the output to go low. Now the capacitor discharges at a faster rate via not only R4 but also resistor R5, diode D1 and potentiometer VR2a, which is used as a variable resistor. As a result, the time that the output of IC1c is low, can be varied in relation to the time that it is high.

Schmitt NAND gates IC2a and IC2b invert and buffer the twin pulse waveforms from IC1c/IC1d and their outputs are used to control the switching of power MOSFETs TR1 and TR2 in push-pull fashion. In turn, the MOSFETS drive the 6V-0V-6V dual winding of transformer T1, whose centre tap is connected to the +12V supply line.

The transformer is used in step-up mode and the resulting voltage across its output

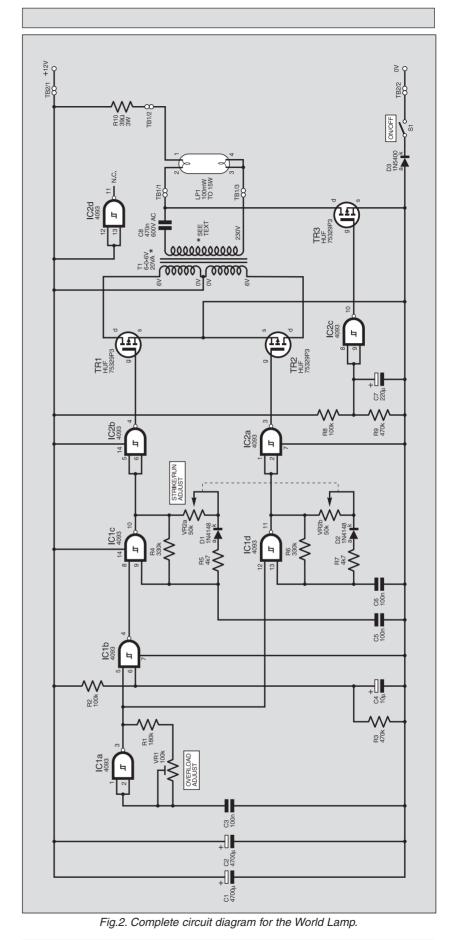


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winding is nominally about 230V peak-topeak, but with the power available dependent on the width of the pulse that drives the transformer. Thus by varying the setting of dual-gang potentiometer VR2a/VR2b, the power which reaches the fluorescent tube can be varied.

### SPECIFICATIONS . . .

- Supply voltage. 12V d.c. nominal, ±20 per cent. This was the subject of keen debate between the authors, as 6V batteries are more freely available in certain parts of the world. However, 6V would likely have ruled out the use of more common stock parts.
- All stock parts (off-the-shelf). The boldest design decision – also keenly debated – which of necessity meant partially reduced light output, and less than the maximum quoted tube life. Nevertheless, the lamp surpasses various budget designs surveyed by the authors. This was considered necessary to make the difference between a more obscure design and one that could be built the world-over with ease.
- Current consumption. Approximately 120mA for a 100mW fluorescent "glow lamp", rising to about 1.5A for a 15W fluorescent tube at full brightness. Tubes may also be dimmed, so that current consumption is much reduced.
- Tube types. Any fluorescent lamp rated 100mW to 15W, on condition that these *do not* contain an integral starter or ballast. This includes linear "strip lights", four-pin (but not two-pin) compact fluorescent (c.f.l.s), 2-D lamps, "05 colour" (insect killer) lamps, ultra-violet "black lamps" (you must observe the necessary precautions to protect your eyes), and miniature glow lamps and neon indicators.
- Light output. Between one lumen for a sub-miniature fluorescent glow-lamp, up to about 900 lumens (the equivalent of 75W incandescent) for a four-pin 11W single c.f.l. at maximum brightness.
- Starting method. Single-filament warm-start. While cold-starting has advantages of simplicity and circuit economy in particular, this causes "sputtering" in the tube, which shortens the life of the tube. The alternative is warm-start, which first warms the filaments in the tube. In the present circuit, the warming of a *single* filament vastly improves starting.
- Frequency. 60Hz to 70Hz. This was a necessary result of the "all stock parts" decision, and minimises iron loss in the transformer in particular.



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### ON STRIKE

Capacitor C8 in series with the output winding of T1 provides current limiting – but not too much, otherwise more "stubborn" fluorescent lamps may not strike. If more stubborn fluorescent lamps are used (i.e. which will not strike easily), the value of C8 may be increased – with caution. The value of C8 may also be reduced, to prevent accidental abuse of smaller tubes.

"Pre-striking" of lamps when power is switched on is prevented by the R2, R3, C4 arrangement on one input of IC1b. This holds pin 6 low for a brief moment during switch-on, for the duration it takes capacitor C4 to charge up via resistor R2 before the gate's threshold logic level is passed. During this time gate IC1c is disabled, so that only pulsed d.c. reaches the fluorescent lamp, as triggered via the IC1d path.

This greatly assists "striking" of the tube, since a unidirectional bombardment of ions first takes place, thus initiating conduction in the tube. A bidirectional bombardment can inhibit conduction.

It was found unnecessary to add any protection circuitry to the lamp. Only diode D3, in the 0V line, is added to prevent reversed polarity.

Transistor TR3 is switched on for half a minute at start-up to warm one of the filaments in the tube. While it is not entirely orthodox to strike a tube while warming a filament, this is common practice, and simplifies the start-up procedure. The warming of one filament can reduce the necessary start-up power by half, and thus greatly extends the life of the tube.

Although TR3 is directly connected to the high voltage section of the circuit, it does not close it, and is therefore unaffected by the high voltages present.

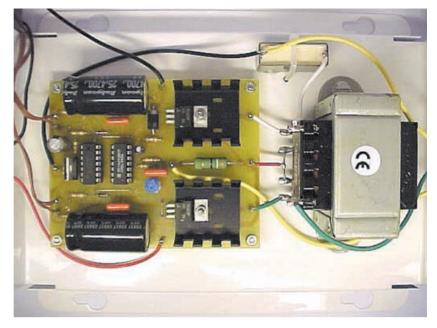
### **BENEFICIAL BLIPS**

In order for the pulse width modulation to work smoothly, IC1a must oscillate either at or below the frequencies of oscillators IC1c and IC1d, otherwise these begin to fall out of sync with IC1a. If the frequency of IC1a rises above that of gates IC1c and IC1d, "blips" appear in the waveform across transformer T1's windings, and the fluorescent lamp begins to "cycle", or blink.

This, however, has a very useful function. With the correct setting of VR1, as soon as the lamp is turned up too high, it begins to blink. This provides a cheap and effective means of preventing any serious wastage of power, which can easily occur with fluorescent lighting. Preset VR1 needs to be adjusted with the aid of an ammeter (see later), or is disabled by turning it back (anti-clockwise).

The operation of this "Overload Adjust" function may best be understood visually. The two oscilloscope traces in Fig.3 show the voltage across the fluorescent tube when the World Lamp is: a) powering a 10W linear "strip light" at full brightness, and b) is turned up too high, so that a "blip" appears in the pulse width modulation.

Note that in some cases the lamp may need to be turned up "too high" momentarily in order to "strike" the tube (that is, to ionize the gas inside). Once this has been accomplished, it is turned down to its optimal level, and ceases to blink. In many



cases a correct setting of VR2a/VR2b will cause the tube to strike without the need for readjustment, as "run" current disables the higher "strike" voltage.

Note that since the output of the World Lamp is continuously variable, it is also possible on occasion to light up an otherwise "dead" fluorescent lamp. This could squeeze a few more evenings of light out of a lamp which would otherwise be a lost cause. Life may also be extended sometimes by reversing a tube.

Lastly, although transformer T1 may sometimes be pushed beyond its ratings, in practice this should not present a problem, so long as power dissipation, and thus heat, is kept within bounds. The authors pushed several standard transformers way beyond their voltage and power ratings, without any sign of failure. However, iron loss increases at higher power, which means reduced efficiency.

### CONSTRUCTION

The World Lamp is built up on a singlesided printed circuit board (p.c.b.) measuring 100mm × 90mm. Details of the topside

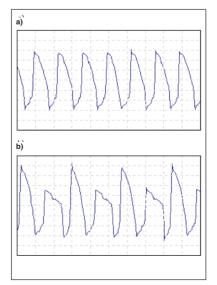


Fig.3. "Blips" across a 10W tube: (a) full brightness and (b) too high.

component layout, together with the underside details, are shown in Fig.4. This board is available from the *EPE PCB Service*, code 340.

Begin construction by soldering the seven link-wires. Note that three of these are soldered underneath IC1 and IC2. Continue with the solder pins and 14-pin dual-in-line (d.i.l.) sockets. Then solder the resistors and diodes, continuing with the capacitors and transistor TR3.

Be careful to observe the correct polarity of the electrolytic capacitors, and the correct orientation of the transistors, diodes, and i.c.s. Note that C1 and C2 mirror each other on the board. The cathodes of the diodes are banded. Capacitor C8 may be strapped to the case with a cable-tie.

Bolt transistors TR1 and TR2 to their two heatsinks, using heatsink compound, then solder them in place, to achieve the correct stand-off from the p.c.b. Since the transistors are MOSFET devices, these should be treated with due care (ideally, short out the pins with a paper-clip while handling).

Attach switch S1, potentiometer VR2, and transformer T1 to the solder pins provided by means of suitable lengths of sheathed and stranded wire. If desired, use a panel mounting potentiometer in place of cermet preset VR1, and attach this to the p.c.b. as well. Insert IC1 and IC2 in their sockets, observing antistatic precautions (touching an earthed item before handling them).

# BOXING UP

A metal enclosure is recommended, to help dissipate heat from the transformer in particular, and to minimise radio frequency interference (r.f.i.).

The p.c.b. and transformer are mounted securely inside with suitable nuts and bolts. If necessary, drill a few holes in the case to assist cooling. Fix S1 and VR2 (and VR1 if desired) to the front panel, adding a calibrated scale if preferred.

Ensure that all the circuitry surrounding the transformer's 230V winding is enclosed. If available, connect this winding to proprietary fixtures to hold the tube, using suitably rated mains connecting

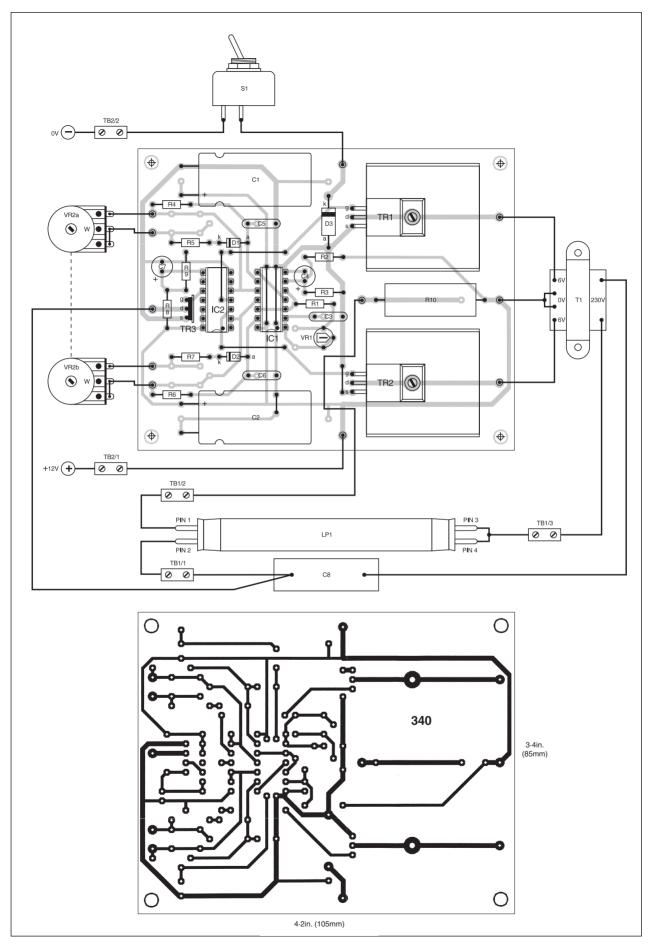


Fig.4. Printed circuit board component layout and off-board connection details, plus full size underside copper foil master track pattern.



Completed World Lamp in a robust well-ventilated metal case.

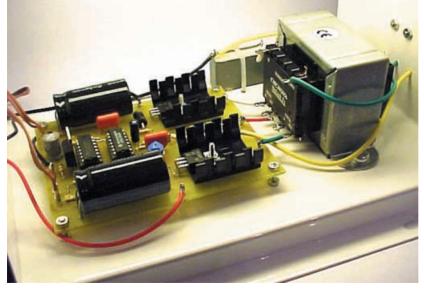
wire. Keep these wires relatively short, to minimise wiring capacitance. A 2-way and 3-way terminal block are used to connect wires to the battery and lamp, and in the prototype they were mounted on the outside of the case.

To prevent over-voltages in a tube through careless starting (thus causing sputtering from the filaments), optional presets may be wired in parallel with VR2a/VR2b to make up about 50k $\Omega$  in parallel – the equivalent of a rotation-limit stop. Provision is made for such presets on the p.c.b.

### EQUIVALENTS

Generally speaking, any rough equivalent to the components specified may be used. Where equivalent transistors are used, check the pinouts for correct placement on the p.c.b. Special note should be taken of the following:

- Potentiometer VR2a/VR2b. A dual potentiometer is ideal, but two single potentiometers may be used, with one or both of these being used to strike the tube. During normal running, both potentiometers are turned to the same position.
- Capacitor C6. A 600V a.c. rating is recommended, although slightly lower voltages may work without trouble.
- Transistors TR1 and TR2. Some care needs to be taken in the selection of these MOSFETs. They should be able to conduct at least 50W, and their "on" resistance should ideally be  $0.02\Omega$ . An "on" resistance of up to  $0.05\Omega$  may be used, but monitor heat dissipation carefully. Note that prices of equivalents may vary tremendouslv. Possible equivalents would include: IRFZ44N, IRL3202, IRFU3303, HUF75321P3 and (likely to run hotter) BUZ22, IRFZ34E, RFP25N06 and IRL2703.
- Transistor TR3. This may be the same as TR1 and TR2, or virtually any MOS-FET which is rated about 0.5A or more.
- Gate IC1. The Motorola version of this Schmitt trigger NAND gate is recommended (MC14093BCP), although any make should suffice. Do not use the 4011 quad NAND gate as this is not a Schmitt trigger device.
- Transformer T1. The VA rating should be at least double that of the fluorescent lamp wattage to prevent overheating – e.g. an 11W c.f.l. tube should use at least a 22VA transformer (11VA or 1.8A per low voltage winding). Other close similar secondary voltage ratings may be used, particularly 5V-0V-5V. Secondaries having too high a voltage



Interior view of the World Lamp, showing the relative positions of the printed circuit board and transformer.



The World Lamp can drive fluorescent tubes from 100mW to 15W.

rating, though, may not strike the fluorescent tubes, especially the larger ones.

СОМ	PONENTS		
<b>Resistors</b> R1 R2, R8 R3, R9 R4, R6 R5, R7 R10 All carbon film	180k         See           100k (2 off)         SHOP           470k (2 off)         TALK           330k (2 off)         Page           39Ω 3W         0-25W 20% except R10		
Potentiomet VR1	<b>ers</b> 100k cermet preset		
VR2	(see text) 50k dual-ganged rotary carbon, lin		
Capacitors C1, C2 C3, C5, C6 C4 C7 C8	4700μ radial elect. 25V (2 off) 100n polyester (3 off) 10μ radial elect. 16V 220μ radial elect, 16V 470n 600V a.c. rated		
Semiconduc	(see text)		
D3	(2 off) 1N5400 rectifier diode HUF75329P3 <i>n</i> -channel power MOSFET		
IC1, IC2	(3 off) (see text) 4093 quad Schmitt trigger NAND gate		
Miscellaneo			
S1 T1	s.p.s.t. toggle switch, 3A 6V-0V-6V 20VA mains transformer, chassis mounting (see text)		
Printed circuit board, available from the EPE PCB Service, code 340; heatsink, 20°C/W or less (2 off); terminal blocks PVC or ceramic, 500V a.c. 3-way and 2-way; fluorescent lamp and fittings (see text); metal case to suit; 14-pin d.i.l. socket (2 off), connecting wire; terminal			

Approx. Cost Guidance Only excluding batts

pins; solder, etc.

• Heatsinks. Any 0.5mm or greater thickness metal may be cut and drilled. Note that the two heatsinks should never touch each other.

### SAFETY PRECAUTIONS

The World Lamp produces high voltage, up to and exceeding 400V a.c. This is hazardous, even though the current is limited, and is capable of giving a nasty, potentially fatal, shock.

Contact with all circuitry surrounding transformer T1's 230V winding should be carefully avoided when the unit is on. If you need to work on the World Lamp when it is on, use insulated tools, and work with one hand behind your back.

High voltage circuitry should be fully enclosed and, as far as possible, proprietary fittings should be used to hold the fluorescent tube. Failing this, suitable lengths of plastic tubing may be tightly fitted over the ends of a tube to prevent fingers from touching the terminals.

Fluorescent tubes contain a small amount of metallic mercury, so contact with any broken glass would best be avoided.

### IN USE

Begin by turning back (anti-clockwise) VR1 and VR2 completely. Attach a fluorescent lamp to the circuit, exactly as shown in Fig.4, observing full safety precautions. Colour-coded wires were used in the prototype.

To calculate the circuit's likely power consumption, note the wattage of the tube on test. Add 20 per cent to this (e.g. 10W + 20% = 12W). Then divide by 12. This represents the approximate power which the circuit should be drawing while running normally (this might differ from tube to tube, and even drop as tube filaments warm).

Power up the circuit, using an ammeter in series with a d.c. power supply to monitor current consumption. If you have a choice, set this to a 2A or higher output setting. Use an adequate power supply, otherwise the lamp may flicker. It is suggested that a modestly rated tube be used to begin with, say 6W.

On start-up, the florescent tube should briefly flicker, then gently glow. Current consumption should initially be between 200mA and 300mA. If not, switch off and recheck the circuit.

Now gently turn up (clockwise) VR2. At a certain point, the tube should "strike" – that is, its brightness should suddenly and dramatically increase. Then it may be necessary to turn VR2 back to find the normal running current. Note that one filament is warmed for half a minute at start-up. If the tube does not "strike", you may have exceeded this period. In this case, switch off the lamp for five seconds, then start again. The filament draws around 200mA current.

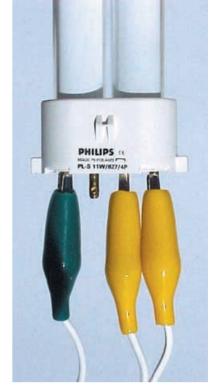
Once the lamp is running satisfactorily, gently turn VR1 (Overload Adjust) clockwise until the tube begins to blink or flicker. Then turn back slightly so that it just stops flickering. If VR2 is now turned up too high, the lamp will blink again. VR1 may be disabled by turning it back (anticlockwise) – but not too far, otherwise the lamp may again flicker slightly due to reduced frequency.

For the first hour, carefully monitor heat dissipation in the circuit, since heat can slowly build up. The transformer may become warm, as well as transistors TR1 and TR2, and diode D3. The transformer in particular ought to run below 50°C.

If you are sure that there is no chance of reversed polarity, D3 may be omitted for improved efficiency (its absence will release seven per cent more power, and may help to strike tubes up to about 20W). The circuit may buzz gently. This is nor-

mal for a fluorescent lighting circuit.

While the approximate cost is shown in the components list as  $\pounds 20$ , this represents a conservative first-world estimate.



Test clips on an 11W c.f.l. assembly.

The one-off cost in poorer regions of the world is likely to fall substantially below this.

The following Internet resources were found to be particularly helpful, and may be referred to:

Lights and electronics – www.epanora ma.net/links/lights.html (links)

Sam's F-Lamp FAQ – www.misty.com/ people/don/f-lamp.html

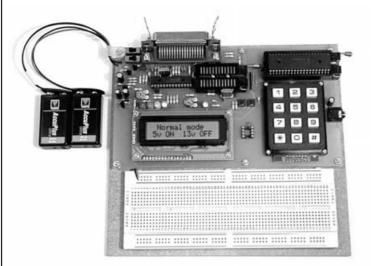
Fluorescent lamp inverters – www. misty.com/people/don/flvdc.html

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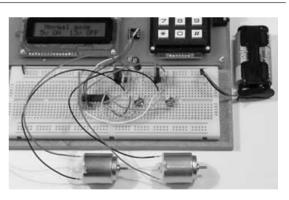
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The 2nd edition has two new chapters. The PIC16F627 is introduced as a low cost PIC16F84. We use the PIC16F627 as a step up switching regulator, and to control the speed of a DC motor with maximum torque still available. Then we study how to use a PIC to switch mains power using an optoisolated triac driving a high current triac.

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John Becker addresses some of the general points readers have raised. Have you anything interesting to say? Drop us a line!

# WIN A DIGITAL MULTIMETER

A 3<sup>1/2</sup> digit pocket-sized I.c.d. multimeter which measures a.c. and d.c. voltage, d.c. current and resistance. It can also test diodes and bipolar transistors.

Every month we will give a Digital Multimeter to the author of the best *Readout* letter.



# ★ LETTER OF THE MONTH ★

#### ALARM CONSIDERATIONS Dear EPE

I notice that the inputs to the *PIC Controlled Intruder Alarm* (Apr '02) are directly interfaced with the microcontroller via resistors. This means that there is no hardware RFI or surge (transient voltage) protection on the inputs. I note that the author, John Becker, asks for any reader comments on this.

On many circuits I have seen, three-terminal capacitor filters have been used to counteract RFI problems in hardware. Software solutions don't always work in my experience. However, my major concerns would be over transient surges.

Transients from motors, relays and even lightning could pose a more serious threat to the PIC as the high voltages could cause premature failure of the device. This could be difficult to detect as transients could damage the PIC along with stored data and other hardware. Normal practice would be to use a metal oxide varistor or a decent Zener diode (or something similar) to clamp the high voltages.

The EMC standard for alarm systems (BS EN 50130-4) requires that commercial systems be tested with slow and fast transients of IkV for signal inputs and outputs. This is higher than many other EMC standards due to the threat of false alarms on both intruder and fire alarm systems. This test has nothing to do with malicious use of electromagnetic pulses to

destroy the integrity of the system, as this would be covered under a functional standard.

I suppose that I should say that, having just taken the hobby to professional leap, I have noticed a number of things that are mandatory in commercial designs, but often left out in hobbyist designs, including EMC. However, I consider that people who are in (or considering) the hobby to professional transition would benefit from this information – I would have without a doubt. As the constraints are simply not there in hobby design, it would just be "best practice" rather than mandatory.

Many thanks for an invaluable and indispensable magazine. Keep the current balance between hardware and software: without hardware skills, we would not be able to use microcontrollers in the first place!

Lyn Jones B.Eng. (Hons), AMIEE via email

Thanks for your various comments, Lyn. By our nature, we publish designs principally from hobbyists and do not expect them to conform to all commercial niceties, though wet to make sure they do not break any rules, and we do publish feature articles that look periodically into deeper design considerations.

The input side to this design is based on that used in the Intruder Alarm Control Panel of Apr/May '01, which was designed to meet British Standards specification BS4737.

Thanks Guy. Yes, fortunately throughout the ages many rebels have emerged who question the "orthodox" opinions of their day, resulting in discoveries that profoundly changed our understanding of "life, the universe and everything". Marconi is but one name on a very lengthy list of illustrious technology-changing investigators who emerged once Aristotle's influence receded.

### EPROM ERASER

Dear EPE,

I have a PIC development kit for programming various PIC's (in particular the 16C57). To erase the program on this PIC, ultra-violet light needs to be used. However, they cost about 170 Euro. Do you know of any better value EPROM erasers. Would it be practical to just buy a UV light and place the light and the PIC together into a box?

#### Paul McGovern, via email

UV erasure is not something we've done for many years, Paul, and we can't help you on it. I would comment though that UV lights for p.c.b. prototypes and those for EPROM erasing are not the same, each using very different UV wavelengths. I once tried to erase an EPROM with my p.c.b. exposure unit, and failed to do it, even after 24 hours. Many years ago I bought a UV erasure kit from Magenta – ask them if they still do one.

Incidentally, always remember never to look directly at the UV light, which can be damaging to evesight.

### AUSTRALIAN SKILLS SHORTAGES

Dear EPE,

I am currently in Adelaide, South Australia. I have been following the recent discussions relating to both employment and skills shortages in the UK with interest. Coming originally from the UK some 37-odd years ago, I have never lost the feeling that the UK was "home" for me. Some people will probably say "why?" but I wish to return there with my new wife. The problem is – employment.

I have been involved in electronics ever since I was old enough to use a soldering iron, did my training with the New Zealand Post Office as a Telephone Exchange Tech, moved to Australia and held various jobs all in the electronics field as a tech. I've run my own business designing, prototyping and building electronic equipment for 15 years and a couple of years ago decided to have a break and work as an employee for a while. Big mistake!

Despite all my years of knowledge (I kept myself up-to-date in the business), at 44 nobody wanted to know me. There were jobs advertised out there and I thought maybe I was just too old. Eventually I got a casual job, at the equivalent of four pounds per hour as a senior testing technician and was working with people in the 18 to 26 age group. They too were having trouble getting permanent jobs. In conversations with them, they had no problems getting into courses and getting their qualifications, apparently the classes were and are still quite full. So I would like to know, who are actually getting the supposedly advertised jobs?

Do they genuinely exist? We out here seem to have plenty of qualified people, or is it the old "must be 16 to 18 yrs old with experience" roundabout? One day the employers will wake up, but by then it will be too late because, judging by your experiences in the UK, the young have worked out that better money can be made elsewhere along with better job security and satisfaction.

Terry Mowles, via email

It's interesting to have an overseas viewpoint on this subject as well, Terry, thank you.

### I.C. DATASHEETS

Dear EPE,

As a regular reader of *EPE*, I am writing to see if you can help me to track down a source of data sheets for some i.c.s. This information is for my 14-year-old godson who I introduced to electronics a few years ago, and who is now trying to design circuits with components he has procured.

#### Jim Bradshaw, Preston, via email

I find that manufacturer's web sites are normally the best source for data sheets, which can be downloaded free. I think the ones you quoted in your email are Philips. I suggest you search via www.google.com on Philips for their i.c. site. When a manufacturer is not known it is worth searching generally via Google on the i.c. numbers, it's amazing how often such a search produces results.

#### MARCONI AND ANTENNAE Dear EPE

Ian Poole's excellent article on Guglielmo Marconi (Dec '01) makes the important point that he was not a theoretical scientist. If he had been he would not have tried to transmit across the Atlantic Ocean. It was generally thought that Herzian waves travelled in more or less straight lines and it was known that they did not pass through water. The theoretical physicists pointed out that if the earth's curvature was taken into account there was a 50 mile high mountain of water between Poldhu and Cape Cod so that there was no chance of any radio waves crossing the ocean. However, Marconi was not one to be put off by experts and no one knew of the existence of the ionosphere but which allowed his tests to work.

Marconi never used an antenna. This was a word introduced into the technology by the Americans, many of whom deny many of Marconi's achievements. The classic book on the subject *Antennas* by Professor Kraus does not contain the name Marconi although Yagi, Booker and many other pioneers are mentioned. The same applies to the many works of Professor Terman.

Marconi's early patents refer to "an aerial conductor" which he and his colleagues abbreviated to *aerial*. This was the term in common use in the UK until the 1939/45 War brought an influx of US equipment. I doubt whether Marconi ever considered an antenna to be other than the sense organ of an insect.

Guy Selby-Lowndes, Billingshurst, Kent

### **ENLIGHTENING JAVA**

Dear EPE, I hope that your readers will be interested in my suite of Javascript/HTML programs. Two of them, temperature conversion (actually intended for hygrometers) and frequencies of musical notes, were inspired by the November edition. The musical notes program, in particular, gives a display rather like page 808 Fig.7 of the Pitch Switch, but can be set for a wide range of octaves

from the sub-audible up to ultrasonic and beyond to VHF! Also available are: reactance, resonance, trigonometry, quadratic equations and the coded character set used in HTML.

Readers wanting any (or all) of these should send a formatted 3.5in IBM floppy with return (pre-paid and pre-addressed) mailing facilities. There is no charge, but because the software is copyright, readers may only install it on one computer and must not copy or give it to anyone else. Feedback as to performance is welcomed.

All run in a web browser that can interpret Javascript. I tested the software using Microsoft Internet Explorer 5.

I would also suggest that the best use of the Lights Needed Alert (Nov '01) would be to always switch on side lights as part of the "afterstart" check. The "lights on" feed at TB2/3 would then be from a pair of diodes, cathodes together to the R8/C8 junction but each anode going to one filament of the headlights (dim and main beam respectively). The warning then sounds when it gets dark and the headlamps (not side lights) need switching on. Regrettably, it won't work this way on my car as it has the dim-dip system. Dr G.L. Manning,

63 The Drive, Edgware, Middx, HA8 8PS

Thank you, Doctor, for the offer and suggestion.

### ASTEROID MONITORING

Dear EPE

Thank you for your excellent magazine. Wonderful ideas and ingenious adaptations. I have a potential project that I want to discuss with you. As you may remember, a very large asteroid recently passed close by. Astronomers didn't know it until it was about 5,000,000 miles away and there-in is my question and idea. First, how did they discover it? I assume it was from continual searching with passive receivers (telescopes) and not radar units. If so, then the mechanism used was the eyes, or maybe sensitive film exposed over a "period" of time allowing for comparisons to detect movement.

If human eyes can detect the asteroids, it must be because: (1) they reflect light; (2) block out light behind them (like a moving black hole); or (3) generate light/energy, or, maybe, some combination thereof. And that's my idea.

Just like a photoreceptor reacts to light - or the absence of it - couldn't a way be found to focus the sensitivity of a device, using large (3-metre) backyard C-Band TVRO antennas (available everywhere for free in the US), so that as an object blacks out the light behind it, a sensor would activate and record it. The sensor would, of course, pick up airplanes, falling trees, maybe even fog, but with the right sensors and computer power, maybe it could be adapted not to sound a siren but instead select a different electronic wavelength to monitor. Infrared? Visible? Near infrared? Background noise disrupted by a passing object . . . ?

I don't know what the "trigger event" would be, but if it's true that the majority of asteroids are located by backyard telescopes and amateur scientists – like those reading EPE – maybe electronics could be developed to substitute. What do you think?

#### Dave Mynatt. Manchaca, Texas, via email

I thanked Dave for the suggestion and commented that radar is probably used in astronomy for asteroid monitoring, rather than optical techniques. Not the sort of thing that really seems feasible for us to design a project for. I suggested he should ask one of the astronomy

mags for more info on how it's done, or browse the web. He replied:

I searched the web and found that both are used, with visual a predominant methodology. Radar has too many limitations, in terms of range, band spread, filtering. So a bunch of folks get together and search the skies with telescopes, comparing one night's star positions to another, looking for streaks and smudges and new things.

I'm not sure anything can be done, so just wanted to get your take on it. What do you think if it is an optical design?

I really don't know Dave, it seems that optical techniques need very high magnification, maybe beyond the scope of amateurs, and that the sky area searched on any scan becomes smaller the greater the magnification, hence lots of people with similar equipment would be needed, as you say.

Some years ago I designed a CCTV camera that would respond to bright starlight and I was able to view a few stars through my PC, but it certainly would not have been up to asteroid searching. Cryogenically-cooled electronics is used professionally when faint star images need to be monitored.

It's possibly not really a subject for a hobbyist electronics mag, but an interesting one nonetheless. Let's see what readership reaction might be.

#### **MSF REPEATER**

Dear EPE.

I live in Capetown SA, and have built Andy Flind's MSF Signal Repeater (Jul '01). My problem is that the antenna is not readily available here. Can you inform me how to build the antenna myself. I will need specs on the ferrite core. the wire used and the tuning cap value. How many turns are needed on the core. I would really appreciate this help from you to help me complete this project.

#### Donovan Robertson. South Africa, via email

Andy Flind replies to Donovan:

At such low frequencies as 60kHz all one needs is a bit of ferrite rod, some enamelled copper wire and a suitable capacitor, say a silvermica of around 470p. I'd try driving the circuit with a signal generator through a series resistor of about 10k whilst observing the amplitude across the tuned circuit with a scope. This would get broadly into range, though final trimming would be required, if only to compensate for the removal of the capacitance of the scope lead. There's a lot of trial and error in this sort of stuff, and needs a little patience.

Andy Flind

#### P.C.B. SIZES

Dear EPE

I subscribe to EPE and download it electronically as a pdf. I've recently tried making a couple of p.c.b.s using printouts from the magazine, only to find that they are coming out at a reduced scale. My printer is set for 100% printing. Any ideas how I can overcome this problem.

#### Dave Stacey, Northampton College, via email

The page size of the Online issue is reduced to American A4 so that pages can be printed on that size paper. Also various printers will distort the original size which is why we give the size of the p.c.b. alongside the image. If you are lucky enough to have some graphics software, i.e. Photoshop or Corel Draw, then you should be able to import the image and resize it before you print it. Alternatively, you could use an enlarging photocopier, printing onto OHP (overhead projection) film, which is translucent enough to use in a UV exposure unit. I use it all the time when making my own prototype p.c.b.s via PC printouts, it's excellent. Major computer retailers sell it, and possibly also office stationers.

### **GRAPHICALLY SPEAKING**

Dear EPE

I design and build loudspeaker enclosures and as you know most of the work is done in testing and measuring of the loudspeaker parameters. So when I saw your PIC Graphics L.C.D. Scope of May '01 I thought to build it to replace my old equipment. I see in previous articles you make light of the Lissajous mode, but in my case it allows one to easily and precisely find the speaker or crossover network's resonant frequency (zero phase angle).

Is it possible to enable this feature with this scope? If it requires two inputs one could use the spare opamp IC1b. All one needs is to see the Lissajous circle collapse to a straight line which indicates that zero phase or that the circuit is resonating.

Looking on the Web, one will notice that the majority of electronics involves audio and the building of amps and speakers, so test equipment will be of much interest to all concerned. I see you have just published a Spectrum Analyser (Feb '02) for the PC, but I would like to see a portable version similar to the Graphics Scope but displaying a bar type or VU display.

I was busy designing an 8-channel one that would use l.e.d.s for the display but think this is rather outdated when one sees your designs using l.c.d. graphics displays. The VU display would use the usual eight filters multiplexed by a 4051 under control of the PIC.

I am also building an 8-channel graphic equalizer that will be used in measuring room response and aid in the setting up of hi-fi systems.

I hope that this inspires you to consider designing the above ideas which I am sure will stimulate much interest from the audio fraternity. Mike MacLeod, via email

Thanks for the suggestions which I'll give thought to (but can't promise!). Lissajous I've dabbled with through PICs but it's not too successful because of the limitation of the sampling and plotting rates. A real dual-trace scope is far more suited to the task it would seem. Having said that, my Virtual Scope of Jan/Feb '98, which was PC controlled (rather than PIC), had far better sampling rates, but was complex to build.

Best wishes for your own designs.

#### **TK3 AND PICtutor**

I have downloaded the PIC Toolkit TK3 software which appears to work as documented. However, when I try to run the PICtutor program from within TK3 it requests a PICtutor.exe. This file was not in the ftp folder, is it available on the site?

#### Eb Sleep, via email

You have misunderstood, Eb. The TK3 facility for accessing PICtutor is for those who already possess this commercial product. PICtutor can be bought from us on CD-ROM, see the advert in this issue.

#### TK3 AND XP

There seem to be loads of posts on the EPE Chat Zone over TK3 and Windows XP with suggestions ranging from AllowIO to others more ludicrous.

If (in Win XP) you create a shortcut to an EXE on your desktop, then right click and select properties, then select compatibility. You can then tell it to run in Windows 95 mode. This bypasses all IO problems et cetera, my TK3 prog running fine under Win XP once this has been configured.

#### Mark Jones via email

Following Mark's email, we had discussions with him, and we now have his more detailed article on XP/W2K and TK3 in the pipeline for publication. It's useful, watch out for it! Thank vou Mark

# Constructional Project

# Part 2 – Preamplifiers, Tone Controls and Filters

# RAYMOND HAIGH

A selection of "pic-n-mix" low-cost audio circuits – from preamplifier to speaker!

LTHOUGH the power amplifiers described last month have a respectable amount of gain, some signals may be too weak to produce an adequate loudspeaker output without additional amplification. They can also be further weakened by an excessive mismatch between signal source and amplifier. Tone controls are usually required when music is being reproduced, and restricting the bandwidth will clarify speech signals, especially under noisy conditions.

These three issues: preamplification, impedance matching and tailoring the frequency response, are covered in this article.

### TRANSISTOR AMPLIFIERS Impedances

The impedances presented by the input and output ports of transistor amplifier stages are extremely variable. Load and bias resistors exert a major influence, as do the gain of the transistor and its emitter current. Negative feedback can either raise or lower impedance and, to further confuse the issue, the load connected across one port influences the impedance presented by the other.

The impedance figures quoted are, therefore, intended as no more than *a guide* when selecting the best circuit for a particular application.

#### Biasing

Transistor amplifier stages are usually biased so that the output (collector or emitter; drain or source) rests at half the supply voltage under no-signal conditions. This enables the stage to deliver the greatest possible signal swing; i.e. the highest output, before the onset of clipping.

Transistor gain ( $h_{fc}$ ), and supply voltage, affect the biasing. However, over a wide range of  $h_{fc}$  values (at least 200 to 600), and supply voltages from +9V to +12V, the circuits described here will deliver a low distortion output that is more than

sufficient to fully drive the power amplifiers described last month.

Experimenters who require the stages to have the highest possible signal-handling capability for a given supply voltage may have to adjust the bias resistors. Guidance on this is given later.

#### Cascading

The various preamplifiers, tone controls and filters can be combined to suit individual requirements. Blocking capacitors have been provided at the inputs and outputs, and the units can be used safely with any equipment.

Cascading makes one of these capacitors redundant. Similarly, when they are connected to the power amp described last month, the output blocking capacitor can be omitted (C1 on the power amplifier p.c.b. duplicates this component).

### Decoupling

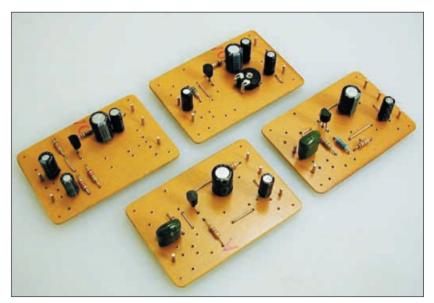
All of the preamplifier circuits are decoupled from the power supply by a resistor and capacitor. Failure to include these components will almost certainly result in motor boating (low frequency instability).

The main cause of this instability is the wide swing in power amplifier current drain: even with small units this can range from 10mA to 150mA. These signal-induced current swings cause variations in the voltage of dry batteries or badly regulated mains power supplies. When high gain preamplifiers share the same supply rail, the resulting feedback causes low-frequency oscillation.

If problems are encountered, increase the value of the decoupling resistor, or capacitor, or both, by a factor of ten. A capacitor of  $2000\mu$ F or more, connected across a dry battery power supply, will also help to eliminate instability at high volume levels.

#### R.F. Interference

The single transistor preamplifiers described here have an extended high



Four single-transistor preamplifiers (left-to-right). ● Low Impedance ● Medium Impedance ● High Impedance ● F.E.T. High Impedance.

frequency response, and problems with r.f. interference may be encountered. Connecting a low value ceramic capacitor between the input (emitter or base) and the OV rail will cure the problem, and the accompanying printed circuit board (p.c.b.) makes provision for this.

### SINGLE TRANSISTOR CIRCUITS

In many cases, all that is required is the additional gain and/or impedance matching afforded by a single transistor stage. Four circuits will now be considered.

### Low Input Impedance Preamplifier

It is convenient, with simple intercom units, to make the speaker double up as a microphone. Voice coil impedance and output are very low: a few ohms and less than 1mV at a close speaking distance. Transformers are often used to increase the impedance and voltage of this signal source, but a transistor can be made to do the job just as well.

The "grounded base" stage illustrated in Fig.1 has an input impedance of around 50 ohms, an output impedance roughly equal to the collector load resistance (R2) of 10 kilohms, and a voltage gain of around 100. Although more commonly encountered at the front-end of a radio receiver, this configuration is suitable for matching low source impedances to the power amplifier and, at the same time, providing a useful amount of voltage gain.

In the circuit diagram for the Low Input Impedance Preamplifier shown in Fig.1, C1 is a d.c. blocking capacitor, R1 and R2 are the input and output load resistors, and resistors R3 and R4 bias the transistor. The base (b) is grounded at audio frequencies by capacitor C3.

Supply line decoupling is effected by C4 and R5, and C2 is the output coupling and d.c. blocking capacitor.

# CIRCUIT BOARD

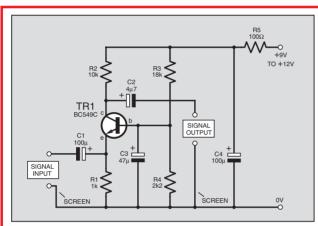
The printed circuit board component layout, wiring details and full-size copper foil master pattern are shown in Fig.2. This board is available from the *EPE PCB* Service, code 349 (Single Trans.).

Before commencing assembly, check the component, construction and interconnection notes at the end of the article.

### VARIATIONS

Readers wishing to operate the stage from lower supply voltages should check the voltage on the collector (c) of transistor TR1 under no-signal conditions. If it is much more than half the supply voltage, reduce the value of resistor R3 to increase the bias current. With 3V on the supply rail, R3 will need reducing to around 6·8 kilohms and, with a 6V supply, its value will be in the region of 12k.

Because of its very low input impedance, the circuit of Fig.1 is not prone to capacitative hum pick up, and the input lead can be



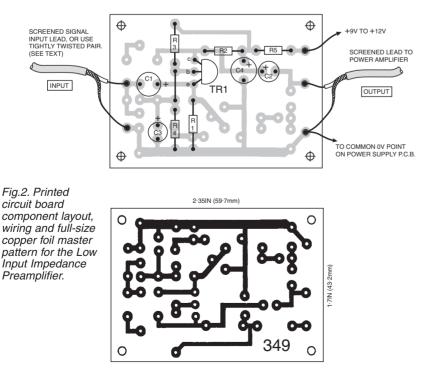
VOLTAGE GAIN 100 OVER AN  $\rm h_{fe}$  SPREAD OF 110 TO 600. CURRENT DRAIN AT 9V SUPPLY 0.75mA.

Fig.1. Circuit diagram for the single-transistor Low Input Impedance Preamplifier.

LOW INPUT IMPEDANGE PREAMPLIFIER

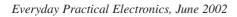


Low Input Impedance Preamplifier components mounted on the "single" p.c.b.



# **COMPONENTS**

LOW INPUT IMPEDANCE				
Resistors R1 R2	1k 10k	See Shop		
R3 R4	18k (see text) 2k2	TALK		
R5 All 0·25W 5%	$100\Omega$ carbon film	page		
Capacitors C1, C4	100µ radia (2 off)	al elect. 25V		
C2 C3	$4\mu7$ radial $47\mu$ radial	elect. 25V elect. 25V		
Semicondu TR1	BC549C n	<i>ppn</i> transistor ar – see text)		
the EPE PCB Trans); audic strand connec	cuit board Service, co screened cting wire; in	available from de 349 (Single cable; multi- put and output ; solder pins;		
Approx. Cos Guidance O		£7		



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tightly twisted flex rather than screened cable. If r.f. interference problems are encountered, connect a 100nF capacitor between the emitter (e) of TR1 and the 0V rail: provision is made for this on the p.c.b.

Combining this low impedance circuit (Fig.1) with the LM386N-1 or the TBA820M power amplifiers (fully described in Part 1, last month) will produce a decent intercom unit, but more amplification is needed for surveillance purposes. Cascading the grounded base stage with the medium impedance preamplifier described next (Fig.3) is one possible answer.

### Medium Input Impedance Preamplifier

The input impedance of the single transistor, common emitter preamplifier illustrated in Fig.3 is approximately 1500 ohms (1.5k), and the output impedance roughly equal to the value of the load resistor, R2; i.e. 4700 ohms (4.7k).

Base bias resistor R1 is connected to transistor TR1 collector (c) rather than the supply rail. The resulting d.c. negative feedback makes the biasing more immune to transistor gain spreads and variations in supply voltage.

Preset potentiometer VR1 acts as the emitter bias resistor. Connecting capacitor C2 to the slider (moving contact) enables part of it to be left un-bypassed. This introduces varying levels of negative feedback and, with the specified transistor, the gain of the stage can be set between 10 and 160 times to suit different applications.

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SCREENED SIGNAL

INPUT

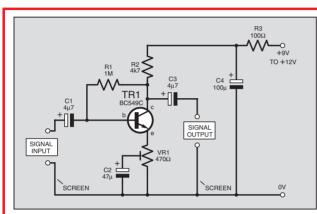
Comment has already been made about supply rail decouplers, R3 and C4, and blocking capacitors, C1 and C3.

### CIRCUIT BOARD

The printed circuit board component layout, wiring details and full-size copper foil master pattern are shown in Fig.4. This board is available from the EPE PCB Service, code 349 (Single Trans.).

Before undertaking assembly work, see the component, construction and interconnection details at the end of the article.

Provision is made for connecting an r.f. bypass capacitor across the input. A 1nF or 10nF ceramic component should be adequate if problems arise.



VOLTAGE GAIN WITH VR1 SLIDER AT 0V RAIL, 8 TO 10 OVER AN hfe SPREAD OF 110 TO 600 VOLTAGE GAIN WITH SLIDER AT TR1 EMITTER, 80 TO 600 OVER AN h SPREAD OF 110 TO 600 CURRENT DRAIN AT 9V SUPPLY: 1.25mA.

Fig.3. Circuit diagram for the Medium Input Impedance Preamplifier.

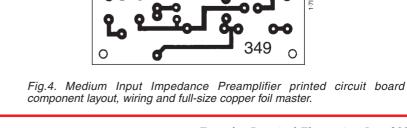
MEDIUM INPUT IMPEDANCE PREAMPLIER



Medium Input Impedance preamplifier components mounted on the "single" p.c.b.

# COMPONENTS

MEDIUM INPUT IMPEDANCE				
Resistors R1 R2 R3 All 0·25W 5%	1M 4k7 100Ω carbon film			
Potentiome VR1		page osed carbon		
Capacitors C1, C3 C2 C4	4μ7 radial (2 off) 47μ radial 100μ radial			
Semicondu TR1	BC549C n	<i>on</i> transistor ar – see text)		
<b>Miscellaneous</b> Printed circuit board available from the <i>EPE PCB Service</i> , code 349 (Single Trans); audio screened cable; multi- strand connecting wire; input and output sockets, type and size to choice; solder pins; solder etc.				
Approx. Cos Guidance O		£7		



TR

2-35IN (59-7mm)

 $\oplus$ 

 $\oplus$ 

9V TO +12V

SCREENED LEAD TO POWER AMPLIFIER

OUTPUT

. TO COMMON 0V POINT ON POWER SUPPLY P.C.B.

0

### High Input Impedance Preamplifier

Crystal microphones and ceramic gramophone pick-ups (there are still a few in use) require an amplifier with a high input impedance, and a stage of this kind is useful when the damping on a signal source has to be kept low.

Configuring a bipolar transistor in the emitter-follower (common collector) mode results in a high input and low output impedance, and a typical High Input Impedance Preamplifier circuit diagram is shown in Fig.5. The input impedance is roughly equal to the gain of the transistor ( $h_{fe}$ ) multiplied by the value of the emitter load resistor R2.

This is, however, limited by the bias resistor R1, and the output load, which shunts the emitter resistor. Nevertheless, a high gain transistor will still produce an input impedance of about 100 kilohms.

Often the low output impedance is the sought after feature, either for matching purposes or for avoiding high-frequency losses and hum pick-up when long screened cables have to be used. Output impedance is directly related to the impedance presented by the signal source, and is usually in the region of 1000 ohms. The voltage gain of the circuit is a little less than unity.

### CIRCUIT BOARD

The printed circuit board component layout, wiring details and full-size copper foil master pattern for the High Input Impedance Preamplifier are shown in Fig.6. This board is the same one used for all the single transistor preamplifiers, and is available from the *EPE PCB Service*, code 349 (Single Trans.). See the component, construction and interconnection notes at the end of the article.

High input impedance makes the stage very vulnerable to hum pick up. Careful attention must, therefore, be paid to screening the input leads and, possibly, the entire unit.

### VARIATIONS

It is possible to obtain higher input impedances with a bipolar transistor by applying positive feedback from the emitter to the base bias network. This involves an extra pair of resistors and a capacitor, and an alternative solution, if very high input impedances are required, is to use a field effect transistor (f.e.t.); a device which tends to introduce less noise at audio frequencies.

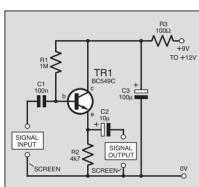
### USING A F.E.T.

A circuit diagram for a F.E.T. High Input Impedance Preamplifier is given in Fig.7. The gate resistor R1 is tapped down to the source resistors R2/R3 in order to improve biasing and, hence, signal handling. By this means the f.e.t. develops its gate bias across R2, and R3 drops an additional 3V or so to fix the voltage on the source at around half the supply voltage.

Connecting the gate resistor R1 in this way applies a proportion of the in-phase output signal to its lower end, and the resulting positive feedback, or "bootstrapping", increases its effective resistance, and the input impedance of the circuit, to around 6 megohms (6M).

Output impedance is independent of signal source impedance. It is governed by the transconductance (gain) of the device, and is usually of the order of 500 ohms.

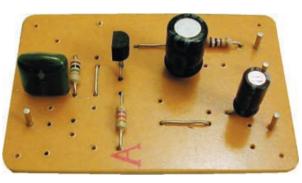
HIGH INPUT IMPEDANGE PREAMPLIFIER



VOLTAGE GAIN: UNITY CURRENT DRAIN AT 9V SUPPLY: 1.25mA.

Fig.5. High Input Impedance Preamplifier circuit diagram.

COMPONENTS	
HIGH I Resistors B1	NPUT IMPEDANCE See 1M QMAD
R2 R3 All 0·25W 5%	4k7 SUUP 100Ω TALK
Capacitors C1 C2 C3	100n polyester 10μ radial elect. 25V 100μ radial elect. 25V
Semicondu TR1	<b>ctors</b> BC549C <i>npn</i> transistor (or similar – see text)
<b>Miscellaneous</b> Printed circuit board available from the <i>EPE PCB Service</i> , code 349 (Single Trans); audio screened cable; multi- strand connecting wire; input and output sockets, type to choice; solder pins; solder etc.	
Approx. Cost Guidance Only	



High Input Impedance Preamplfier circuit board.

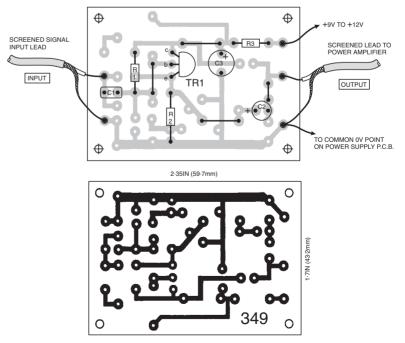


Fig.6. Printed circuit board component layout, wiring and full-size copper foil master for the High Input Impedance Preamplifier.

This is the circuit of choice when a high impedance source has to be connected to a long screened cable; e.g., a capacitor or crystal microphone. However, f.e.t. characteristics vary widely, and readers wishing to use the circuit of Fig.7 should be prepared to adjust the value of resistor R3, over the range of 1500 to 4700 ohms, especially when low supply voltages are used, in order to optimise signal handling capability.

#### CIRCUIT BOARD

Details of the printed circuit board component layout, wiring and copper foil master pattern are given in Fig.8. The board is the single transistor version and is available from the *EPE PCB Service*, code 349 (Single Trans).

Before assembly, check the component, construction and interconnection details at the end of the article.

#### LOW-NOISE PREAMPLIFIER

Amplifiers introduce unwanted noise and, as gain increases, more care has to be taken to prevent the noise becoming too intrusive. The noise generated by a bipolar transistor can be reduced by operating it at a low collector current, typically between  $10\mu A$  and  $50\mu A$ . This technique has been adopted for the first stage of the directlycoupled, two transistor, Low-Noise Preamplifier shown in Fig.9.

Overall gain is stabilised by negative feedback applied via preset VR2. With the value shown, gain is approximately 300. If a 47k potentiometer is used instead, gain will be reduced to around 150, and it can be taken down to 70 or so with a 22k component.

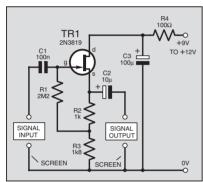
Rotating the slider (moving contact) of preset VR2 causes it to be progressively bypassed by capacitor C6, increasing the negative feedback, and reducing gain, at high frequencies. This feature is useful for reducing noise and for correcting the recording characteristic of long playing records. It is usual to incorporate more complicated *RC* networks in the VR2 position for the latter purpose but, unless the listener has a very refined ear, there will be little or no discernible difference.

Operating conditions are stabilised by d.c. negative feedback applied via resistor R5. This, together with the high value collector load, R3, fixes the collector current of transistor TR1 at around  $50\mu$ A with a 12V supply.

Input impedance is around 50k, but the optimum signal source resistance for lowest noise is between 5k and 10k. This has influenced the value of the input potentiometer, VR1.

The purpose of the remaining components will be evident from earlier circuit descriptions. However, because of the

F.E.T. HIGH INPUT IMPEDANGE PREAMPLIFIER



VOLTAGE GAIN: UNITY CURRENT DRAIN AT 9V SUPPLY: 1.75mA.

Fig.7. Alternative circuit diagram for a High Input Impedance Preamplifier using a field effect transistor (f.e.t.).

COMPONENTS					
Resistors R1 R2 R3 R4	UT IMPEDAI 2M2 1k 1k8 (see text) 100Ω % carbon film	See Shop Talk			
Capacitors C1 C2 C3	100n polye 10 $\mu$ radial	ester elect. 25V al elect. 25V			
Semicond TR1	2N3819 n-	-channel field ansistor (f.e.t.)			
<b>Miscellaneous</b> Printed circuit board available from the <i>EPE PCB Service</i> , code 349 (Single Trans); audio screened cable; multi- strand connecting wire; input and output sockets, type to choice; solder pins; solder etc.					
Approx. Co Guidance (		£7			



F.E.T. High Input Impedance Preamplifier p.c.b.

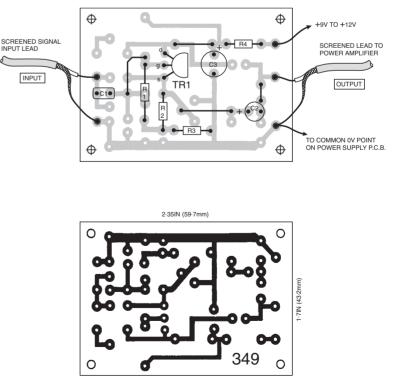


Fig.8. Printed circuit board component layout, wiring and full-size copper foil master for the F.E.T. High Input Impedance Preamplifier.

higher gain, the supply line decoupling capacitor C7 has been increased in value to ensure stability.

#### CIRCUIT BOARD

The printed circuit board component layout, wiring details and full-size copper foil master pattern for the Low-Noise Preamplifier are shown in Fig.10. This board is available from the *EPE PCB* Service, code 350 (Dual Trans.).

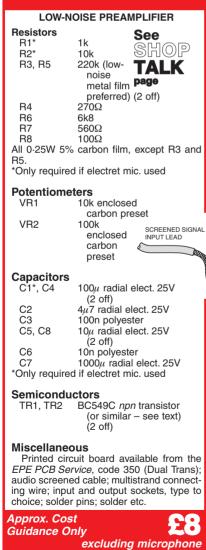
See the general construction, component and interconnection guide-lines on the last page.

#### VARIATIONS

Some readers may wish to use this circuit with electret microphones which contain an internal line-powered f.e.t. amplifier. The load for this remote device is provided by resistor R1, and the supply voltage is reduced to around 4-5V, which is optimum for most microphones of this kind, by resistor R2. Decoupling is by means of capacitor C1.

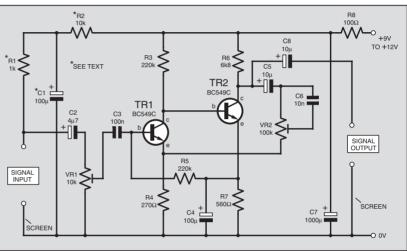
These components (R1, R2 and C2) should only be fitted if an electret microphone is used, as the circuit maintains a

## **COMPONENTS**



LOW-NOISE PREAMPLIFIER

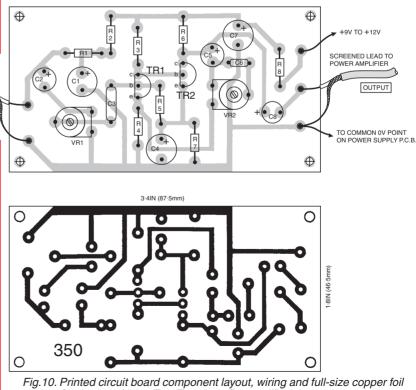
Completed p.c.b. for the Low-Noise Preamplifier.



VOLTAGE GAIN 300 OVER ht SPREAD OF 450 TO 600.

CURRENT DRAIN AT 9V SUPPLY: 1mA.

Fig.9. Circuit diagram for the Low-Noise Preamplifier. Components marked with an asterisk are only needed if an electret microphone is used. Increase the value of R2 to 18k with 12V supplies.



master for the Low-Noise Two-Transistor Preamplifier.

#### d.c. voltage on the input which could disturb the action of some signal sources.

This circuit, and variations of it, form the basis of the front-ends of most high quality preamplifiers. With the component values shown, 3.3mV r.m.s. input will produce a 1V output before the onset of clipping.

The noise introduced by the amplifier is about the same, or a little less, than that generated by the single transistor amplifier set for a gain of 150. The noise level could be further reduced by using low-noise, metal film resistors for R3 and R5.

#### FREQUENCY RESPONSE

Although inductors are sometimes used for "tailoring" the frequency response, the key components in networks which modify audio frequency response are normally capacitors.

The resistance presented by a capacitor to the flow of alternating current (a.c.) decreases as frequency rises. This frequency dependant resistance is known as *reactance*.

Capacitors combined with resistors form frequency dependant potential dividers which can be used to tailor the response.

These *RC* networks can, of course, only attenuate signals. So called "bass boost" is obtained by reducing the response of the system to the higher audio frequencies.

Table 1 lists the reactances of a range of standard capacitor values, at spot frequencies, across the audio spectrum. Referring to it, an  $0.1\mu$ F (100nF) capacitor presents a resistance of 5300 ohms at a frequency of 300Hz. This rises to 32000 ohms at 50Hz and falls to 320 ohms at 5kHz.

Fitting a blocking capacitor of this value to an amplifier with an input impedance of 5000 ohms will result in signal levels at 300Hz being halved. (Capacitor and input impedance act as a potential divider). This attenuation will increase as the frequency is lowered, and reduce as frequency is raised, at a rate of 6dB per octave.

Fitting low value d.c. blocking capacitors to one or more stages will, therefore, roll-off the low frequency response. Capacitors connected from signal lines to ground; e.g. across the tracks of volume controls, will progressively attenuate high frequencies. Although simple, these measures can make a significant improvement in clarity and signal-to-noise ratio.

Refer to Table 1 when selecting a capacitor to give the desired roll-off with a particular input impedance, then refine its value by trial and error.

#### FEEDBACK NETWORKS

Capacitors are used to make gain-reducing negative feedback networks frequency dependant; for example, capacitor C6 in the two-transistor Low-Noise Preamplifier shown in Fig.9.

Reducing the emitter bypass capacitor C2, in the single transistor preamplifier shown in Fig.3, to  $4.7\mu$ F, will progressively increase feedback, and reduce gain, as frequency lowers. This is another simple, but effective, way of securing low frequency roll-off.

#### TONE CONTROLS

Some means of continuously varying the frequency response is desirable when music is being reproduced, and a suitable Tone Control circuit diagram is given in

Table 1: Reactance, in Ohms, of standard value capacitors at stated audio frequencies

at stated addio frequencies													
Cap. μF	50 Hz	100 Hz	200 Hz	300 Hz	400 Hz	500 Hz	1 kHZ	2 kHz	3 kHz	4 kHz	5 kHz	10 kHz	20 kHz
1000	3Ω2	1Ω6	0Ω8	0Ω5	_	_	_	_	_	_	_	_	_
470	6Ω8	3Ω4	1Ω7	1Ω1	_	_	_	_	_	_	_	_	_
100	32	16	8	5	4	3 <u>Ω</u> 2	1Ω6	_	_	_	_	_	_
47	68	34	17	11	$8\Omega 5$	$6\Omega 8$	3 <u>Ω</u> 4	1Ω7	1Ω1	_	_	_	_
10	320	160	80	53	40	32	16	8	$5\Omega 3$	4	$3\Omega^2$	$1\Omega 6$	_
4.7	680	340	170	110	85	68	34	17	11	$8\Omega 5$	$6\Omega 8$	3Ω4	1Ω7
1	3k2	1k6	800	530	400	320	160	80	53	40	32	16	8
0.47	6k8	3k4	1k7	1k1	850	680	340	170	110	85	68	34	17
0.1	32k	16k	8k	5k3	4k	3k2	1k6	800	530	400	320	160	80
0.047	68k	34k	17k	11k	8k5	6k8	3k4	1k7	1k1	850	680	340	170
0.01	320k	160k	80k	53k	40k	32k	16k	8k	5k3	4k	3k2	1k6	800
0.0047	680k	340k	170k	110k	85k	68k	34k	17k	11k	8k5	6k8	3k4	1k7

Reactance values rounded off

Fig.11. This is the medium impedance transistor preamplifier illustrated in Fig.3 with negative feedback applied, via a frequency dependant network, from transistor TR1 collector to base. First published by P J Baxandall in 1952, the circuit has since been used, with minor variations, in most high quality preamplifiers.

Potentiometers VR1 (Bass), and VR2 (Treble), control the impact of capacitors C1, C2 and C3 on the feedback network. Resistors R2 and R3 minimise interaction between the controls, and the circuit affords 15dB of "boost" or cut at 100Hz and 10kHz.

#### CIRCUIT BOARD

The printed circuit board component layout, wiring details and full-size copper foil master pattern are shown in Fig.12. This board is available from the *EPE PCB Service*, code 351 (Tone).

Before undertaking any assembly work, see the general component, construction and interconnection notes at the end of the article.

#### IN-CIRCUIT

When circuits are cascaded, the Tone Control unit should always be the last in the chain; i.e. the one connected to the power amplifier. Most high quality preamplifiers consist of the two transistor circuit illustrated in Fig.9 followed by this Tone Control circuit.

#### BANDPASS FILTERS

Reducing bandwidth to around 300Hz to 3kHz greatly improves the clarity of speech signals, and the practice is adopted by tele-

phone companies around the world. Limiting the frequency response in this way significantly improves signal-to-noise the ratio. This is particularly desirable with sensitive radio equipment and surveillance systems, where the high level of amplification needed for the weakest signals brings with it a good deal of background and equipment generated noise.

For best results, roll-off beyond the pass band should be fairly steep: the 6dB per octave afforded by a single *RC* combination is not sufficient. The Bandpass Filter circuit diagram shown in Fig.13 cascades three high-pass (low frequency cut) sections between transistors TR1 and TR2, and three low-pass (high frequency cut) sections between TR2 and TR3. By this means, a roll-off of 18dB per octave is achieved above and below the desired frequency range.

Filter networks of this kind need to be fed from a comparatively low impedance, and feed into a high impedance. The emitter follower stages, TR2 and TR3, are thus eminently suitable, and amplifiers of this kind have already been discussed. The input stage, transistor TR1, overcomes signal losses, or, with the slider of VR1 at TR1 emitter (e), ensures an overall circuit gain of around 25.

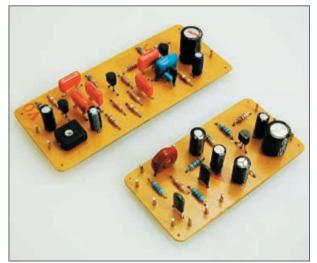
Emitter to base feedback around TR2 and TR3, via the *RC* networks, improves the action of the filters. Component values have been selected to start the roll-off just within the pass band, and the response falls steeply below 300Hz and above 3kHz.

Two capacitors have to be combined to produce a difficult-to-obtain value. To avoid confusion they are shown separately on the circuit diagram as C8 and C9.

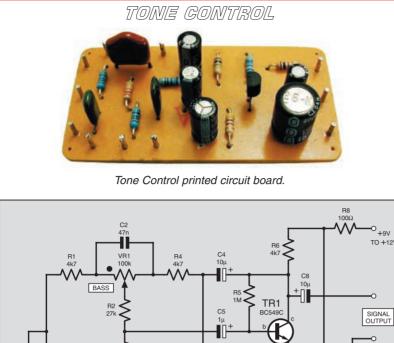
#### CIRCUIT BOARD

Details of the printed circuit board component layout, wiring and copper foil master are given in Fig.14. The Bandpass Filter board is also available from the *EPE PCB Service*, code 352 (Filter).

See component, construction and interconnection notes before commencing building.



Bandpass Filter (top) and Tone Control p.c.b.s.



TO +12V R? C7 Y 100 C1 C3 TREBLE SIGNAL INPUT 2n2 C6 R7 -11 5 SCREEN 47 SCREEN 100 • 0V

● = BOOST END OF POTENTIOMETERS (VR1, VR2) MOVING CONTACT (SLIDER). VOLTAGE GAIN UNITY WHEN VR1 AND VR2 SET AT MID TRAVEL. BOOST AND CUT ±15dB AT 100Hz AND 10kHz. CURRENT DRAIN AT 9V SUPPLY: 1.25mA.

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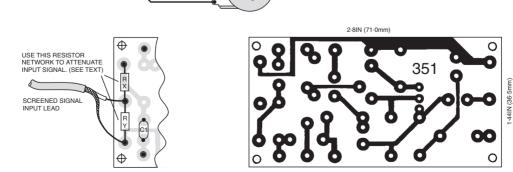
SCREENED SIGNAL

ANCHOR PIN FOR INPUT POTENTIAL DIVIDER RESISTORS

Fig.11. Circuit diagram for the Tone Control (bass, treble boost and cut).

## **COMPONENTS**

	ONE CONTROL				
Resistors R1, R3, R4, R6 R2 R5 R7 R8 All 0.25W 5%	See           4k7 (4 off)           27k           1M           470Ω           100Ω           6 carbon film				
Potentiome VR1, VR2					
Capacitors C1, C3 C2 C4, C8 C5 C6 C7	2n2 polyester (2 off) 47n polyester 10 $\mu$ radial elect. 25V (2 off) 1 $\mu$ radial elect. 25V 47 $\mu$ radial elect. 25V 100 $\mu$ radial elect. 25V				
Semicondu TR1	Ictors BC549C npn transistor (or similar – see text)				
<b>Miscellaneous</b> Printed circuit board available from the <i>EPE PCB Service</i> , code 351 (Tone); metal case (optional), size and type to choice – see text; audio screened cable; multistrand connecting wire; input and output sockets, type to choice; solder pins; solder etc.					
	st CO				
pins; solder e	st £9				
pins; solder e Approx. Cos Guidance O +9V TO +12V screened lead TO	st £9				
pins; solder e Approx. Cos Guidance O +9V TO +12V screened lead TO	excluding case				
+9V TO +12V SCREENED LEAD TO POWER AMPLIFIER TO COMMON 0V POI	excluding case				



BASS

TR1

TREBLE

R 7

R8

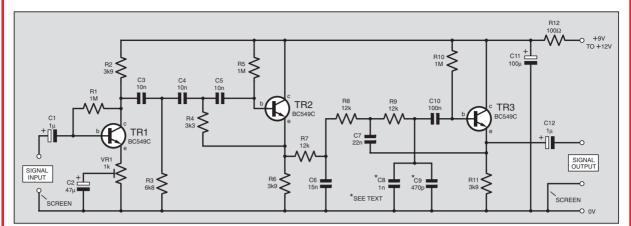
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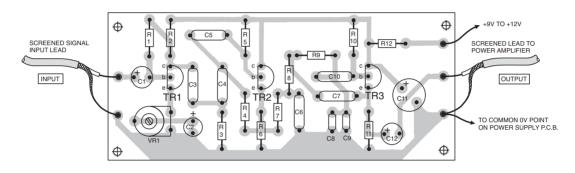
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Fig.12. Tone Control printed circuit board component layout, interwiring and full-size copper foil master. The tape and CD player signal input attenuation resistors (see text) are shown in the inset diagram (left).

SPEECH FREQUENCIES (300Hz TO 3kHz) BANDPASS FILTER



VOLTAGE GAIN WITH PASSBAND, UNITY WITH VR1 SLIDER AT 0V RAIL; 25 WITH SLIDER AT TR1 EMITTER END. ROLL-OFF 18dB PER OCTAVE BELOW 30Hz AND ABOVE 3kHz. *CURRENT DRAIN AT 9V SUPPLY: 4mA. Fig. 13. Circuit diagram for the Bandpass Filter for speech frequencies (300Hz - 3kHz).* 



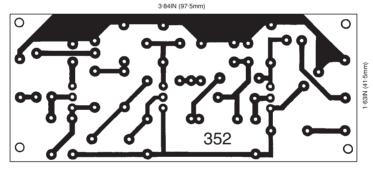


Fig. 14. Printed circuit board component layout, wiring and fullsize copper foil master for the Bandpass Filter.

## **COMPONENTS**

#### BANDPASS FILTER

#### Resistors

 R1, R5, R10 1M (3 off)

 R2, R6, R11 3k9 (3 off)

 R3
 6k8

 R4
 3k3

 R7 to R9
 12k 1% metal film (3 off)

 R12
 100Ω

 All 0.25W 5% carbon film, except R7 to R9

#### Potentiometers

VR1	1k carbon preset
Consoitoro	

#### Capacitors

$1\mu$ radial elect. 25V (2 off)
47µ radial elect. 25V
10n polyester (5% or
better) (3 off)

Bandpass Filter printed circuit board.

C6	15n polyester
C7	22n polyester
C8*	1n polyester
C9*	470p ceramic
C10	100n polyester
C11	100µ radial elect. 25V
*Combined	(parallel) to give 1n5

#### Semiconductors

TR1 to TR3 BC549C npn transistor (or similar – see text) (3 off)

#### Miscellaneous

Printed circuit board available from the EPE PCB Service, code 352 (Filter); audio screened cable; multistrand connecting wire; input and output sockets, type to choice; solder pins; solder etc.

See

page

SHOP

TALK

E



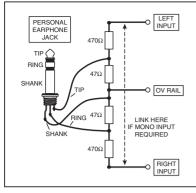


Fig.15. Method of connecting a "Walkman" tape or CD player.

#### SUMMARY

Operational amplifiers (op.amps) are more commonly used in filters of this kind but, when the need is simply for a unity gain buffer with a high input and low output impedance, the ubiquitous bipolar transistor can be made to serve our purpose just as well.

#### SIGNAL SOURCES

Radio Receivers

The output from the detector or f.m. discriminator in a superhet radio receiver should fully load the power amplifiers described last month. After the usual filtering, the signal can be fed directly to the power amplifier, or via the Tone Control unit shown in Fig.11 and Fig.12.

#### **Microphones**

The single transistor preamplifiers shown in Fig.1 to Fig.8 will provide appropriate matching and sufficient gain for dynamic (moving coil), electret and crystal microphones when they are used for intercom purposes. (A circuit for line-powering electret microphones can be taken from Fig.9). The common emitter circuit given in Fig.3 should be used with moving coil units as these present an impedance of around 600 ohms.

When electret or dynamic microphones are deployed for surveillance or "sound capturing" purposes, the two transistor circuit of Fig.9 will ensure a good degree of sensitivity. Electret microphones have an extended low frequency response. If this proves troublesome, reduce the value of the d.c. blocking capacitor C2. Try 47nF (0.047 $\mu$ F) as a starting point.

#### Gramophone Pick-ups

The low output of moving-coil pick-ups necessitates the use of the two transistor preamplifier detailed in Fig.9. Omit preset VR1 and feed the signal to the base of transistor TR1 via capacitor C3. Low output ceramic pick-ups should be connected via a 1M (megohm) or 2M2 series resistor to preserve low frequency response.

The F.E.T. Preamplifier circuit illustrated in Fig.7 is more suitable for high output ceramic and crystal pick-ups.

#### Personal Tape and CD Players

An arrangement for extracting the signal from personal cassette players and headphone radios is given in Fig.15. The 47 ohm resistors substitute for the 32 ohm earpieces, and the 470 ohm resistors attenuate the signal. Preamplification is not required, but readers may wish to use the Tone Control unit to process the signal. Provision is accordingly made, on the Tone Control p.c.b. illustrated in Fig.12, for a signal attenuating network; resistors Rx and Ry.

#### STEREO

The chosen system must, of course, be

duplicated if stereo operation is required. Tone and Volume controls are usually ganged, and an additional potentiometer is provided to balance the gain of the two channels.

With the simple circuit arrangement shown in Fig.16, the Balance potentiometer is connected across the ganged Volume controls at the inputs to the two power amplifiers (VR1 on the power amplifier circuit diagrams).

#### **COMPONENTS**

All of the components, for this part of the series, are readily available from a variety of sources. Transistor types are not critical and almost any small-signal *npn* device will function in the circuits.

A low-noise, high gain transistor will, however, ensure the best performance, and the base connections for some alternative types are given in Fig.17. With European transistors, the suffix "C" indicates the highest gain grouping.

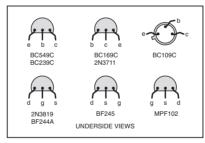


Fig.17. Base connections for suitable transistors and f.e.t.s.

If possible, use transistors with an  $h_{fe}$  of at least 450 for the input stage of the Low-Noise Preamplifier and for the various emitter follower stages (where high input impedance depends on the use of a high gain device).

#### CONSTRUCTION

All the preamplifiers covered in this part are assembled on printed circuit boards and construction is reasonably straightforward. Solder pins, inserted at the lead-out points, will simplify any offboard wiring. Remember to earth the metal bodies of rotary potentiometers and to use screened audio (mic.) cable for the leads to tone and volume controls to minimise hum pick-up.

The single transistor preamplifiers all use the same p.c.b. and wire links are required. If units are cascaded, and coupling capacitors deleted, remember to install wire links to maintain the signal path.

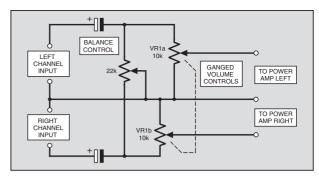


Fig.16. Circuit arrangement for a stereo Balance control.

It may help to start construction by first placing and soldering in position the various wire links on the chosen preamplifier p.c.b. This should be followed by the leadoff solder pins, and then the smallest components (resistors) working up to the largest, electrolytic capacitors and presets. Finally, the lead-off wires (including the screened cables) should be attached to the p.c.b.

On completion, check the orientation of electrolytic capacitors and transistors, and examine the board for poor connections and bridged tracks, before connecting the power supply. The approximate current drains are included with the circuit diagrams.

#### INTERCONNECTIONS

Overall voltage gain can be in excess of 2000, and care must be taken to avoid hum pick-up and instability.

Hum pick-up is of two kinds, capacitative and inductive. High impedance circuits are prone to the former, and low impedance to the latter. Housing the pre- and power amplifiers in a metal case will do much to minimise these problems.

If hum increases when a finger is brought near to the preamplifier, the pickup is capacitative. It can usually be cured by providing an earthed metal screen around the input wiring or even the entire preamplifier board.

All mains and a.c. power leads within the metal case of the unit must be tightly twisted to minimise external fields, and the mains transformer should be sited at least 150mm (6in) from the input circuitry. Tightly twist power amplifier output leads, and keep them as far away as possible from preamplifier inputs. Keep all leads as short as possible.

Run a separate negative power supply connection from *each* of the p.c.b.s to a common 0V point on the power supply board, or to the negative battery terminal. *Do not* connect one circuit board via another to supply negative, or rely upon screened cable braiding or a metal case to provide this connection. Make only one connection to any metal case, close to the negative terminal on the power supply p.c.b.

If all of the above measures have been adopted and hum problems still persist, try disconnecting, one by one, the screens of the audio cables, *at one end only*. Reorientating the mains transformer can also effect a cure.

Next Month: Mains power supplies, loudspeakers and signal filtering will be discussed.

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M06/02

#### )P with David Barrington

#### **Biopic Heartbeat Monitor**



Biopic Heartbeat Monitor The 740L6000 logic-to-logic opto-isolator used in the *Biopic Heartbeat Monitor* came from **RS** and can be ordered from any bona-fide stockists, including some of our advertisers, code 650-829. You can order direct (credit card only) from RS on 01536 444079 or on the web at *rswww.com*. A post and handling charge will be made. The original prototype used a TLP215 opto-isolator, which has now become obsolete, but if you already have this device you can use it in this circuit.

For those readers unable to program their own PICs, a ready-programmed PIC16F876-4P microcontroller can be purchased from Magenta Electronics PIC 16F876-4P microcontroller can be purchased from Magenta Electrolics ( $\Im$  01283 565435 or www.magenta2000.cc.uk) for the inclusive price of £10 each (overseas add £1 p&p). They are also able to supply a suitable 2 line x 16 characters per line alphanumeric display module and a set of four TENS electrodes at a very reasonable price. The T.E.N.S. Replacement Electrode Pads used in the author's model were purchased from Boots together with a concerted load production.

separate lead pack. The software is available on a 3-5in. PC-compatible disk (*EPE* Disk 5) from the *EPE* Editorial Office for the sum of  $\mathfrak{L}3$  each (UK), to cover admin costs (for overseas charges see page 461). It is also available *Free* from the *EPE* we

site: **ftp:**//**ttp.epenag.wimborne.co.uk**/pub/PICS/Biopic. The printed circuit board is available from the *EPE PCB Service*, code 355 (see page 461). Finally, you *MUST* take heed of the safety notes under the cross-head "Opto-Isolation".

#### World Lamp

Most parts needed to construct the *World Lamp* project should, it is claimed, be very easy to acquire as they are standard "off-the-shelf" stock items. Also, most of our components advertisers should be able to suggest suitable substitutes, including rough equivalents to those specified in the circuit.

The *n*-channel power MOSFET type HUF75329P3 appears to be only list-ed by **RS** (**3** 01536 444079 or *rswww.com* – see earlier notes above regard-ing ordering), code 294-9648. Suggested alternatives include: IRFZ44N; IRFU3303; BUZ22 (likely to run hot). The Motorola version (MC14093BCP – RS code 640-765) of the quad Schmitt trigger NAND gate is recommended, but any 4093 device should work here. but any 4093 device should work here.

If you do not want to use a finned heatsink for the MOSFETs TR1 and TR2 then any 0.5mm or greater thickness piece of metal may be cut and drilled. Take great care to ensure that the two heatsinks are never allowed to touch each other.

Note that capacitor C8 **MUST** be rated at a minimum of 600V a.c. This will probably mean that you have to select a suppression type (Class X2) rated at 275V a.c. (See also this month's *Circuit Surgery* page 439.) The VA rating of the mains transformer should be at least double the fluorescent lamp wattage to prevent overheating



The printed circuit board is available from the EPE PCB Service, code 340 (see page 461).

#### **Frequency Standard Generator**

All the semiconductor devices called up for the Receiver and Digital circuits that drive the *Frequency Standard Generator* project are standard devices and should be stocked by most of our components advertisers, such as ESR, Bardwell, Bowood, Cricklewood and Sherwood Electronics.

The ferrite rod for the homemade "aerial" coil used in the Receiver came from Rapid Electronics (2) 01206 751166 or www.rapidelectronics.co.uk), code 88-3098. The mains transformer was purchased from Maplin (20870 264 6000 or www.maplin.co.uk), code YN17T. Almost any small 250mA output mains transformer with 15V-0V-15V, centre tapped secondaries should be suitable for this circuit

The low-current 10mH choke used in the model is an RS device and was ordered from **RS** (credit card only) on **01536 444079** or **rswww.com**), code 228-343. A p&p charge will be incurred.

The printed circuit boards are available from the EPE PCB Service, codes 354 (Rec.) and 355 (Digital), see page 461.

#### Simple Audio Circuits - 2

We no not expect any buying problems when shopping for parts for this month's instalment of the *Simple Audio Circuits*. Transistor types are not critical and almost any small-signal *npn* will func-

tion in the various circuits. However, a low-noise high gain transistor will ensure the best performance and some alternative types to those shown in the circuits would be: BC109C; BC239C; BC169C and 2N3711. For f.et.s you could use: BF244A; BF245 and MPF102. With European transistors the suffix

C indicates the highest gain grouping. All the printed circuit boards for this month's modules are available from the EPE PCB Service, see page 461. The single-transistor Low, Med, High Impedance and F.E.T. Preamplifiers are covered by p.c.b. code 349.

#### Teach-In 2002 – Lab 8

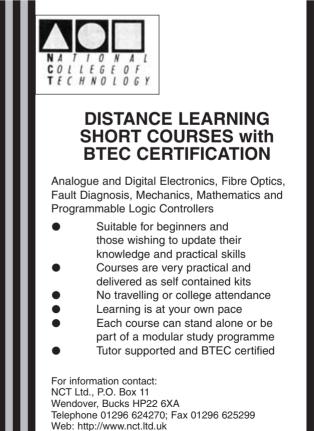
Just some of the semiconductor and sensor devices could give sourcing problems in this month's *Teach-In 2002 Lab Works*. The Intersil ICL8038CC waveform generator i.c. was purchased from **RS** (201536 444079 or *rswww.com*), order code 305-844. You could also use the Maxim MAX8038.

The same above company also supplied the Linear Technology LTC1062CN8 5th order switched capacitor low-pass filter chip, code 633-880. The QPE1113 matched pair of infra-red emitter and phototransistor detec-tor came from Farnell (🕾 0113 263 6311 or www.farnell.com), code 280-367. The emitter is the opaque yellow device.

#### PLEASE TAKE NOTE

May '02

Washing Ready Indicator It has been found that the ICL7642 quad op.amp is now discontinued. It is suggested that the LMC6044IN is a pin-for-pin alternative. It is listed by Farnell (code 954-767) and RS (code 853-472. See above for ordering details



## **Constructional Project**

# FREQUENCY Standard Generator



## ANDY FLIND

A high-precision selectable 1Hz to 100kHz frequency source derived from BBC Radio Four's transmission signal.

URING the design stage of the *Synchronous Clock Driver*, featured in *EPE* Sept '01, doubts arose as to the accuracy of the frequency meter being used to check and adjust the output frequency of the project. Since this instrument was the author's primary means of measuring frequency, the difficulty arose as to how it could itself be checked and, if necessary, adjusted.

#### TRANSMISSION FREQUENCIES

One of the broadcast radio carrier signals appeared to be the best way of obtaining a suitable reference. Most British readers will know of the time signal transmitted at 60kHz from Rugby, but this isn't really suitable for frequency testing since it is pulsed on and off by the data signals it carries.

Another source which seemed better suited for the purpose was the 198kHz "longwave" carrier for *Radio 4*. Originally this was intended for use as a national frequency standard and its accuracy is still maintained to an incredible level, having a Rubidium frequency source as its reference with constant monitoring by the National Physical Laboratory.

In fact, the accuracy is claimed to be one part in  $10^{11}$ , which translates to about a third of a millisecond per year of error. This should be more than adequate as a standard for most home workshops!

#### HOME SERVICE DESIGNING

Various circuits are available for receiving and using this signal "off air", so one was soon obtained through the good offices of one of our better radio magazines and hastily constructed. In fact, the "error" of the author's meter turned out to be of insignificant proportions, but by then the "design bug" had bitten.

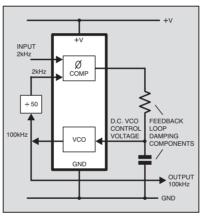


Fig.1. Phase-locked loop principle.

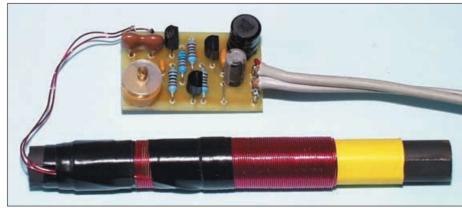
Could a better version be built offering more useful output frequencies such as 100kHz and the decades beneath it, 10kHz, 1kHz and so forth? Such a source would be very useful for calibrating all sorts of equipment, including oscilloscopes and frequency meters, and perhaps also in the testing and adjustment of clocks. Electronic "old-timers" sometimes fondly recall the days when Radio 4 (the Home Service for really old-timers!) was broadcast on the longwave frequency of 200kHz. Division by two gave a perfect 100kHz squarewave and subsequent decade dividers could reduce this to any required value.

#### FREQUENCY CHANGING

Nowadays, getting to 100kHz from 198kHz presents slightly more difficulty. It turns out that the largest factor common to both frequencies is just 2, so to obtain 100kHz one must divide by 99 and then multiply by 50, but not necessarily in that order. Division is easy enough using modern logic, even with an odd number like 99.

Multiplication requires a phase-locked loop (PLL), however, with a divider in the feedback circuit. How this method was used to obtain 100kHz from a 2kHz input is shown in Fig.1. The most important components of the phase-locked loop, a phase comparator and a voltage-controlled oscillator (VCO), are shown here.

In essence, the input is compared with a feedback signal from the voltage-controlled oscillator and if the two are not in phase the phase comparator adjusts a control voltage to bring the oscillator into line with the input. A couple of external components filter the control voltage to ensure stability.



If the output is divided by a discrete factor n before going to the comparator the oscillator will automatically run at n times the input frequency, so frequency multiplication is achieved. Integrated phase-locked loop devices are available with most of the necessary building blocks contained internally.

#### PHASE-LOCKED LOOP

The CMOS 4046 phase-locked loop device has been around for some years and has many useful features, including a phase comparator that can operate happily with signals which do not have equal mark-space ratios, and a high impedance input for the VCO control voltage to simplify the loop filter design.

In the author's first attempt at this design, the 198kHz signal was divided by 99 to obtain 2kHz and then multiplied by 50 using a phase-locked loop. However, unlike dividing circuits these loops are inherently slightly unstable since the output frequency is controlled by a series of minute adjustments of the oscillator control voltage, made each time a phase comparison takes place.

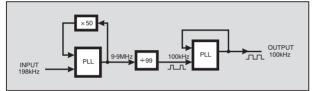


Fig.2. Block diagram showing conversion from 198kHz to 100kHz.

It follows that with an input of only 2kHz and an output of 100kHz the output frequency is only adjusted every  $500\mu$ s, or fifty output cycles, allowing plenty of scope for output phase jitter and perhaps short-term frequency errors. It was decided therefore to try operating the circuit the other way round, with multiplication before division, as this would result in the adjustments taking place very nearly twice per cycle of output frequency.

The block diagram of this is shown in Fig.2, where the first PLL multiplies the 198kHz input to 9.9MHz, which is then divided by 99 to reach 100kHz. The divider circuit output has an uneven mark-space ratio so it is passed through a further PLL, this time without a divider, to produce the same frequency but as a perfect squarewave.

Whilst the intermediate frequency of 9-9MHz is well above the capability of ordinary CMOS, it is well within the range of *high-speed* CMOS devices (74HC series).

#### **RECEIVER CIRCUIT**

Moving on to the circuit shown in Fig.3, this is the Receiver used to obtain the signal "off-air". A lot of difficulty was initially encountered due to feedback from later parts of the circuit, but as soon as the receiver circuit was positioned a metre or so away from the rest of the unit on screened leads these problems vanished.

The Receiver was therefore designed as a separate unit with its own small printed circuit board (p.c.b.). Coil L1 is wound on a short ferrite rod and uses fixed capacitor C1 with variable capacitor VC1 to tune it to



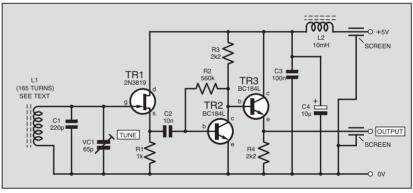
resonance at 198kHz. Field effect transistor (f.e.t.) TR1 buffers this resonant circuit to minimize loading whilst transistors TR2 and TR3 provide voltage gain and buffering of the output before the main circuit.

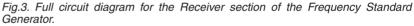
A regulated power supply of 5V is used

as this can be taken from the supply for the following highspeed CMOS circuit. Connections are made with screened twin "figure-of-eight" audio lead, with the power arriving through one core, the output signal leaving through the other and the two screens acting as ground or 0V.

Local supply decoupling is provided by capacitors C3 and C4 with choke L2 in place of the usual resistor, since with a supply of only 5V the voltage drop across a resistor would be unacceptable. The output from this circuit obviously depends on the signal available, but at the author's location, some 100 miles from the Droitwich transmitter, it is about 400mV peak-to-peak.

The circuit also continued to operate well during a period of drastically reduced transmitter power over a maintenance period, suggesting that a much greater operating range is achievable.





CO	MPONEI	VTS	Approx. C Guidance	
Rec	eiver s	ee	Semicondu	
Resistors		SHOP	TR1	2N3819 <i>n</i> -channel field effect transistor
R1	1K <sup>–</sup>	-	TR2, TR3	
R2	010 (0 -#) -	'ALK		(2 off)
All 0.6W 1%	2k2 (2 off) pe metal film	ige	Miscellaneo	aus
Conseitore			L1	ferrite rod, 10mm dia x
Capacitors C1	220p silvered m	lica		100mm length (see text), 165 turns 0.4mm
C2	10n resin-dippe			enamelled copper wire
C3	100n resin-dipp ceramic	ed	L2	10mH choke
C4	10µ radial elect			uit board (Receiver), avail-
VC1	5.5p to 65p trim	mer		e EPE PCB Service, code ontainer, see text.

#### MAIN CIRCUIT

In the main circuit of Fig.4 the signal is first amplified to logic levels. This amplifier was the main cause of feedback problems when the receiver was close to it, since it inevitably re-radiates a little of the amplified signal. After much trial and error, the simple amplifier based on IC1, a CMOS 4007 transistor pair plus inverter i.c., is used as a three-stage amplifier, was found to be by far the most effective.

Each of the first two stages has an a.c. input coupling capacitor (C1 and C2) and a resistor (R1 and R2) to bias it into analogue operation, whilst the third stage buffers the output.

Next IC2, a 74HCT7046 which is a high-speed version of the 4046 PLL, raises the frequency to 9-9MHz. To do this it has a divide-by-50 circuit in its logic feedback, provided by binary divider IC3 and one half of the dual quad-input AND gate IC4a, again high-speed types. The gate decodes three outputs from IC3 and when these reach the binary equivalent of 50 it pulses IC3's Reset pin.

Preset VR1 is used to set the VCO to the centre of its control voltage range at the normal operating frequency. The 9-9MHz output from IC2 pin 4 is divided by another high-speed binary divider IC5, used with IC4b to divide this time by 99, again by decoding the divider outputs and pulsing the Reset pin (2) of IC5.

Two outputs are available from this part of the circuit. The first is raw 198kHz from IC1, at socket SK1. The second is the 100kHz from IC5 at SK2, which may be useful for checking frequency counters although it does not have an even markspace ratio. Both of these are 0V to 5V logic-level outputs.

#### LOGIC LEVEL SHIFTING

The high-speed versions of CMOS *must* have a supply of 5V so this is supplied by the 5V positive 100mA regulator IC6, which also supplies the receiver.

For reasons which will be explained, most of the rest of the circuit (IC8 and beyond) operates from a 12V supply. Consequently, IC8 acts as a comparator to convert the 5V logic output of IC5 into a 12V logic output. IC8 is a CA3130 CMOS op.amp, which is fairly fast and has a railto-rail output.

The signal from this drives IC9, this time a standard 4046 CMOS PLL. The purpose is to convert the input to a perfect 50:50 squarewave output. Preset VR2 is used to set the optimum operating point for the VCO, and the output is taken to the first of six output buffers provided by IC15.

The signal from IC8 also goes to the first of the string of five decade dividers IC10 to IC14, giving a series of frequencies down to 1Hz. It doesn't matter that the input to IC10 is not a squarewave since the output will be anyway. All the outputs are buffered by the remaining five buffers of IC15.

The reason for the 12V supply can now be explained. IC15 does more than simply buffer the outputs, it is also capable of "voltage translation", meaning that its output signal "high" or positive level is determined by its supply voltage. If a suitable variable supply is provided for this i.c. its output can be adjusted from about 3V to 15V. However, for this to work the input signal "high" voltage must be greater than half the maximum supply voltage, so it is necessary to raise the supply voltage from 5V used by the first part of the circuit to the 12V used by the rest. The 12V supply is provided by regulator IC7 and the variable supply is generated by IC16, an LM317 adjustable positive regulator controlled by panel-mounted potentiometer VR3 used as a variable resistor.

Pushbutton switch S2 is fitted for resetting all the counters simultaneously so that they can be synchronized to an external event if necessary. As a manual switch this is really only useful for synchronizing the final output which counts seconds, but some form of electronic switching could be added here if this feature is required for a particular application.

It works by pulling all the reset inputs high very briefly at the instant the line from S2 goes positive. Resistor R11 and capacitor C20 ensure that only one reset pulse takes place for each operation of S2, eliminating the effects of switch bounce.

Power for all three regulators comes from the centre-tapped transformer T1 and rectifier diodes D1 and D2, together with main supply decoupling capacitor C14. T1 is a 15V-0V-15V type which produces about 20V of unregulated output in this circuit.

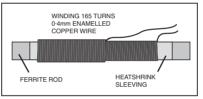


Fig.5. Ferrite rod aerial winding details. Using 0.4mm enamelled copper wire and starting from one end, 100 turns are close-wound on the "sleeving" and then a further 65 turns are wound over this, working back towards the start, to give a total of 165 turns.

#### ANTENNA WINDING

Despite the apparent complexity of the circuit diagram, this is a relatively simple circuit to construct and test. It is suggested that the Receiver should be built and tested first as this is required for testing the remainder of the project.

The antenna is wound as shown in Fig.5 on a 10cm  $\times$  10mm diameter ferrite rod. The one obtained for the prototype had rather sharp lengthwise moulding edges so these were smoothed off with a file and a length of heat-shrink sleeving was fitted over it. Warming the ferrite a little before attempting to shrink the sleeve proved helpful for this process. The coil was then wound onto it using 0.4mm enamelled copper wire, which is relatively thick and easy to handle.

Starting about 10mm from one end, 100 turns were close-wound into position, then a further 65 turns were wound over this going back towards the start, giving a total of 165 turns altogether. The winding was secured with insulating tape, taking care to prevent the wire from coming into contact with the ferrite to avoid the possibility of insulation damage.

The tuning range of trimmer capacitor VC1 is quite small so if another type of rod

## **COMPONENTS**

#### **Digital Circuit**

DI	
Resistors R1, R2 R3 R4, R9 R5, R10 R6 R7, R12 R8 R11 R13 R14 All 0.6W 1% m	47k (2 off) 15k 220k (2 off) 2k2 (2 off) 39k 10k (2 off) 100k 470k 470Ω 560Ω tetal film
Potentiomet	ers
VR1 VR2 VR3	<ul> <li>10k min. preset, horiz.</li> <li>100k min. preset, horiz.</li> <li>4k7 (or 5k) rotary carbon, lin.</li> </ul>
Capacitors	
C1, C2	10n resin-dipped ceramic
00.001	(2 off)
C3, C21 C4 to C8,	100p ceramic (2 off)
C10, C12, C13, C15, C16, C19, C20, C22, C24	100n resin-dipped ceramic (14 off)
C9, C11, C23	10., radial alast EOV
623	10μ radial elect. 50V (3 off)
C14	470µ radial elect. 35V
C17 C18	470p ceramic 470n resin-dipped
010	ceramic
0	• • • •
D1, D2	1N4001 rec. diode (2 off)
IC1	4007UBE dual CMOS
IC2	transistor pair/inverter 74HCT7046AE phase-
102	locked loop
IC3, IC5	74HC4024 7-stage binary counter (2 off)
IC4	74HC21 dual quad-input
IC6	AND gate 78L05 +5V 100mA
IC7	voltage regulator 78L12 +12V 100mA
	voltage regulator
IC8 IC9	CA3130E CMOS op.amp 4046 phase-locked loop
IC10 to	
IC14	4017B decade counter (5 off)
IC15	4050 hex buffer
IC16	LM317 adjustable positive voltage
	regulator
Miscellaneo	2
S1	d.p.s.t. mains switch
S2	push-to-make switch
T1	15V-0V-15V 250mA mains transformer

Printed circuit board (Digital), available from the EPE PCB Service, code 354; 8pin d.i.l. socket; 14-pin d.i.l. socket (4 off); 16-pin d.i.l. sockets (8 off); 4mm chassis socket, red (8 off); 4mm chassis socket, black; metal case, see text; p.c.b. mounting supports, to suit; twin screened audio cable, see text; connecting wire; solder, etc.



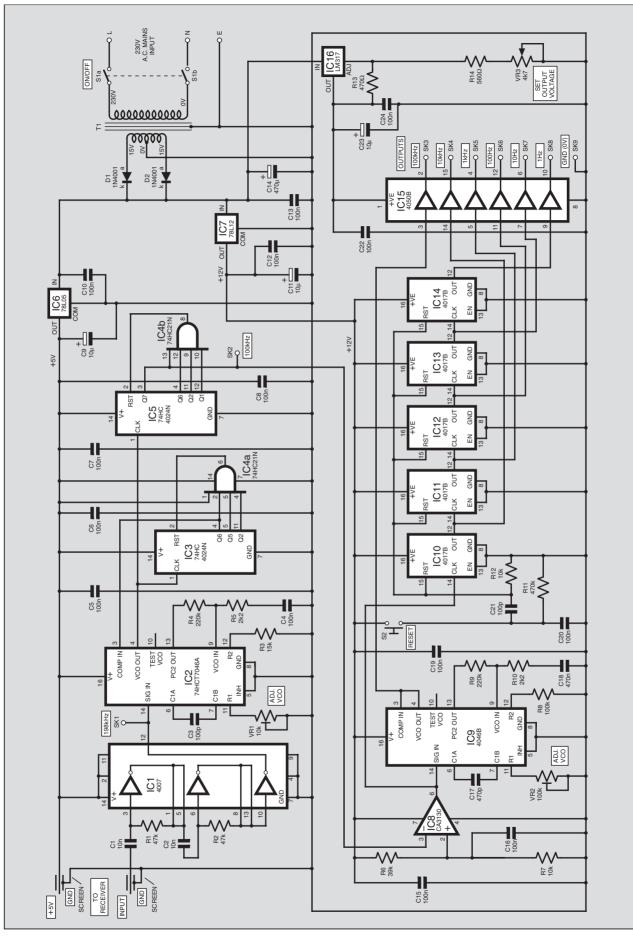
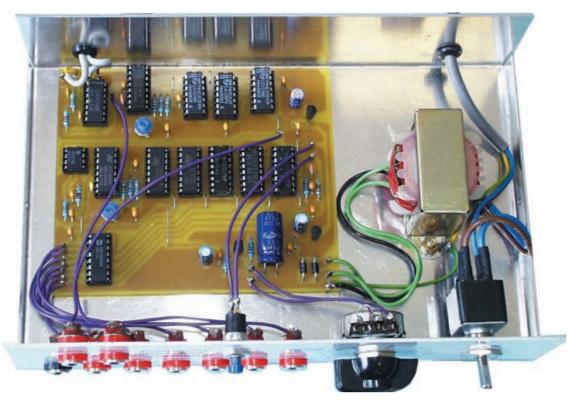


Fig.4. Complete circuit diagram for the Digital section of the Frequency Standard Generator.



is employed or difficulty is experienced in tuning a check of the resonant frequency may be needed. A simple method often used by the author is shown in Fig.6. It requires a frequency generator and an oscilloscope and is an extremely easy way to find the resonant frequency since the peak produced is quite unmistakable, much greater than those due to harmonics.

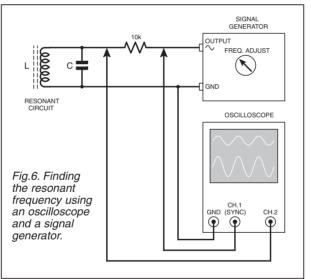
Turns can be simply added or removed on the coil until the desired point is reached.

#### RECEIVER CONSTRUCTION

The component layout of the Receiver p.c.b. is shown in Fig.7 and construction should present no problems. Note that capacitor C1 is a silvered-mica type for maximum stability. When powered at 5V with the antenna attached, it forms part of the biasing circuit and a small d.c. voltage should appear at the source (s) of TR1. The actual value of this voltage will depend on the characteristics of the individual f.e.t. used for TR1 but a figure of 0.5V to 2V should be acceptable.

Likewise, about 1.5V should be present at the emitter (e) of transistor TR3 though this will be dependent to some extent on the gain of TR2. A scope can be used to set the tuning, but if one is not available the test circuit shown in Fig.8 works well.

The diode drops about 0.5V so the output will be about 1V plus the peak value of



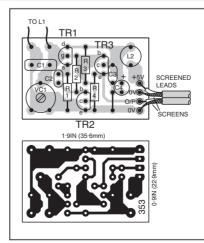


Fig.7. Printed circuit board component layout, wiring and full-size copper foil master pattern for the Receiver.

the signal, so tuning should be adjusted for the maximum obtainable value. An analogue meter may be found preferable to a digital one when making this adjustment.

It should be remembered that the signal is amplitude modulated – a bit of a nuisance this, really! – so the level will fluctuate a little. Ferrite aerials are also very directional, so if difficulty is experienced in finding the signal it may be helpful to use a longwave radio receiver to find the correct orientation for it.

The receiver and aerial must be housed in a non-metallic case so that the radio signals can penetrate to them. The prototype uses the plastic tube from a pack of effervescent vitamin C tablets, which is about the right size and can easily be made waterproof. Small pieces of foam plastic secure the board and ferrite rod in place inside the tube.

Interconnection between Receiver and Digital board is made through "figure-of-8" twin screened audio cable. A couple of metres is all that is required, though in some areas of weak signal it may be preferable to place the receiver in an elevated or external location for reliable results.

#### ASSEMBLY OPTIONS

If the full facilities of this project are not required it may not be necessary to construct all of it. For example, if a quick test of a frequency meter is all that is required, the Receiver on its own may be all that is

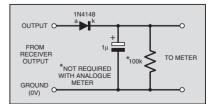


Fig.8. Simple test circuit for tuning the Receiver.

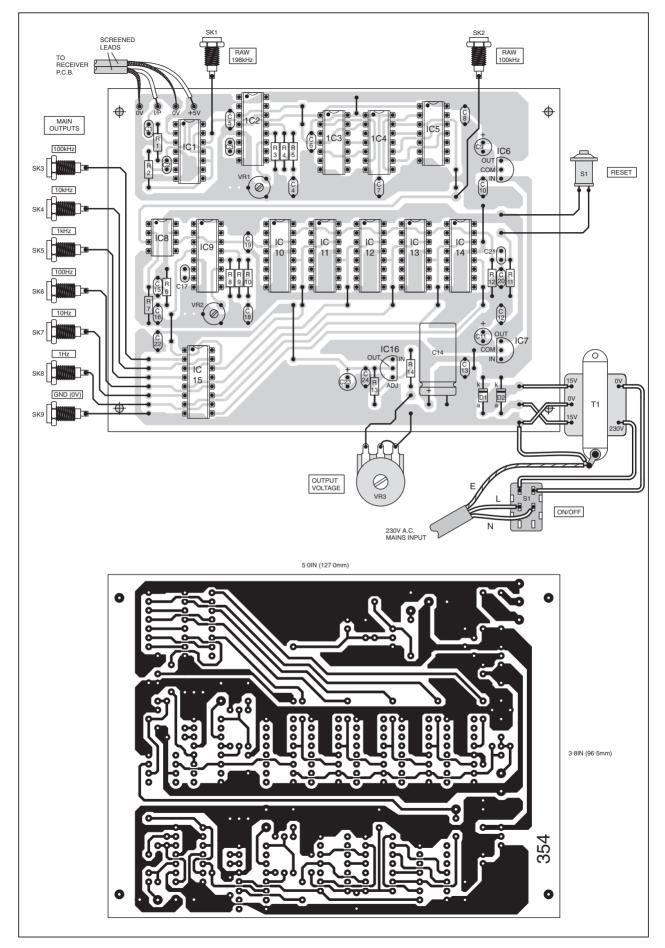


Fig.9. Printed circuit board component layout, wiring and full-size master for the Digital board.

current consumption should now be about 25mA to 26mA.

After this the five 4017B decade dividers, IC10 to IC14, can be fitted. This made no perceptible difference to the supply current of the prototype. Their square-wave outputs, at pin 12, can be checked if required.

This leaves just the output buffer IC15 and its supply regulator to be fitted to complete the board. The variable regulator IC16 should be fitted first. With VR3 tem-

porarily connected, the board should be powered again and the output from IC16 checked, pin 1 of the socket for IC15 can be used for this. Note that the pinout for IC15 is different from most CMOS devices in that, although pin 8 is negative, the positive supply is applied to pin 1. Rotating VR3 should cause this supply to vary between about 3V and 15V. If this works, IC15 can be inserted and its outputs checked.

#### ENCLOSURE

Having completed the main board, it can now be fitted into the case of the constructor's choice and connected to the output sockets and the transformer as shown in Fig.9. A metal case is recommended, connected to mains earth as shown through a solder tag under one of the mounting bolts of transformer T1. This connection is essential as the high frequencies around IC2 to IC5, plus the squarewave nature of the signals throughout the circuit, can radiate some interference. Use of an earthed metal case does much to reduce this.

The outputs from this circuit can be used for many purposes, including the testing of digital circuits where the ability to vary their input signal voltage and use several outputs simultaneously should come in very useful (but do not exceed the power line voltage of the i.c.s. under test).

However, the primary virtue of this design is its phenomenal accuracy and stability. There will not usually be many really accurate standards of any kind in the workshop of a home constructor since they are usually prohibitively expensive. This design provides an exception to this rule by bringing a national frequency standard right onto the amateur's bench.

It should prove useful for checking and adjusting the calibration of frequency meters, oscilloscopes, and any other equipment used for measuring or generating frequency of any kind.



needed. If the output level from this is insufficient, the amplifier section of IC1 of the main board and its associated components should be sufficient to do the job.

The p.c.b. component and track layout for the full digital circuit is shown in Fig.9. To simplify testing following construction, dual-in-line (d.i.l.) sockets are recommended for all the i.c.s, except, of course, for the three voltage regulators.

Where a current-limited bench power supply is available, use of this would be preferable to simply connecting the unit to its transformer and powering up. It can be connected across the leads of capacitor C14.

Construction should begin with the fitting of all the passive components, links, resistors, diodes and capacitors, then the d.i.l. sockets, then the 5V regulator IC6. The two presets VR1 and VR2 can also be fitted, holes are provided to accept a variety of types.

#### TESTING

If the circuit is powered, with around 18V d.c. from a bench supply, the presence of the 5V regulated supply can be checked at the top right-hand pin of any of the upper five d.i.l. sockets. It should draw only a couple of milliamps from the supply at this stage.

If the receiver is connected and IC1 inserted into its socket, this should rise to about 5mA in total. The d.c. voltage at pin 12 of IC1 should measure as about 2.5V, indicating (hopefully!) that the output is operating at 198kHz squarewave. If a 'scope or frequency meter is available, these can be used to check it, of course.

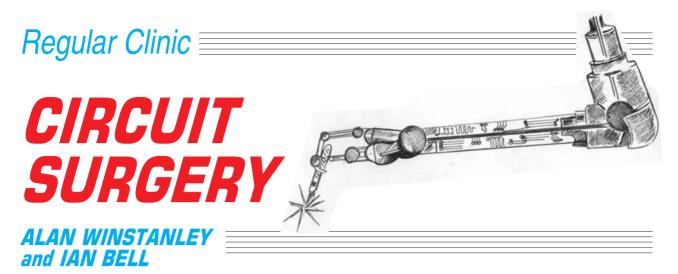
Next, IC2, IC3, IC4 and IC5 can be inserted. These all work together so there is really no way of testing them individually. The method of setting up the operating point of IC2 is quite simple, a DVM should be connected between ground (negative) and pin 10 (Test VCO, positive) and VR1 carefully adjusted for a reading of about half the supply, or 2.5V.

The PLL should then be locked and working at the correct frequency and optimum VCO operating point. The output from IC5 pin 3 should now be exactly 100kHz, although it will not be a squarewave. A meter connected to it should read about 1.75V d.c., and a scope will show it as positive-going pulses. The overall supply drain should now be about 20mA.

Next the 12V regulator IC7 should be fitted and the presence of its output checked. This should appear at all of the positive supply pins for the logic i.c.s on the lower part of the board. The current drawn by IC7 should raise the supply current to about 23mA. If this checks out IC8 can be fitted, which will add another 1mA or so.

Following this the second PLL, IC9, can be fitted and adjusted in a similar manner to the first by monitoring the voltage at pin 10 whilst adjusting VR2. In this case, though, since the supply is 12V, the voltage set at this pin should be about 6V. The 100kHz squarewave output should now be available from pin 4 of IC9 and, of course, the average d.c. voltage measured here should be half the supply, or 6V. Total

Everyday Practical Electronics, June 2002



Our monthly roundup of readers' queries looks at Darlington transistors, ambidextrous hands, a grounding problem and a close-up on X-Class and Y-Class capacitors

#### All the way to Darlington

**Muhammed Abdallah Saif** from Ngora, Uganda emailed to ask for an explanation of the operation and uses of Push-Pull transistors and "Darlington" pairs. Both of these are circuits comprising two transistors, and are commonly (but not exclusively) used in outputs stages of circuits including high power applications.

The basic ideal of a push-pull circuit is to use two complementary transistors (an *npn* and *pnp* matched pair) to drive current through the load in opposite directions (hence *push* and *pull*). A classic application is in analogue power amplification at both low and high power levels, the most basic form of which is shown in Fig.1.

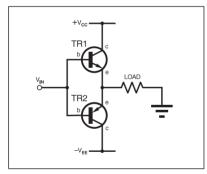


Fig.1. Basic push-pull amplifier circuit arrangement.

The basic push-pull output stage suffers from a problem called *crossover-distortion*, which is important if the signal in the load must be an accurate copy of the input (e.g. in a high fidelity audio circuit). The effect of crossover distortion on a waveform is illustrated in Fig.2.

In Fig.1 only one transistor can be on at any time, that is: if  $V_{in} > V_{BE}$  (the baseemitter voltage of the transistor) then TR1 is conducting, and if  $V_{in} < -V_{BE}$  then TR2 is conducting instead. But this means that for small inputs neither transistor is on: if  $-V_{BE} < V_{in} < V_{BE}$  then TR1 *and* TR2 are both off. By using appropriate bias circuits, which hold both transistors at the point of conduction, this problem can be overcome.

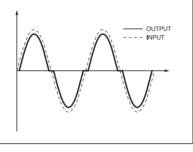


Fig.2. Waveform showing crossover distortion.

Elimination of crossover distortion is not always required, particularly if the pushpull circuit is driven with a digital signal (e.g. to drive a motor full speed forward or reverse). The H-bridge circuit used in the "Dog and Cat Scarer" discussed in May 2002 *Circuit Surgery* uses two push-pull circuits driven in opposite directions. Although this is an "audio" application hifi sound is not required; and the signal source is a square wave so crossover distortion is not an issue – the basic push-pull circuit can be used.

In the Darlington configuration, one transistor is used to drive another resulting in a very high gain (typically thousands) and high input impedance. A couple of variations on the theme are shown in Fig.3. The Darlington pair behaves like a single transistor with twice the value of  $V_{BE}$  (i.e.

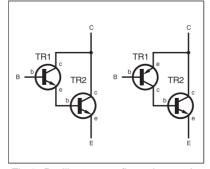


Fig.3. Darllington configurations using a driver transistor, TR1, to provide base current to TR2, which could be a power device.

it has a higher switch-on voltage) and gain equal to the product of the gain of the two transistors. The input resistance is increased by a factor comparable with the gain of (one of) the transistors. One disadvantage is that the configuration can be rather slow in switching.

In high power applications the first transistor (TR1) makes sure that the power transistor (TR2) receives sufficient base current to fully turn on. A high power transistor may require significant base current, which would not be readily available from the controlling circuit's output (e.g. logic gate) if it were connected directly.

Darlington configurations can be used in situations other than power outputs, for example a Darlington arrangement can be used to increase the gain of a phototransistor. Lastly, remember that you can buy "ready-made" Darlington transistors that make life easier. They have a very high gain, say 20-50,000 or so. *I.M.B.* 

#### More Scope for Grounding

"With respect, your answer to Gerard Galvin in the first section of Circuit Surgery in the April '02 issue seems to make no sense! With reference to Fig.1 in April and just considering measurements: if the oscilloscope has its probe's screen and croc-clip joined to earth (ground) via the mains plug, and the power supply circuit under test has its 0V rail grounded via its mains plug as well, then the probe's ground connection can short out nothing when connected to 0V in the Power Supply. In fact the croc clip does not need to be connected at all!

"It seems to me that there is more "potential danger" of doing damage if the power supply has its output fully floating. In this case when connecting the probe's earth to the 0V line of the supply, that will be pulled to ground potential. But even here it is difficult to see what the damage might be." Dan Woods via email.

We were attempting to refer to the problem of measuring the voltage between two points in a circuit *other than ground*. If the p.s.u. 0V rail is grounded and so is the scope probe, then if you connect the probe croc clip anywhere in the circuit other than 0V you obviously connect that point to ground. How a component can be accidentally shorted to 0V in this way is illustrated in Fig.4.

As we said in the article, if the power supply is floating then there should be no d.c. path to earth so connecting a single scope ground should not cause a problem. It is often true that the croc clip might not need to be connected at all, but only if the reference point is ground in both cases. *I.M.B.* 

#### **Ambidexterity Rules OK**

I have just received the April issue of EPE and on scanning the articles I am puzzled by the item in Teach-In 2002 Part 6 on page 252. Fig.6.16 shows Fleming's Right Hand Rule while the text speaks about:

"We sometimes need to know the force exerted by a magnetic field given the direction of movement of a charge and the magnetic field's direction."

I am a retired Electronic Engineer and I am sure Fleming's Rule was always taught as: Left Hand Rule for Motors (i.e. Magnetic Field and Current Known – Direction of Motion required); Right Hand Rule for Generators (i.e. Magnetic Field and Motion Known – Current Direction required.) The rule shown in Fig. 6.16 would appear to be the wrong one, or does my memory fail me! Good magazine, keep it up. John Avery via email.

Your memory is fine, Mr. Avery! To clarify, what we were actually referring to in the *Teach-In* text was the force exerted on the electrons, not a physical force placed on the conductor itself.

You are right to say that Fleming's Left Hand Rule relates to the "motor effect", and it describes the direction of force with respect to the direction of the magnetic field and electric current. With fingers and thumb of the left hand held perpendicular as in the Right Hand Rule, if the first finger points in the direction of the field and the second finger points in the direction of current, then the thumb points in the direction of motion. This gives us the principle of the electric motor. *ARW*.

#### X/Y Capacitors

*Mr C. A. White* writes by email: "Wanting a method of driving a 6V to 12V piezo buzzer from the mains without using a transformer/relay, I trawled back through my stack of Everyday Electronics (and its progeny to the early seventies!) and I found "Transformerless P.S.U.s" by Andy Flind (EPE February 1995).

"I was aware of "X" and "Y" rated capacitors but could only find one rather expensive one in an old Maplin catalogue. When I went into my local components store, I found that we also had X2 and Y2 and even, I believe, Y1(X1). Can you explain the difference between X1, X2, Y1, Y2 types?"

This question has cropped up several times recently in my mailbag. The full answer involves looking through some scary technical standards, which goes beyond the scope of *Circuit Surgery*, but the basic aspects are as follows.

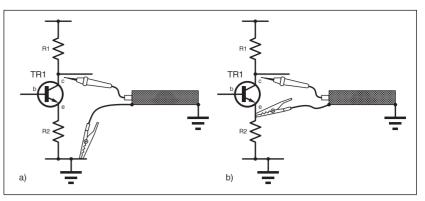


Fig.4. (a) "Safe" measurement – both referenced to ground and (b) "unsafe" measurement – resistor R2 is shorted to ground, via croc-clip and cable screen.

Constructors usually first come across the terms "X1" "Y1" etc. when dealing with mains-rated filter capacitors. These are designed to be fitted directly across the mains supply between live and neutral, but certain types are safe enough to be fitted between live-earth and neutralearth. A Class X capacitor can go across Live and Neutral, but **only a Class Y should be connected to the Earth terminal** as they are designed to be able to bridge insulation in the equipment: there is much less chance of them causing a shock should they fail.

Next, voltage ratings: filter capacitors have to withstand spikes and surges on the mains caused by everything from e.g. nearby equipment operating (say 400V-800V spikes for up to 1 millisecond) all the way up to 6kV or more caused by lightning impulses. It is said that 80% of all transients last no more than 10 microseconds with amplitudes up to 1.2kV; in their working life a suppressor will have to withstand thousands of such spikes without catching fire, blowing out or failing catastrophically.

The Euronorm EN 132 400 contains the technical information relating to mains filter capacitor specifications. Amongst other things, the standards cover flammability, pulses and ageing and the circumstances in which they are to be connected to the mains. A Class X1 type of  $1.0\mu$ F or less has been impulse tested at 4.0kV whilst Class X2 is the most common, and is tested up to 2.5kV. The latter will be found, where applicable, in electrical equipment that is plugged into the mains.

Being connected directly to ground, Y Class capacitors are designed so as not to



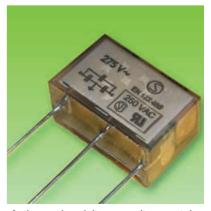
A Class X2 capacitor for direct connection across the mains supply.

be a source of electric shock should they fail. There are several classes that design engineers have to consider, and which class to select also depends on the insulation characteristics of the equipment itself. As far as I know, only Class Y1 and Y2 are used, the latter being the most popular for use up to 250V and are impulse tested to 5kV (Class Y1: 8kV).

#### **Delta Force**

A convenient arrangement is the *delta* capacitor which has three wires and contains an X Class  $0.1\mu$ F straight across the mains, plus two Y Class 5,000pF capacitors between live/earth and neutral/earth. Some stud-mounting types even have a built-in suppressor choke. For many suppression jobs a delta capacitor across the mains is effective enough, and remember that they can also help prevent "noisy" equipment from feeding RFI back into the mains as well.

If I may round off with a true story: The "boss" of the writer's household owns an awesome Swiss-built motorised double-bed knitting machine, a wonder to behold when it's in full flight. The author was in the process of photographing some components including an X Class RFI capacitor, when a "pop" was heard to come from the room next door. A dense plume of acrid smoke was pouring from the back of the machine's motor controller, which was hastily isolated from the mains. Would you believe that its X Class RFI filter capacitor had just failed? The replacement was fitted in less than two minutes - once I'd finished taking its photograph that is! ARW.



A three-wire delta capacitor contains an X class and two Y Class capacitors.







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#### ACCESSING SERIAL PORTS VIA MSCOMM CONTROL

HERE has been some discussion in the **T**Readout pages about a free version of Visual BASIC, and I have received a few enquiries on the same subject. Reasonably enough, Microsoft does not give away the full product, but it did produce a demonstration version of Visual BASIC 6.

Unfortunately, this does not seem to be available as a download on their web site, and it was only given away by one or two computer magazines on their cover-mounted discs. Your chances of tracking it down are now probably negligible.

#### In Control

The next best thing is Visual BASIC 5 CCE (control creation edition), which is available as a free download from the www.ms.com web site. Using something like "Visual BASIC CCE" in the main search engine should soon track down the program file and some useful documentation.

The download search engine does not seem to have heard of this program, so it is best to use the main one. This version is primarily intended for producing ActiveX controls, but the Standard EXE option is available from the opening screen.

Neither the demonstration version of Visual BASIC 6 nor CCE version 5 has the ability to compile standalone EXE files or program groups. However, both versions have the option to compile and run programs from within the programming language itself. This is much like using an interpreted language, but with the speed of a language that is largely compiled. The usual Save and Open functions are also available. Therefore, you can still write and develop your own applications, and at zero cost.

Many people have a form of Visual BASIC already installed on their PCs, but are unaware of its

existence. Some of the more major applications programs, such as Microsoft's Office, are supplied with Visual BASIC for Applications, or VBA as it is better known. This is intended as a means of adding functions to applications programs, but it can be used to develop simple software for PC add-ons.

However, this is a rather clumsy way of doing things, and the range of applicable components available in VBA seems to be a bit limited. I would definitely recommend forgetting VBA for this type of thing and downloading Visual BASIC 5 CCE instead.

#### **MSComm**

Using serial ports for communications with add-ons via a UART was covered in the previous Interface article. The hardware for serial interfacing is reasonably

straightforward, but how does the software make contact with the add-on device?

One option is to directly control the serial port hardware using INP and OUT instructions, or an equivalent to these. This is not the only method available though, and most high level programming languages have instructions that provide access to the serial and parallel ports.

The approved method of accessing the serial ports using Visual BASIC is to use the MSComm control. Unfortunately, this option is only available to those using the Professional and Enterprise editions. It does not seem to be present in any of the free versions or in the low-cost versions such as the Standard edition. This control is not included in the Toolbox by default, and it must be loaded before it can be used.

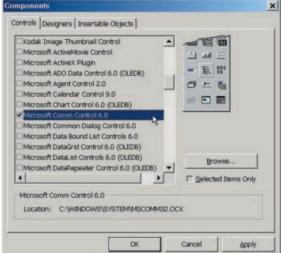


Fig.1. The MSComm control must be switched on via the Components window.

> In order to do this, first select Components from the Project menu, which will produce a window showing all the available components (Fig.1). Scroll down the list for a control called "Microsoft Comm Control 6.0". You do not have a suitable version of Visual BASIC if this control is not listed. Assuming it is present and correct, place a tick in its checkbox and then operate the Apply button followed by the Close (OK) button. The icon for MSComm, which looks like a yellow telephone, should then appear in the Toolbox.

#### On Form

Probably the best starting point with MSComm is a simple program that will output values to a serial port. Enlarge the form slightly and then add an MSComm control to it. The position of the icon for

this control is unimportant because it will not be displayed when the program is run. It is only shown on the form so that its parameters can be accessed via the Properties window. Simply position it out of the way in a corner of the form.

With the MSComm icon selected, the Properties window will show a number of parameters for it. The defaults might be suitable, but it is important to check down the list to ensure that the settings are all acceptable.

It is particularly important to check that the CommPort setting is correct. This defaults to a value of 1, which means that the control is used for communications via serial port 1 (Com1). This must be changed to a different value if a different port will be used for this test. Note that a different MSComm control is required for each serial port if a program will be used with more than one of these ports.

The Settings parameter controls the baud rate, the type of parity checking, the number data bits, and the number of stop bits. By default this will be 9600-baud, no parity, eight data bits, and one stop bit (9600,n,8,1). This word format is the best one to use when interfacing user add-ons to a PC, but a baud rate of 19200 might be preferable in some applications as it gives double the rate of data transfer. To double the baud rate to 19200 simply delete 9600 and replace it with 19200. Windows supports some "turbo"

baud rates, and one of these can be used if a higher rate of transfer is required. The highest rate that is likely to work is 115200 baud, which provides a maximum transfer rate of about 11 kilobytes per second. Note that the figure used for the baud rate parameter must correspond to one of the standard rates supported by Windows. Trying to set any baud rate that happens to take your fancy will simply produce

an error message. Various handshaking options are available, but handshaking is not needed with most user add-ons. The speed of the peripheral device and the PC, together with the limited flow of data, make it unnecessary. If handshaking is not required, make sure that the None option is selected. There might otherwise be problems with things grinding to a halt due to the PC expecting handshake signals that it does not receive.

#### Hitting the Buffers

Two further parameters that can be of importance are InBuffer and OutBuffer. It is not necessary for programs to read each byte of data as it is received. Instead, data can be stored in a section of memory called a buffer, and then read when the appropriate number has been received. Similarly, a program does not have to wait for one byte to be transmitted before it sends the next one to the serial port. A block of data can be sent, and it will be stored in the buffer. The operating system then handles the transmission of this data.

In both cases it is obviously essential for the buffer to be large enough, or the buffer will overflow and data will be lost. This process is sometimes called "hitting the buffers", and it more or less guarantees a breakdown in communications between the PC and the peripheral device.

The InBuffer and OutBuffer parameters respectively set the sizes of the input (receiving) and output (transmitting) buffers in bytes. In most cases the default values will do, but higher values might be needed in applications that send or receive blocks of data. The buffer must be comfortably larger than a block of data.

#### Adding Components

Having made any necessary adjustments to the MSComm parameters it is time to add some components to the form so that values can be sent to the selected serial port. Add a large horizontal scrollbar, a command button, and a large label with an equally large font size. The scrollbar will be used to generate the values that will be sent to the port, so its Max parameter must be set at 255. Delete the default caption of the label, and change the button's caption to "Exit".

Next, the following three subroutines should be assigned to the button, the form, and the scrollbar respectively (no subroutine is needed for the label):

Private Sub Command1\_Click() MSComm1.PortOpen = False End End Sub

Private Sub Form\_Load() MSComm1.PortOpen = True End Sub

Private Sub HScroll1\_Change() MSComm1.Output = Chr\$(HScroll1.Value) Label1.Caption = HScroll1.Value

End Sub

Before a port can be used for sending or receiving data it must be opened. This

Fig.2. The completed program ready for testing.

is done by setting the PortOpen parameter of MSComm1 to True when the form loads. It is standard practice for a port to be closed when the program using it is either closed or does not require the port any more. This leaves the port available for other programs to use. In this case the program is closed by operating the EXIT button, which first sets PortOpen as False so that the port is closed. It then uses an End instruction to close the program.

Data is sent to the serial port by the subroutine assigned to the horizontal scrollbar. Changing the setting of the scrollbar triggers its subroutine, which sets MSComm1.Output to the new value read from the scrollbar. This value is in the variable called "Hscroll1.Value". The obvi-0115 method of simply making MSComm1.Output equal to HScroll1.Value does not work. A character or string seems to be needed rather than a number or a numeric variable.

There are ways around this problem, and in this case the Chr\$ function is used. This generates a character equal to the ASCII code number inserted in the brackets, and this value is provided by the scrollbar.

Presumably, MSComm1 then duly changes this character back to its equivalent code number, which then is sent to the serial port. This seems a bit mad to say the least, but it is the only way I could get it to work properly. The next line in the subroutine writes the new scrollbar value to the label so that the user can see what values are being sent.

#### **XP** Compatibility

Having completed the program you

should have something like Fig.2. The prototype program in operation is shown in Fig.3. Since writing data direct to the serial port hardware is a very simple process, using MSComm might seem to be a pointless exercise.

However, it does have one or two advantages. It does not require any third party add-ons such as Inpout32.dll, but note that the file MSComm32.ocx must be present in the C:\Windows\System folder for the compiled program to work. This is the ActiveX control that provides communication with the serial port. It will be installed on your PC as part of the Visual BASIC installation.

The biggest advantage of using this method is that it accesses the port via the operating system. With Windows 95, 98, and ME it is acceptable to directly access ports, but this is not permitted with Windows NT4, 2000, or XP. Ports have to be accessed via the official channels so that the operating system can prevent two programs trying to access the same port simultaneously.

The practical consequence of this is that programs do not work with Windows NT4, etc. if they use Inpout32.dll, assembly language routines, or any other method of directly accessing the ports. Ploys such as using the Windows XP compatibility modes do not seem to get around this problem.

On the other hand, using MSComm to access the serial ports results in the data going via the operating system, and should give compatibility with any 32-bit version of Windows. Fig.4 shows the test program running under Windows XP. It was found to be perfectly stable and changes in the scrollbar produced the correct data from the serial port. This contrasts with programs that directly access the ports, which at best appear to run correctly but do not communicate with the ports at all.

As Windows ME and its predecessors are due to be phased out and replaced by Windows XP, compatibility will become an increasingly important issue.



Fig.3. The test program running under Windows ME.

S Form1		
	214	
•		( ·
	EXIT	

Fig.4. The test program runs properly under Windows XP, and even "talks" to the serial port.

## EPE IS PLEASED TO BE ABLE TO OFFER YOU THESE ELECTRONICS CD-ROMS



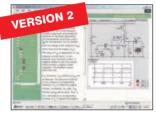
Logic Probe testing

#### **ELECTRONICS PROJECTS**

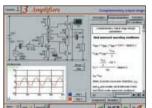
Electronic Projects is split into two main sections: Building Electronic Projects contains comprehensive information about the components, tools and techniques used in developing projects from initial concept through to final circuit board production. Extensive use is made of video presentations showing soldering and construction techniques. The second section contains a set of ten projects for students to build, ranging from simple sensor circuits through to power amplifiers. A shareware version of Matrix's CADPACK schematic capture, circuit simulation and p.c.b. design software is included.

The projects on the CD-ROM are: Logic Probe; Light, Heat and Moisture Sensor; NE555 Timer; Egg Timer; Dice Machine; Bike Alarm; Stereo Mixer; Power Amplifier; Sound Activated Switch; Reaction Tester. Full parts lists, schematics and p.c.b. layouts are included on the CD-ROM.

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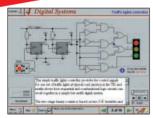


Circuit simulation screen



Complimentary output stage

#### VERSION 2



Virtual laboratory – Traffic Lights

Provides an introduction to the principles and application of the most common types of electronic components and shows how they are used to form complete circuits. The virtual laboratories, worked examples and pre-designed circuits allow students to learn, experiment and check their understanding. Version 2 has been considerably expanded in almost every area following a review of major syllabuses (GCSE, GNVQ, A level and HNC). It also contains both European and American circuit symbols. Sections include: *Fundamentals:* units & multiples, electricity, electric circuits, alternating circuits. *Passive Components:* resistors, capacitors, inductors, transformers. *Semiconductors:* diodes, transistors, op.amps, logic gates. *Passive Circuits. Active Circuits. The Parts Gallery* will help students to recognise common electronic components and their corresponding symbols in circuit diagrams. Included in the Institutional Versions are multiple choice questions, exam style questions, fault finding virtual laboratories and investigations/worksheets.

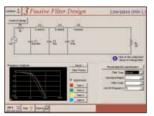
#### **ANALOGUE ELECTRONICS**

Analogue Electronics is a complete learning resource for this most difficult branch of electronics. The CD-ROM includes a host of virtual laboratories, animations, diagrams, photographs and text as well as a SPICE electronic circuit simulator with over 50 pre-designed circuits.

Sections on the CD-ROM include: **Fundamentals** – Analogue Signals (5 sections), Transistors (4 sections), Waveshaping Circuits (6 sections). **Op.Amps** – 17 sections covering everything from Symbols and Signal Connections to Differentiators. **Amplifiers** – Single Stage Amplifiers (8 sections), Multi-stage Amplifiers (3 sections). **Filters** – Passive Filters (10 sections), Phase Shifting Networks (4 sections), Active Filters (6 sections). **Obscillators** – 6 sections from Positive Feedback to Crystal Oscillators. **Systems** – 12 sections from Audio Pre-Amplifiers to 8-Bit ADC plus a gallery showing representative p.c.b. photos.

#### **DIGITAL ELECTRONICS V2.0**

Digital Electronics builds on the knowledge of logic gates covered in Electronic Circuits & Components (opposite), and takes users through the subject of digital electronics up to the operation and architecture of microprocessors. The virtual laboratories allow users to operate many circuits on screen. Covers binary and hexadecimal numbering systems, ASCII, basic logic gates, monostable action and circuits, and bistables – including JK and D-type flip-flops. Multiple gate circuits, equivalent logic functions and specialised logic functions. Introduces sequential logic including clocks and clock circuitry, counters, binary coded decimal and shift registers. A/D and D/A converters, traffic light controllers, memories and microprocessors – architecture, bus systems and their arithmetic logic units. Sections on Boolean Logic and Venn diagrams, displays and chip types have been expanded in Version 2 and new sections include shift registers, digital fault finding, programmable logic controllers, and microcontrollers and microprocessors. The Institutional versions now also include several types of assessment for supervisors, including worksheets, multiple choice tests, fault finding exercises and examination questions.



Filter synthesis

#### FILTERS

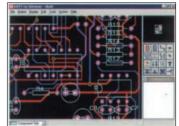
Filters is a complete course in designing active and passive filters that makes use of highly interactive virtual laboratories and simulations to explain how filters are designed. It is split into five chapters: **Revision** which provides underpinning knowledge required for those who need to design filters. **Filter Basics** which is a course in terminology and filter characterization, important classes of filter, filter order, filter impedance and impedance matching, and effects of different filter types. **Advanced Theory** which covers the use of filter tables, mathematics behind filter design, and an explanation of the design of active filters. **Passive Filter Design** which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, band-pass, and band-stop Bessel, Butterworth and Chebyshev ladder filters. **Active Filter Design** which includes an expert system and filter synthesis tool for the design of low-pass, high-pass, and-pass, and band-stop Bessel, Butterworth and Chebyshev op.amp filters.

PRICES Prices for each of the CD-ROMs above are: (Order form on third page)

Hobbyist/Student	£45 inc VAT
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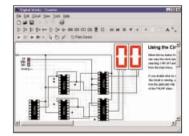
#### ELECTRONICS CAD PACK



#### PCB Layout

Electronics CADPACK allows users to design complex circuit schematics, to view circuit animations using a unique SPICEbased simulation tool, and to design printed circuit boards. CADPACK is made up of three separate software modules. (These are restricted versions of the full Labcenter software.) **ISIS Lite** which provides full schematic drawing features including full control of drawing appearance, automatic wire routing, and over 6,000 parts. **PROSPICE Lite** (integrated into ISIS Lite) which uses unique animation to show the operation of any circuit with mouse-operated switches, pots. etc. The animation is compiled using a full mixed mode SPICE simulator. ARES Lite PCB layout software allows professional quality PCBs to be designed and includes advanced features such as 16-layer boards, SMT components, and an autorouter operating on user generated Net Lists

#### DIGITAL WORKS 3.0



Counter project

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## PICmicro TUTORIALS AND PROGRAMMING

#### VERSION 2 PICmicro MCU DEVELOPMENT BOARD Suitable for use with the three software packages listed below.

This flexible development board allows students to learn both how to program PICmicro microcontrollers as well as program a range of 8, 18, 28 and 40-pin devices. For experienced programmers all programming software is included in the PPP utility that comes with the development board. For those who want to learn, choose one or all of the packages below to use with the Development Board.

- Makes it easier to develop PICmicro projects
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#### SOFTWARE

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#### ASSEMBLY FOR PICmicro V2 (Formerly PICtutor)

Assembly for PICmicro microcontrollers V2.0 (previously known as PICtutor) by John Becker contains a complete course in programming the PIC16F84 PICmicro microcontroller from Arizona Microchip. It starts with fundamental concepts and extends up to complex programs including watchdog timers, interrupts and sleep modes. The CD makes use of the latest simulation techniques which provide a superb tool for learning: the Virtual PICmicro microcontroller. This is a simulation tool that allows users to write and execute MPASM assembler code for the PIC16F84 microcontroller on-screen. Using this you can actually see what happens inside the PICmicro MCU as each instruction is executed which enhances understanding.

Comprehensive instruction through 39 tutorial sections ● Includes Vlab, a Virtual PICmicro microcontroller: a fully functioning simulator ● Tests, exercises and projects covering a wide range of PICmicro MCU applications ● Includes MPLAB assembler ● Visual representation of a PICmicro showing architecture and functions ● Expert system for code entry helps first time users ● Shows data flow and fetch execute cycle and has challenges (washing machine, lift, crossroads etc.) ● Imports MPASM files.



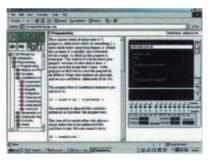
Virtual PICmicro

#### **'C' FOR PICmicro**

The C for PICmicro microcontrollers CD-ROM is designed for students and professionals who need to learn how to program embedded microcontrollers in C. The CD contains a course as well as all the software tools needed to create Hex code for a wide range of PICmicro devices – including a full C compiler for a wide range of PICmicro devices.

Although the course focuses on the use of the PICmicro microcontrollers, this CD-ROM will provide a good grounding in C programming for any microcontroller.

Complete course in C as well as C programming for PICmicro microcontrollers
 Highly interactive course
 Virtual C PICmicro improves understanding
 Includes a C compiler for a wide range of PICmicro devices
 Includes full Integrated Development Environment
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 Compatible with most PICmicro programmers
 Includes a vide range of PICmicro devices.



Minimum system requirements for these items: Pentium PC running Windows 95, 98, NT, 2000, ME, XP; CD-ROM drive; 64MB RAM; 10MB hard disk space.

#### FLOWCODE FOR PICmicro

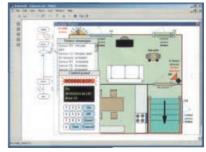
NEW

Flowcode is a very high level language programming system for PICmicro microcontrollers based on flowcharts. Flowcode allows you to design and simulate complex robotics and control systems in a matter of minutes.

Flowcode is a powerful language that uses macros to facilitate the control of complex devices like 7-segment displays, motor controllers and l.c.d. displays. The use of macros allows you to control these electronic devices without getting bogged down in understanding the programming involved.

Flowcode produces MPASM code which is compatible with virtually all PICmicro programmers. When used in conjunction with the Version 2 development board this provides a seamless solution that allows you to program chips in minutes.

Requires no programming experience
 Allows complex PICmicro applications to be designed quickly
 Uses international standard flow chart symbols (ISO5807)
 Full on-screen simulation allows debugging and speeds up the development process
 Facilitates learning via a full suite of demonstration tutorials and virtual systems (burglar alarms, car parks etc.)
 Produces ASM code for a range of 8, 18, 28 and 40-pin devices.

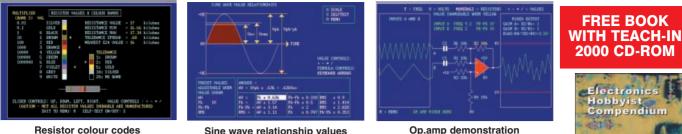


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## TEACH-IN 2000 – LEARN ELECTRONICS WITH EPE



Sine wave relationship values

EPEs own Teach-In CD-ROM, contains the full 12-part Teach-In series by John Becker in PDF form plus the Teach-In interactive software covering all aspects of the series. We have also added Alan Winstanley's highly acclaimed Basic Soldering Guide which is fully illustrated and which also includes Desoldering. The Teach-In series covers: Colour Codes and Resistors, Capacitors, Potentiometers, Sensor Resistors, Ohm's Law, Diodes and L.E.D.s, Waveforms, Frequency and Time, Logic Gates, Binary and Hex Logic, Op.amps, Comparators, Mixers, Audio and Sensor Amplifiers, Transistors, Transformers and Rectifiers, Voltage Regulation, Integration, Differentiation, 7-segment Displays, L.C.D.s, Digital-to-Analogue. Each part has an associated practical section and the series includes a simple PC interface so you can use your PC as a basic oscilloscope with the various circuits.

A hands-on approach to electronics with numerous breadboard circuits to try out.

£12.45 including VAT and postage. Requires Adobe Acrobat (available free from the Internet - www.adobe.com/acrobat). FREE WITH EACH TEACH-IN CD-ROM - Electronics Hobbyist Compendium 80-page book by Robert Penfold. Covers Tools For The Job; Component Testing; Oscilloscope Basics.

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#### **ELECTRONIC COMPONENTS PHOTOS**

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A high quality selection of over 200 JPG images of electronic components. This selection of high resolution photos can be used to enhance projects and presentations or to help with training and

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Minimum system requirements for these CD-ROMs: Pentium PC, CD-ROM drive, 32MB RAM, 10MB hard disk space. Windows 95/98/NT/2000/ME/XP, mouse, sound card, web browser.

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# SURFING THE INTERNET



Memail: "I've just read Net Work in the January issue and my heart goes out to you. It made me feel lucky. I sit here on the other side of the world, tucked away down under in Sydney, Australia getting your magazine and lots more via my way-cool (fast!) cable internet connection. It's connected 24×7 and is very reliable. Downloading a 2-CD Linux distribution takes about 10-15 minutes. The connection comes into a cheap router then a hub and is shared around the house via a wireless (802·11b) 11Mbps LAN... Alan, I hope things get better for you soon."

I welcome your sympathy, Greg, but at last there are signs of improvement in the UK. For many Internet users, dial-up 56k access is a serious bottleneck but it is the only feasible way of connecting to the Internet. From the end users' perspective, British Telecom (BT) has not exactly leaned over backwards to deliver broadband services to the masses, at least until now. They would rather you rented a second line for your modem, or install more expensive ISDN. This month's *Net Work* looks at the options starting to open up to Internet users in the UK as the connectivity "temperature" starts to rise quite dramatically.

#### Alternatives

What alternatives do you have if your 56k modem is too slow? **ISDN** is marginally faster and connects rapidly, but in practice it can prove more expensive, especially if a high number of shorter calls are made (remember BT's minimum 5p per call). To obtain 128k means placing two ISDN calls at the same time, which costs twice as much. I don't know anyone who relishes the cost or speed of ISDN.

Next up is **cable access**; if your region has been cabled up then you could buy a cable modem for your PC. Some cable TV users find that they have had a cable modem installed all the time; they just never knew what it actually was or how to connect it! Obviously, unless the cable network has been laid in your locality by e.g. Telewest or NTL, then this option is ruled out. As regular readers will know, in my case cable was laid approximately four years ago but it has yet to be switched on.

**Satellite** access is the next option, which offers roughly 400k download and 40k to 140k upload, but the cost of installing and running an unwieldy USB satellite modem (up to £1,000) will rule out this option for many. Both BT and Tiscali are testing satellite systems, and they will be perfect for those living in rural areas. A cheaper satellite option will involve using a satellite dish to download from the Internet while uploading is performed via a phone line.

**Wireless** access is another technology that is undergoing limited trials. In the future there will be BFWA – Broadband Fixed Wireless Access – operating in the 28GHz and 40GHz bands.

Last up is Asymmetric Digital Subscriber Line or **ADSL** which operates through your BT line. This offers up to 512kbps download or more if you can afford it. One of BT's problems has been to assess where to start the ADSL network upgrade to begin with. Some exchanges were upgraded by adding scores of ADSL ports but the uptake from the customer base was almost zero, so it has been very hard to plan the rollout. One major hurdle with ADSL is that its performance depends on how far away you are from the exchange in terms of "copper-wire miles". The rule is about three kilometres, but a new rate-adaptive system will extend this to say five kilometres of copper wire if your line passes the tests.

A future problem will be *contention ratios* – with typically 20 users sharing a 512kb ADSL connection for business tariffs, rising

to 50:1 on residential rates, you only need a few users setting up a live videoconferencing feed for example, and the rate of data throughput is likely to plunge. I expect I'll be writing about this problem in five or ten years' time.

#### The Need for Speed

In February 2002 some ISPs including ClaraNet and Pipex unexpectedly cut the cost of home ADSL services, and for heavier users ADSL suddenly started to appear a whole lot more feasible. Pipex were amongst the first to break loose and offer a service for home users priced at £24.99 (\$35) per month, and self-install products also came along.

It seems like the handbrake was released when wholesale broadband rates were cut by BT. This encouraged ISP's to price their ADSL services more realistically. There is hope yet, but there is still one fundamental problem, namely that BT owns the copper wires – the local loop – that connect into your home: a BT line is still necessary, so we all depend upon BT upgrading the exchanges for ADSL to begin with. Presently about 1,000 exchanges have been converted, having a catchment area of 63 per cent of homes.

In order to learn whether your postcode and phone number fall within an ADSL-enabled area, use the Fast Track Checker on BT's web site **www.broadband1.bt.com/home/home.asp** (note the digit "one" after broadband) after which you must still find an ISP who will offer you an ADSL tariff.

#### Estuary English

At a recent broadband seminar in Hull, BT was unable to show me what ADSL looked like or how fast it went because the telco. for the entire Hull area is Kingston Communications, not BT. The former city-owned company has managed to deliver broadband access into 110,000 homes around the city. For this reason the region has been dubbed the "silicon estuary" as the Humber estuary runs close by.

Kingston also claims to run the world's largest video server offering video on demand over its entire network, and some of the applications they are toying with are eye-watering, including security and CCTV delivered by broadband with the future potential for face recognition too. Other applications include online gaming and mobile delivery.

So broadband is finally creeping towards the centre stage. The paradox now is that actually, many users have been conditioned to 56k dialup and don't know that much better things are possible, so they can't see the need for faster speeds. The pressure is now on to develop attractive broadband content alongside the networks that carry it. In time you will be able to download music, monitor your remote security cameras, control your IP-enabled appliances – the central heating, the fridge and more – fetch massive software packages ten times more quickly, watch streaming videos, and record live TV to a hard disk and watch it from a remote system. And of course, broadband means that you are always connected to the Internet so you could finally run a webcam or videoconferencing, or use Voice Over IP to talk over a network to anyone anywhere in the world. Broadband will do this and a whole lot more.

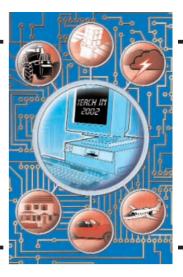
Just under a decade ago my "information superhighway" consisted of me unwinding a telephone extension reel and plugging it into a phone socket next door, then trying to access the Internet on a treacherous phone line via a 14.4kbps modem inside a sprightly 486 PC. Now there is the prospect of TV, video, music, voice, security, real time gaming, powerful remote control and mobile Internet all delivered over a broadband network. The future contents of *EPE* may well include projects that utilise broadband communications at their heart. Interesting and exciting times are ahead! You can email me at **alan@epemag.co.uk** 

## EPE Tutorial Series -

# **TEACH-IN 2002**

Part Eight – Filters, Actuators, Smoke and Gas Detection

## IAN BELL AND DAVE CHESMORE



#### Making Sense of the Real World: Electronics to Measure the Environment

Most of the sensing we have discussed so far in this series has been passive measurement of one parameter or another. However, we are not restricted to this approach; we may if we wish send a signal out into the environment and monitor the response with a sensor. We can call this a **sensor-actuator** combination (an actuator is something that does something or sends out a stimulus).

Our hands and feet are actuators and eyes and ears are sensors, but we can also use a combination approach. For example, we can "measure" the properties of a material by squeezing it and feeling and watching the response.

This month we will be briefly examining the idea of sensor-actuator combinations – we could have written an entire series on this topic so we only have space to look at it briefly. We will also be investigating sensors for smoke and gas, though we examine filters first.

#### **FILTERS**

Electronic filters are circuits that pass signals at certain frequencies (in the **passband**) while rejecting signals at other frequencies (in the **stop-band**). The frequency that divides the pass-band from the stopband is a **cut-off frequency**.

Filters constructed from just resistors, capacitors and inductors are called **passive filters**, whereas filters that employ devices such as transistors or op.amps are called **active filters**.

We can make very good passive filters, but inductors are often bulky and expensive. They are also limited by non-ideal characteristics such as series resistance, and are susceptible to magnetic pickup of interference.

Filters using just resistors and capacitors cannot be used to make high performance filters due to their "soft" response and the high attenuation of the signal they cause. However, we do not always need high performance filters; a single resistor and capacitor filter occurs in many circuits. We have used this in many *Lab Work* circuits in previous parts of *Teach-in 2002*.

Filters can be classified according to the pass-band:

- Low-pass filters let low frequencies through
- **High-pass** filters let high frequencies through
- **Band-pass** filters let a specific range of frequencies through
- Band-stop filters reject a specific range of frequencies

As we will see next month, low-pass filters are of particular importance when we want to convert analogue sensor data into digital for computer storage or analysis. **Bandpass** filters are needed for the technique described above where we measure the response to stimulus at a particular frequency. A **notch filter** is a band-stop filter with a very narrow stop-band, which can be useful where our sensor signal is subjected to interference at specific frequencies (such as mains 50Hz/60Hz).

#### FILTER CHARACTERISTICS

The graph of gain (in dB) against frequency (on a logarithmic scale) is called the **frequency response** of the filter, an example of which is shown Fig.8.1. For an ideal filter the transition from pass-band to stop-band occurs at a single frequency. For real filters (see Fig.8.1) the transition from pass-band to stop-band occurs over a range of frequencies, thus the we need to define specifically what we mean by *cut-off frequency*.

The cut-off is usually defined to be the point where the filter's gain is -3dB with respect to the pass-band gain. Other definitions could be used, particularly for responses where the pass-band gain is not flat.

The stop-band may also be specifically defined in terms of reduction in gain, although there is not a "standard" gain reduction for stop-band as there is with the -3dB point for cut-off. The range of frequencies between the pass- and stop-bands is the transition region.

If the pass-band gain does not vary much with frequency, it is described as **flat**. In some filters the pass-band gain has distinctive ripples as frequency varies, the depth of these ripples is usually measured in decibels. The stop-band may also have ripples.

In sensor applications where the frequency of the sensed signal varies and signal magnitude is of importance, a filter without a flat pass-band may lead to measurement errors.

The slope of the frequency response in the transition region, and possibly the stop-band, indicates how quickly the filter's gain drops as the frequency moves away from the cut-off. The slope is measured in dB per octave, or dB per decade; this value is called the **fall-off** or **roll-off**.

The fall-off may be different near and far from the cut-off, thus we have *initial* falloff and *ultimate* fall-off. Note that an octave is a range of frequencies in which the higher frequency is twice the lower (the same term is used in music). A decade is a range in which the upper value is ten times the lower.

The **order** of a filter determines the ultimate fall-off and can be calculated as 6ndB/octave (or 20ndB/decade), where *n* is the filter order. For a first order filter this is 6dB/octave; for a second order filter it is 12dB/octave, and so on.

The variation of **phase shift** with frequency (the phase response) is also an important characteristic of filters. Phase shift relates to the time delay of signals passing through the filter. If the delay is different at different frequencies the signal will be distorted. Constant delay

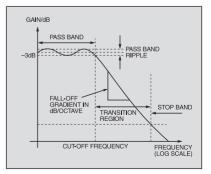


Fig.8.1. Describing a filter's frequency response.

corresponds with a linear increase of phase shift with frequency. The terms constantdelay, or linear-phase are used to refer to filters that are ideal or have very good performance in this respect.

The time domain response of a filter can be obtained by applying a step change to the input (e.g. a sudden change from 0V to some other voltage). The response may have a number of features which are illustrated in Fig.8.2. The terms used can be defined as:

- **Rise time** is the time to get from 10 per cent to 90 per cent of the final value.
- **Overshoot** is the percentage of maximum value over the final value.
- **Ringing** is the decaying oscillation that may occur as the output settles to its final value.
- Settling time is the time the output takes to get within certain small percentage of final value. Settling time may be of importance in high speed sensor applications as we may not be able to get an accurate reading until the circuit has settled.

#### FILTER TYPES

Filter design is a compromise between requirements such as pass-band flatness, sharpness of cut-off, delay flatness (phase linearity), rise time, overshoot, etc. A number of well-known filter types provide different properties. Butterworth filters have a very flat gain response in the passband. Chebyshev filters have a very steep transition from pass-band to stop-band but have ripples (or a resonant peak) in the pass-band gain. Bessel filters have very good phase linearity.

There are many circuit configurations for active filter circuits, with variants for high-pass, low-pass, band-pass etc. We certainly do not have space to look at all of them in detail here! As an example, we have chosen an equal component Sallen and Key second order low-pass filter (see Fig.8.3), which can be set up to provide a variety of types of response as shown in Fig.8.4. Here you can see the flat, but relatively steep, response of the Butterworth filter, and resonant peaks in the Chebyshev responses.

The Sallen and Key part of the name comes from names of the engineers who first described it and the "equal component" bit refers to the fact that the two frequency selection capacitors and resistors have the same value (labelled R and C on the schematic).

The high-pass version of the circuit is obtained by swapping the locations of R

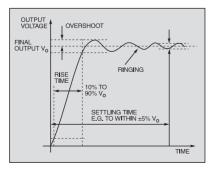


Fig.8.2. Filter time domain response.

and C. Sallen and Key filters are examples of a more general class of filters called single amplifier biquadratic (SAB) filters. Biquadratic (*biquad* for short) is a term relating to the mathematics behind the filter characteristics.



To complete the design of a filter, first select your cut-off frequency,  $f_C$ . Then choose the type of filter you want (Bessel, Butterworth etc., depending on the response shape required) based on the characteristics of each type.

Using a table, such as that in Table 8.1, establish the **damping** factor  $\zeta$  (Greek letter xi) and the resonant frequency  $f_0$ . The damping factor in this context states the filter's ability to respond without ringing occurring. Tables of filter parameter values are published in cookbooks on filter design.

You are now ready to find the actual component values. Select the values of R and C to give the required resonant frequency  $f_0$ . Resistor values of around  $10k\Omega$  are appropriate, but higher values (e.g.  $100k\Omega$ ) may be better for low cut-off frequencies as the size of the capacitors is reduced. Use:

$$f_0 = \frac{1}{2\pi RC}$$
 so  $C = \frac{1}{2\pi Rf_0}$ 

So, for example, if we want a Butterworth filter with a cut-off frequency of 1kHz (and the same resonant frequency) and we decide to use  $10k\Omega$  resistors, we need capacitors of value  $0.016\mu$ F.

The value of  $\zeta$  sets the gain required from the amplifier and hence the values of the op.amp feedback resistors. These values can be set completely independently of the frequency component values, but the best value for  $R_A$  is one that gives about the same resistance seen at the inverting and non-inverting inputs.

This happens when the parallel combination of the gain resistors equals the series

combination of the frequency resistors. From which we get the best value for  $R_A$  as

$$R_A = \frac{(3-2\zeta)}{(1-\zeta)} R$$

Hence, for example, if the frequencysetting resistors are both  $10k\Omega$  and we

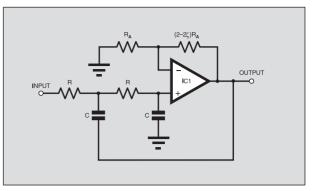


Fig.8.3. Equal component Sallen and Key second order lowpass filter circuit.

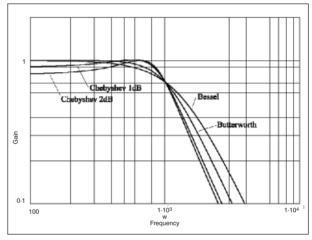


Fig.8.4. Typical responses from a second order Sallen and Key low-pass filter.

want  $\zeta = 0.707$ , we get the best value for  $R_A$  as about 54k $\Omega$ . Calculate the other resistor value using  $(2 - 2\zeta)R_A$  (e.g.  $32k\Omega$  for  $\zeta = 0.707$  and  $R_A = 54k\Omega$ ). If the "best value" for  $R_A$  is not close to

If the "best value" for  $R_A$  is not close to a preferred value is does not matter if you change it a bit, but given your value for  $R_A$ the other gain resistor must be as close as possible to  $(2 - 2\zeta)R_A$  in order to get the right filter characteristic.

#### FILTER DESIGN GUIDELINES

The gain due to op.amp IC1 (Fig.8.3) is set to  $(3 - 2\zeta)$  by the damping factor,  $\zeta$ . If you need a specific gain, say G, add an ordinary op.amp amplifier with gain  $G/(3 - 2\zeta)$ . This could be at the input to provide the d.c. bias path for the filter and act as a buffer for the input signal (see later).

The filter gain should not usually be much greater than 2, above this we get a very high Q but the circuit response becomes very sensitive to component values.

0.852 f

Table 8.1 – Parameters for 2nd order low-pass Sallen and Key filter.  $f_c$  is –3dB (cutoff) frequency. The frequency responses of these filters are shown in Fig.8.4.

Chebvshev

R	Characteristic	Name	Resonant Frequency (f <sub>0</sub> )
ample,	Best Delay	Bessel	1.274 f
iency-	Flattest Pass-band	Butterworth	1.000 f <sub>c</sub>
s are	1dB Ripple	Chebyshev	0∙863 f <sub>c</sub>

2dB Ripple

Damping

(d or 2ζ)

0.866

0.707

0.523

0.448

The input to the filter must have a reasonably low impedance d.c. path to ground (i.e. it should *not* be directly capacitively coupled to the input signal). This is in order to make sure that the op.amp is provided with bias current. If your signals are large, make sure that the input signal will not saturate the op.amp.

If you want your filter to deal with relatively high frequency (tens to hundreds of kilohertz) or large magnitude (several volts) then there is an op.amp parameter you need to take note of.

Slew rate specifies the maximum rate of change of output voltage for an op.amp. The higher the frequency and the larger the signal magnitude, the faster the op.amp output has to change to "keep up with" the required output signal. If this speed exceeds the op.amp's slew rate the op.amp will fail too keep up, resulting in distortion of the signal.

If the required peak output voltage is  $V_M$  and the slew rate is *s* (in volts per second, from the op.amp's datasheet) then the maximum frequency sinewave that can be output without distortion is:

$$f = \frac{s}{2\pi V_{\rm m}}$$

For example, for a slew rate of  $2V/\mu s$ and  $V_M = 15V$ , the maximum frequency is 21kHz, not a particularly high one. The slew rate of the OP177 op.amp is quite low at 0.3V/ $\mu s$  (the 741 is 0.5V/ $\mu s$ ). This is because the device is optimized for very accurate processing of relatively low level and low frequency signals.

Far faster op.amps are available if you need them; in fact, we need to use an LF351  $(13V/\mu s)$  to amplify the output of the sinewave generator in Lab 8.1 because it has a reasonable gain. You could easily see the effect of slew rate by exchanging the LF351 in Lab 8.1 for an OP177 and varying the frequency of the sinewave.

#### COMPUTER AIDED DESIGN

There are a number of low cost and even free software packages available for the design of filters. These remove the need for laborious calculations and searching of filter parameter tables, and of course it is very quick if you want to change one or two parameters.

With such software packages you often have the further advantage of getting the schematics and responses drawn for you. A screenshot from a free package called Filter Free from Nuhertz Technologies (www.nuhertz.com) is shown in Fig. 8.5.

The filter schematic and response curves resulting from the design parameters in Fig.8.5 are shown in Figs.8.6 and 8.7. Note that this circuit is a single amplifier biquad and is in fact a Sallen and Key filter, but without the simplifying "equal component" feature which our previous circuit had in order to make manual design easier.

#### SWITCHED CAPACITOR FILTER

It is fairly obvious that the circuit becomes quite complex if we need high order filters (one op.amp and at least four components for each second order filter).

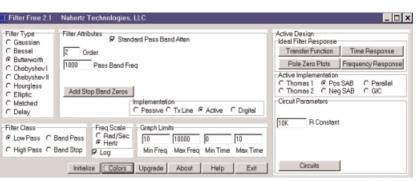


Fig.8.5. Example filter design software.

There is, however, an alternative solution – a **switched capacitor filter**. A switched capacitor filter uses analogue switches to rapidly switch between two different capacitors – controlling the duty cycle changes the effective capacitance.

Such devices allow fine control of capacitor values and can be manufactured on silicon. There are many integrated circuit filters from Linear Technology, Maxim, etc. available as low-pass, highpass, band-pass filters, even Butterworth, Chebyshev, etc. They have many advantages, including small size, programmability and ease of use. However, they suffer from one problem – their need for clocking to control the capacitor switching. The clock, often 100 times the cut-off frequency, can appear at the output of the filter, albeit at low levels, and can interfere with low level signals.

The device we will be using in Lab 8.1 is an LTC1062 from Linear Technology which is a 5th order maximally flat (Butterworth) low-pass filter with an internal clock. The ratio of clock to cut-off frequency is 100:1 and can be generated externally from an oscillator or even a microcontroller. This means we can vary the cut-off frequency. The i.c. itself is 4th order and an additional *RC* section is added to provide the 5th order. A full datasheet can be found at www. **linear.com/pdf/lt1062.pdf**.

#### SENSORS AND ACTUATORS

In electronic sensing the sensor-actuator combination is often used to make it easier to ignore interference signals – we know the properties of the signal we sent out so we can ignore irrelevant parts of the sensor response. The most obvious example is to use a stimulus at a particular frequency and

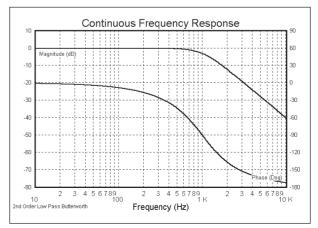


Fig.8.7. Frequency response of the circuit in Fig.8.6.

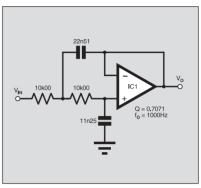


Fig.8.6. Single amplifier biquad Sallen and Key filter.

only measure responses from the sensor at that frequency. In order to extract the frequency of interest we need filter circuits, which, as you have seen, is one of our topics this month.

Next month we see that filters are also very important when we want to digitize the signals from sensors for processing by a microcontroller or computer.

Sensor actuator combinations are essential for some measurement processes. For example, we may need to apply heat or electrical signals to a substance for chemical sensing. Indeed, many gas sensors need to be heated to around 350°C and this is best achieved if the heating controller is actively controlled so that any deviations from the optimum temperature are counteracted.

The simplest way of achieving this is to feed back the difference between the sensor's actual temperature and its desired temperature in such a way as to reduce the difference to zero. Any changes in sensor

temperature are detected and the optimum temperature is reached.

A simpler and more familiar feedback circuit is in controlling the temperature of a room where the heater is turned on until the desired temperature is reached, when it is turned off. This is a simple on-off control and is suitable for many applications.

We often need more accurate control where, as above, the difference is fed back and the output is a function of the difference. So, in the heater example, we would vary the amount of power to the heater depending on the difference – the larger the difference, the more power applied. Feedback control is found just about everywhere, from controlling the speed of an engine, to an autofocus on a camera.

#### SMOKE DETECTORS

Everyone is familiar with smoke detectors, but how do they work? There are actually two basic types – **photoelectric** and **ionization** detectors.

The most common form of photoelectric smoke detector relies on the detection of light scattered from smoke particles. The principle of light scattering is also used in measuring the turbidity (cloudiness) of water, an important factor in many aquatic applications. Such an instrument is called a **Nephelometer** and is widely used in the water industry.

The way in which light is scattered by small particles is quite complex and involves the size of the particle and the wavelength of light (remember the equation relating wavelength, frequency and speed,  $\lambda = fc^{-1}$  where *f* is the frequency and *c* the speed of light,  $-3 \times 10^8$ ms<sup>-1</sup>).

Basically, the shorter the wavelength in comparison to the size of the particle, the more the light is scattered. In fact, if the particle size is smaller than the wavelength, the amount of scattering increases by the fourth power of the wavelength, and short wavelengths are scattered much more than longer wavelengths.

This explains why the sky is blue and the setting sun is red – blue light is scattered from air molecules much more than red light and it is scattered sideways so the sky looks blue. At sunset, the light from the sun has to pass through more atmosphere and the blue is scattered even more, increasing its apparent red content. If you look carefully at the sky when the sun is on the horizon, you will actually see all the colours ranging from red to violet as you look directly at the sun.)

At the opposite extreme, when the particles are much larger than the wavelength of light, all wavelengths are scattered equally. This is why thin clouds appear to be white. When a cloud becomes very thick, the light is attenuated and it becomes grey.

#### PRACTICAL TEST

One of the best ways of illustrating the idea of light scattering is to use a torch and two glasses of water. Put a couple of drops of milk into one glass of water. If you look through the glass at right angles to the torch beam, you will not be able to see the beam in the glass only containing water, but will see it in the water-milk mixture. Try increasing the number of drops of milk and see the effect.

In addition to the effect of particle size on scattering, the chemical nature of particles also changes the amount of scatter. This has application in monitoring of gases emitted from chimneys, which can be carried out at a distance using a telescope and laser, as indicated in Fig.8.8.

The laser illuminates the pollution cloud and light scattered back from particles is detected by sensitive detectors in the telescope. The chemical makeup of the cloud can be found out from the received signal. It is also possible to plot the pollution in the cloud by moving the telescope

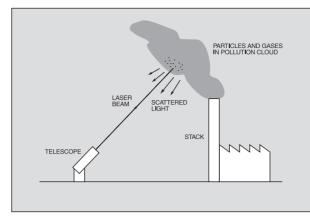


Fig.8.8. Principle of a LIDAR system.

and measuring the time taken for each laser pulse to return (the longer the time, the further away the scattering particles). This system is known as a **LIDAR**, which stands for Laser Radar.

#### PHOTOELECTRIC SMOKE DETECTORS

The principle of operation of a photoelectric smoke detector is simple, as shown in Fig.8.9 where a beam of light (usually infra-red – IR) is passed along a tube and a photodiode is placed at right angles to the beam. Under normal conditions, the photodiode will not pick up any light, but if smoke is present, the light is scattered by the smoke particles and the photodiode will detect the light.

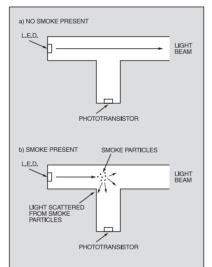


Fig.8.9. Photoelectric smoke detector.

Photoelectric smoke detectors are commercially available but are not as common as the ionization chamber devices. We discuss the building a simple photoelectric smoke detector in this month's *Lab Work*.

As said earlier, smoke is detected by light being scattered. Detecting small changes in light can be difficult, especially if there is a lot of ambient light around. We can reduce the ambient light in two ways – placing the whole detector in a dark box or using IR light. The problem with a dark box is that we need to allow smoke to enter but not light!

To use IR is the best option since IR detectors are usually encased in a black

package which is opaque to visible light but transparent to IR light. Even so, we need to reduce the overall light to a minimum because there is usually quite a lot of ambient IR light around.

In the Lab, we will be building a sensor from discrete components and some digital circuits. If we wished, we could have used commercially available integrated circuits which contain all circuitry including

piezo-buzzer drivers. Examples are the Motorola MC145010 and Allegro A5366 (both functionally identical). All they need are a few resistors and capacitors, and a IR l.e.d. and photosensor. They are also capable of being interconnected (e.g. 40 on a common signalling bus).

#### IONIZATION CHAMBER DETECTORS

Ionization chamber detectors are very different from photoelectric detectors and they rely on detecting changes in a current caused by a radiation source. The schematic diagram of a typical ionizing chamber is shown in Fig.8.10, consisting of a radioactive source and two plates across which is a voltage.

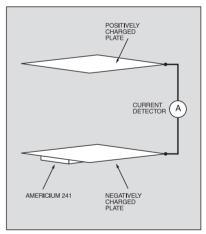


Fig.8.10. Ionization chamber.

The principle of operation is as follows. The radioactive source (a very small quantity of Americium-241) produces alpha particles which are helium nuclei. These particles ionize air molecules within the chamber by knocking electrons of the atoms, causing them to have a positive charge, the free electrons being attracted to the positive plate and the ions to the negative plate. A current is therefore created which is amplified by electronics. When smoke particles enter the chamber the ion current is disrupted and the electronics detect the drop in current.

Ionization chamber detectors are more sensitive than photoelectric detectors and are completely safe. The amount of radioactive material is very small, typically 200µg and even a piece of paper will completely absorb alpha particles. The only thing you should



#### Table 8.2 Some Gas Sensors from Capteur and Figaro

Gas	Concentration Range (Capteur)	Concentration Range (Figaro)	
Ammonia	0-100ppm	30-300ppm	
Carbon Monoxide	0-400ppm	50-1,000ppm	
Hydrogen	0-10,000ppm	50-1,000ppm	
LPG	0-1%	500-10,000ppm	
Methane	0-1%	500-10,000ppm	
Ozone	0-300ppb	_	
Propane	0-1%	500-10,000ppm	
VOCs (volatile			
organic compound	s) 0-10ppm	_	
Oxygen	-	0-100%	
ppb = parts per billion $(10^8)$ ppm = parts per million $(10^6)$			

Left: Example of a smoke detector for domestic use.

not do is disturb the Americium and cause it to become airborne.

#### GAS SENSORS

Gas detection and monitoring has taken on an ever-increasing importance due to the awareness of the damaging effects that some volatile gaseous compounds have on the environment and our health. There are many gas sensors available and Capteur provides a good range. Table 8.2 gives examples of some sensors and their sensitivity. Full details can be found on the Capteur website **www.capteur.co.uk**.

Another company, Figaro Inc., has been in the business for more than 30 years and details of their sensors can be found at **www.figarosensor.com**. Sensor cost is quite high, from £10 to over £25 depending on the gas to be detected. The sensor we will be using next month is about £10 and is capable of detecting a range of gases, as we will see later.

The range of gases for which sensors are available is very large, ranging from combustible gases such as methane, propane and hydrogen through toxic gases (chlorine, carbon monoxide) to organic solvents such as alcohol, toluene and xylene.

How do they work? There are several types depending on the material used. The simplest types utilizing a special tin dioxide semiconductor coated on a ceramic tube. The conductivity of the tube varies in response to its exposure to a wide range of detectable gases.

These sensors need to be heated to operate and they incorporate an internal heating element which must be maintained at  $350^{\circ}$ C or so, depending on the sensor. The most common circuit used is a bridge where the sensor forms one arm of the bridge. The other arm of the bridge is either an accurate resistor or a compensator. In fact, the sensor we will be using next month comprises two separate devices – a sensor and a compensator.

#### USING GAS SENSORS

Many gases are heavier than air so the sensors are best placed low down when general monitoring is being undertaken;



Example of a gas sensor from Figaro.

different sensors can be selected for an improved response to particular gases. The ability to respond to carbon monoxide also means that some sensors will respond well to smoke, acting as a smoke or fire alarm.

One disadvantage is that the heater element draws a high current, e.g. the Figaro TGS813 needs typically 160mA or more at 5V, and they are also easily damaged by the presence of some chemicals, including silicone and salts (boat owners take note).

Sensors incorporate a fine stainless steel mesh which acts as a flame arrestor. You are probably aware of the principle of intrinsic safety by which a gas detector system is designed in such a way that it cannot create a spark or ignition hazard anywhere, or cause an explosion when a flammable gas is detected.

Ignition sparks can be caused in many different ways, including the arcing of electrical switches or relay contacts, faulty insulation, loose plugs and sockets, the operation of electric buzzers or even static electricity discharge from nylon clothing. Sources of ignition also include light fittings (fluorescent and incandescent) and electric heaters.

For this reason, if ever you detect a gas leak, you should never turn any electric lights on or off (including torches/flashlights) or operate any electrical item, because any sparks may cause an explosion. Likewise, mobile phones are banned from use at petrol filling stations in case the high frequency transmissions induce sparks in electrical equipment and cause a fire in any pockets of gas.

## **TEACH-IN 2002 – Lab Work 8** DAVE CHESMORE

#### FILTER AND SMOKE DETECTOR EXPERIMENTS

#### Lab 8.1 Signal Generator

In this Lab we build a sinewave generator so that we can test the filter circuits in Labs.8.2 to 8.5 and to illustrate aliasing which will be described in Part 9. The circuit in Fig.8.11 is based on an Intersil ICL8038 waveform generator (IC1). This device is easy to use and can generate sine, square and triangular waves between 0.001Hz and 300kHz. A full descriptive datasheet for the 8038 can be downloaded from www.intersil.com/data/FN/FN2/FN2864/FN2864.pdf.

You could alternatively use a Maxim MAX038 instead as it is pin-compatible with the 8038 but operates up to 20MHz.

However, here we are only interested in audio frequencies and the circuit has been designed to operate up to 20kHz.

Op.amp IC2 provides gain and output buffering since the sinewave output has a relatively high impedance. It is connected to the 8038 via potentiometer VR3 which allows the output amplitude to be varied.

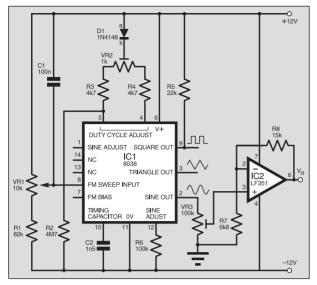


Fig.8.11. Signal generator circuit.

Notice that IC2 is a type LF351 – this is a wide bandwidth op.amp (high slew rate).

Later, if you want to see why this device is recommended, try replacing it with an OP177 or 741 and increase the frequency to 10kHz or more. The op.amp's output will become a triangular wave instead of a sinusoidal one, due to the low slew rate of the amplifier (see Tutorial section).

#### ASSEMBLY

Build the circuit of Fig.8.11 on breadboard or, alternatively on stripboard if you wish to make a more permanent circuit (see photograph). There are three potentiometers, which control frequency (VR1), duty cycle (VR2) and output amplitude (VR3).

Once the circuit is built, set VR1 and VR3 to halfway (VR3 in our circuit is a preset). Connect the Picoscope and check to see that the waveform looks like that in Fig.8.12. You will notice that it doesn't actually look particularly sinusoidal because of the steps – we will explain this next month.

The shape can be altered with VR2 – adjust it until the shape is as close to a sinusoid as possible. The next stage is to calibrate VR1, at least at the low frequency end by measuring the frequency with the Picoscope and noting the wiper position. Be aware that at very low frequencies, the output becomes non-sinusoidal as the positive peaks become flattened.

Whilst all we need is the sinewave output, the circuit is versatile and you could add a three way switch to allow selection of sine, triangle or square wave and change VR3 to a panel mounting potentiometer to give easier control of the output amplitude.

#### Lab 8.2 Low-pass Filters

In Fig.8.13 is shown the circuit for a Butterworth second order low-pass filter based on the Sallen and Key filter described in this month's tutorial section. The cut-off frequency is set at approximately 1kHz. Build the circuit and test it using the signal generator from Lab 8.1 and the Picoscope to measure the output signal.

Set the signal generator's output amplitude to about 6V peak-to-peak. The filtered Stripboard assembly for the signal generator circuit in Fig.8.11.

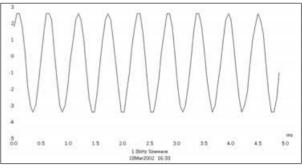


Fig.8.12. Sinewave output of the signal generator at 1.8kHz, as viewed on the Picoscope

CUIV	IPONENTS	Guidance Only	excl. har
	1 Signal Generator		4 Switching Filter
Resistors R1	82k See	Resistors B1	56k
R2			22k
R3, R4	4M7 4k7 (2 off) SHOP	R3	680k
R5	22k TALK	R4, R5	27k (2 off)
R6 R7	100k Page	All 0·25W 5%	carbon film
R8	15k	Capacitors	
All 0.25W, 5%		C1	1n5 polyester
Detentiomet		C2, C3	10n polyester (2 c
VR1	10k rotary carbon lin.	C4	$10\mu$ elect. 16V
VR2	1k min. carbon preset	Semiconduc	tors
VR3	100k min. carbon preset	IC1	4093 quad Schmi
Capacitors		100	trigger NAND G
C1	100n polyester	IC2	LTC1062 5th orde switched capac
C2	1n5 polyester		low pass filter
Semicondu	ctors		
D1	1N4148 signal diode	Lab 8.5 F	hotoelectric Smo Detector
IC1	ICL8038 waveform	Resistors	Delector
IC2	generator (see text) LF351 bifet op.amp		1k5 (3 off)
102	El do l'bliet optamp	R2	1k
	2 Low Pass Filters	R3 R4	12k 470k
Resistors		R6	8k2
R1, R2, R5, R6	1k5 (4 off)	R7	470k
R3, R7	15k (2 off)	All 0·25W 5%	carbon film.
R4, R8	18k (2 off)	Capacitors	
Capacitors		C1	1µ elect. 16V
C1 to C4	100n polyester (4 off)	C2, C4	100n polyester
Comisondu		C3	4μ7 elect. 16V
IC1, IC2	LF351 bifet op.amp (or	Semiconduo	tors
,	LF353 dual bifet	D1/TR1	QPE1113 IR
	op.amp)	TDO	emitter-detector
		TR2 IC1	BC184 <i>npn</i> transi 4093 guad Schmi
		.01	trigger NAND G
		IC2	4538 monostable
N.B. Some	components are repeated	Miscellaneo	201
	etween Lab Works.	X1	Piezoelectric buzz
		Stripboard	sections (see text)

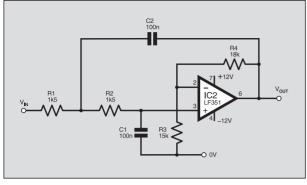


Fig.8.13. Second order low-pass filter.

### Right: Breadboard assembly for the circuit in Fig.8.13, also showing the sinewave generator assembly.

peak-to-peak output voltage from IC1 should decrease as the signal frequency increases. You can plot a graph of output voltage against frequency by setting the input voltage to 1V peak-to-peak and measuring the output voltage at different frequencies.

The most common form of graph shows decibels (dB) plotted against frequency as shown earlier in Fig.8.7. The value in dB can be calculated from:

value in dB = 
$$20\log_{10}(V_{out}/V_{in})$$

where  $\log_{10}$  is the log function on a calculator (not the *ln* function).

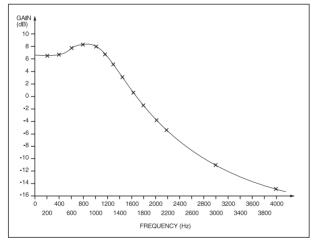


Fig.8.14.. Frequency response for the circuit in Fig.8.13, plotted from actual measurements.

The value of 20 is present because we are using signal *voltages*; it should be changed to 10 if signal *power* is measured.

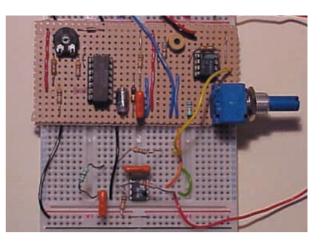
For example, if  $V_{out} = 2V$  and  $V_{in} = 1V$ then the output is 6dB; if  $V_{out} = 10V$  and  $V_{in} = 2V$ , the output is 14dB. Conversely, if the output is smaller than the input, then the value is -dB, e.g.  $V_{out} = 2V$ ,  $V_{in} = 5V$ , the decibel value is -8dB.

Plot the output in dB for frequencies between 10Hz and 2kHz. As the frequency increases, you should see the output remaining constant until 1kHz or so when it will start decreasing. The shape of the curve shows that the filter is a low-pass type. As discussed in the tutorial, the rate at

which the output drops is called the **roll-off** and can be calculated as 6ndB per octave (or 20ndB per decade) where *n* is the order of the filter.

In our case, n = 2, so the roll-off should be 12dB per octave, or 40dB per decade. Fig.8.14 shows the frequency response graph for the circuit.

Build the circuit in Fig.8.15 and repeat the measurements. Now you should see that the roll-off has increased to 24dB per octave (80dB per decade) because the filter is now 4th order.



#### Lab.8.3 High-pass Filters

It is a simple matter to change the circuits in Figs.8.13 and 8.15 to high-pass by swapping R (R1, R2) for C (C1, C2). Try this using the circuit in Fig.8.13 and plot the graph. You should now see that the filter is high-pass.

Changing the cut-off frequency requires recalculating R and C as we described in the tutorial section. Note that the resistor and capacitor values must be close to the calculated values otherwise the filter response will not be accurate.

You can build a band-pass filter by cascading high- and low-pass filters. For example, say we wanted to create a 300Hz to 3kHz band-pass filter, we design a 300Hz high-pass filter and connect its output to the input of a 3kHz lowpass filter.

#### Lab 8.4 Integrated Circuit Filter

The device we are using in this Lab is a 5th order low-pass filter based on a switched mode capacitor device whose cutoff frequency is controlled by an oscillator clock, and is 100th of the clock frequency. So, to use it we only need to provide a clock 100 times that of the cut-off frequency, ideal for circuits where the cut-off frequency needs to be changed.

These switched mode devices are often used for anti-aliasing filters and in microcontroller applications where the microcontroller directly produces a clock from one of its timers.

The complete circuit diagram for a clocked filter is shown in Fig.8.16. It is less complex than even the 2nd order

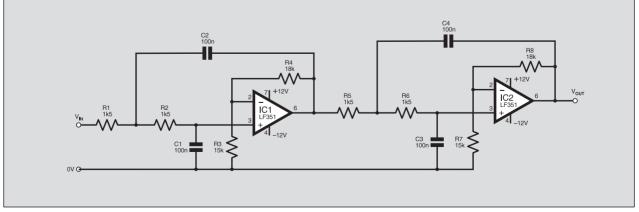


Fig.8.15. Fourth order low-pass filter circuit.

filter! The LTC1062 filter device (IC2) can operate on single or dual power supplies but only to  $\pm 8V$  so we are operating the circuit at  $\pm 5V$  only. This means that the "ground" pin (pin 2) has to be biassed at half of the supply, created by resistors R4 and R5.

The clock signal is generated by Schmitt NAND gate IC1a/IC1b and is set to approximately 50kHz to give a cut-off frequency of 500Hz. There are two outputs, from pin 8, which is buffered, or from pin 7 via C3, to give an accurate d.c. output.

You can plot the frequency response which will now be steeper than the others (30dB/octave). Note that the input signal should be less than 2V peak-to-peak. Changing the value of resistor R2 allows the cut-off frequency to be changed.

We will be returning to filters and this filter in particular next month, when we examine aliasing and analogue-to-digital conversion.

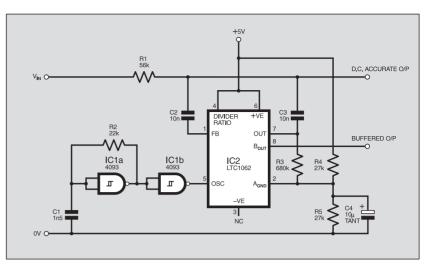
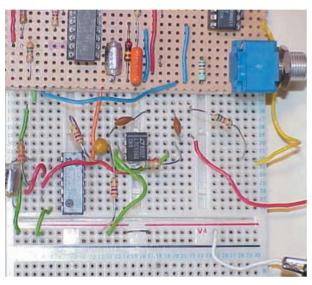
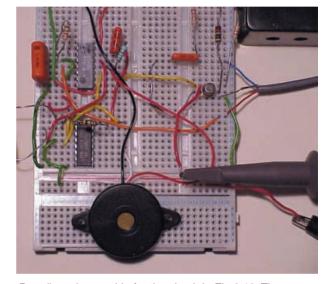


Fig.8.16. Switched capacitor filter circuit.



Breadboard assembly for the circuit in Fig.8.16.



Breadboard assembly for the circuit in Fig.8.18. The sensor is mounted on a separate stripboard and housed in the box at top right.

The filter is maximally flat (Butterworth response) if the RC components are calculated as follows:

$$\frac{1}{2\pi R1C} = \frac{fc}{1.84}$$
$$C3 = C2$$

and  $R3 \cong 12R1$ 

#### Lab 8.5 Photoelectric Smoke Detector

In this Lab we build a photoelectric smoke detector using the light scattering method. Fig.8.17 shows the circuit diagram which consists of a 1kHz oscillator based around Schmitt NAND gate IC1a. Its output is buffered IC1b which drives IR l.e.d. D1. The light scattered from any smoke particles is detected by phototransistor TR1 and amplified by TR2 to give a digital output. i.e. either fully high or fully low.

The IR emitter and phototransistor are usually purchased as a pair, in our case a QPE1113. Others can be used (e.g. CH10L and CH11M) but the values of R3 to R5 may need to be adjusted if the phototransistor's gain is different.

The output of TR2 is fed into NAND gate IC1c, with the other input taken from

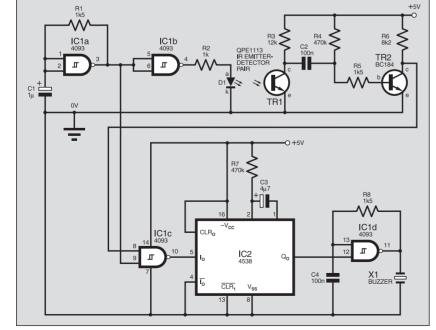
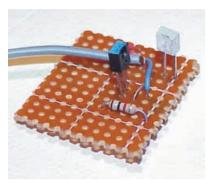


Fig.8.17. Circuit diagram for the smoke detector.

the output of IC1a. Its output feeds into monostable IC2. Normally (no light), no pulses will appear at the output of IC1c. When sufficient light is scattered, the pulses on the collector of TR2 (see Fig.8.18) become large enough for gate IC1c to produce pulses, causing the monostable to trigger. The monostable is continually retriggered until the output of TR2 drops again.

The output of the monostable goes high when it is triggered and turns on the oscillator



Stripboard assembly for the smoke detector sensor pair.

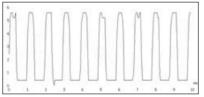


Fig.8.18. Output at TR2 when smoke detected

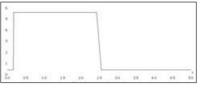


Fig.8.19. Output of monostable when triggered.

based around IC1d, which produces an audible output from piezo buzzer X1. The monostable's period is set to a few seconds to give a reasonably long audio output (see Fig.8.19). The period can be lengthened by increasing the value of C3.

When building the circuit, mount the phototransistor and l.e.d. at right angles to each other and about one centimetre apart. As discussed in the theory section, you should place the assembly inside a container to reduce the ambient light level as much as possible. The accompanying pho-tographs show a typical configuration.

When setting up the circuit, you should monitor the output of gate IC1c using the Picoscope and twist the l.e.d. until pulses just stop - any object scattering light towards the phototransistor will cause the light level to increase.

Finally, test the circuit using smoke and re-adjust the l.e.d. until it gives reliable detection.

Note that this circuit is for illustrative purposes only and should not be used for actual smoke detection as a substitute for a commercial device.

#### NEXT MONTH

In Part 9 next month, we continue looking at filters and then discuss data sampling and digital converters.

If you have any queries directly related to this series, you can write to the authors c/o the Editorial address, or you can email them at teach-in@epemag.demon.co.uk (no file attachments or general electronic queries please).

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