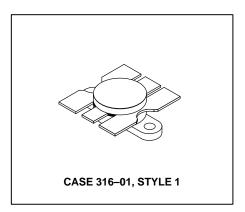
# The RF Line NPN Silicon RF Power Transistor

The MRF247 is designed for 12.5 Volt VHF large—signal amplifier applications in industrial and commercial FM equipment operating to 175 MHz.

- Specified 12.5 Volt, 175 MHz Characteristics —
   Output Power = 75 Watts
   Power Gain = 7.0 dB Min
   Efficiency = 55% Min
- Characterized With Series Equivalent Large-Signal Impedance Parameters
- Internal Matching Network Optimized for Minimum Gain Frequency Slope Response Over the Range 136 to 175 MHz
- Load Mismatch Capability at Rated Pout and Supply Voltage

## **MRF247**

75 W, 175 MHz CONTROLLED Q RF POWER TRANSISTOR NPN SILICON



#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	VCEO	18	Vdc	
Collector–Base Voltage	VCBO	36	Vdc	
Emitter–Base Voltage	VEBO	4.0	Vdc	
Collector Current — Peak	IC	20	Adc	
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) Derate above 25°C	PD	250 1.43	Watts W/°C	
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C	

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)	$R_{\theta JC}$	0.7	°C/W

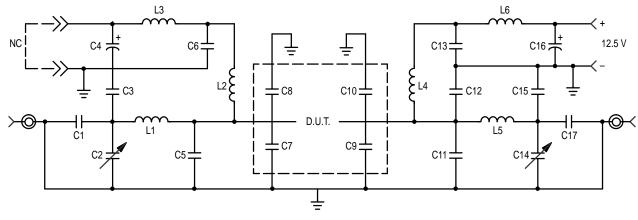
#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	V(BR)CEO	18	_	_	Vdc
Collector–Emitter Breakdown Voltage (IC = 50 mAdc, V <sub>BE</sub> = 0)	V(BR)CES	36	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 mAdc, I <sub>C</sub> = 0)	V(BR)EBO	4.0	_	_	Vdc

- (1) This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.
- (2) Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.



Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS	•				
DC Current Gain (I <sub>C</sub> = 5.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	hFE	10	75	150	_
DYNAMIC CHARACTERISTICS	•				
Output Capacitance (V <sub>CB</sub> = 15 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	235	300	pF
FUNCTIONAL TESTS	•			•	
Common–Emitter Amplifier Power Gain (V <sub>CC</sub> = 12.5 Vdc, P <sub>out</sub> = 75 Watts, f = 175 MHz)	GPE	7.0	8.5	_	dB
Collector Efficiency (V <sub>CC</sub> = 12.5 Vdc, P <sub>out</sub> = 75 Watts, f = 175 MHz)	η	55	60	_	%
Load Mismatch (V <sub>CC</sub> = 12.5 Vdc, P <sub>out</sub> = 75 Watts, f = 175 MHz, VSWR = 30:1 All Phase Angles)	Ψ	No Degradation in Output Power			



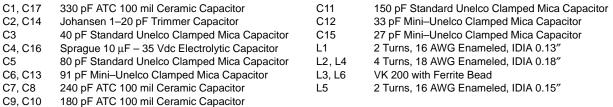


Figure 1. Output Power versus Input Power

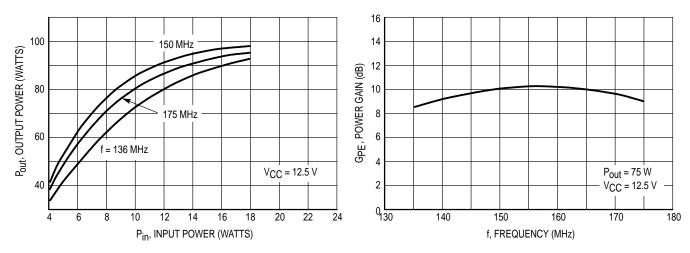
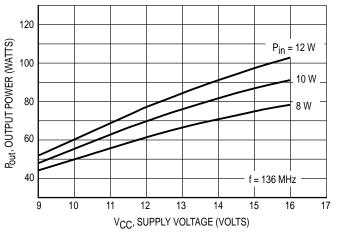


Figure 2. Output Power versus Input Power

Figure 3. Power Gain versus Frequency



120 P<sub>in</sub> = 12 W
100 80 8 W
80 60 40 9 10 11 12 13 14 15 16 17
V<sub>CC</sub>, SUPPLY VOLTAGE (VOLTS)

Figure 4. Output Power versus Supply Voltage

Figure 5. Output Power versus Supply Voltage

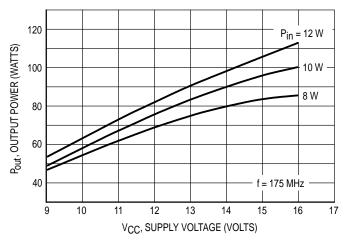


Figure 6. Output Power versus Supply Voltage

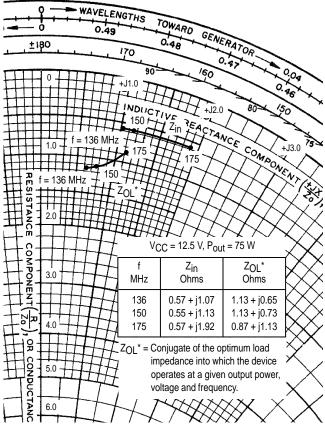
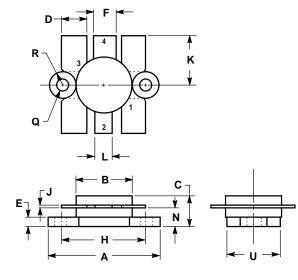


Figure 7. Series Equivalent Impedances

#### PACKAGE DIMENSIONS



1. FLANGE IS ISOLATED IN ALL STYLES.

	INC	INCHES		IETERS
DIM	MIN	MAX	MAX MIN	
Α	24.38	25.14	0.960	0.990
В	12.45	12.95	0.490	0.510
С	5.97	7.62	0.235	0.300
D	5.33	5.58	0.210	0.220
Е	2.16	3.04	0.085	0.120
F	5.08	5.33	0.200	0.210
Н	18.29	18.54	0.720	0.730
J	0.10	0.15	0.004	0.006
K	10.29	11.17	0.405	0.440
L	3.81	4.06	0.150	0.160
N	3.81	4.31	0.150	0.170
Q	2.92	3.30	0.115	0.130
R	3.05	3.30	0.120	0.130
U	11.94	12.57	0.470	0.495

STYLE 1: PIN 1. EMITTER 2. COLLECTOR 3. EMITTER

4. BASE

**CASE 316-01 ISSUE D** 

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